imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

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Product Summary

V _{(BR)DS} S	R _{DS(ON)} max	Package	I _{D max} T _A = +25℃
201/	$72m\Omega @ V_{GS} = -10V$	SOT-23	-3.9A
-30V	$85m\Omega @ V_{GS} = -4.5V$	501-23	-3.6A

Description and Applications

This new generation MOSFET is designed to minimize the on-state resistance (R_{DS(on)}) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

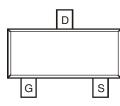
Features

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

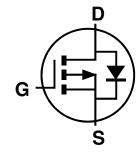
Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Lead-Free Plating (Matte Tin Finish Annealed over Alloy 42 Leadframe).
- Terminals: Solderable per MIL-STD-202, Method 208 🕲
- Terminal Connections: See Diagram
- Weight: 0.006 grams (Approximate)





SOT23



Equivalent Circuit

Top View

Top View Pin Configuration

Ordering Information (Note 4)

Part Number	Case	Packaging
DMP3068L-7	SOT23	3,000/Tape & Reel
DMP3068L-13	SOT23	10,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

 See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http"//www.diodes.com/products/packages.html.

Marking Information

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			so	DT23								
Date Code Key			6	8 ¥	YM = Y or	Date Cod $\overline{7} = Year ($	duct Type Marking Code te Code Marking Year (ex: B = 2014) Month (ex: 9 = September)					
Year	2014	1	2015		2016	20	17	2018		2019		2020
Code	В		С		D			F		G		Н
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	a.	0	N	р



Maximum Ratings (@T_A = +25 °C unless otherwise specified.)

Characteris	stic		Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	-30	V
Gate-Source Voltage			V _{GSS}	±12	V
Drain Current (Nate C) Vac. 10V	Steady State	T _A = +25 ℃ T _A = +70 ℃	I _D	-3.3 -2.6	А
Drain Current (Note 6) Vgs= -10V	t<10s	T _A = +25 ℃ T _A = +70 ℃	I _D	-3.9 -3.2	А
Pulsed Drain Current (Pulse width ≤10µS, D	uty Cycle ≤1%)		I _{DM}	-18	А

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	0.7	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State t<10s	$R_{ extsf{ heta}JA}$	182 133	°C/W
Total Power Dissipation (Note 6)		PD	1.2	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State t<10s	R _{0JA}	103 75	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	C

Electrical Characteristics (@T_A = +25 °C unless otherwise specified.)

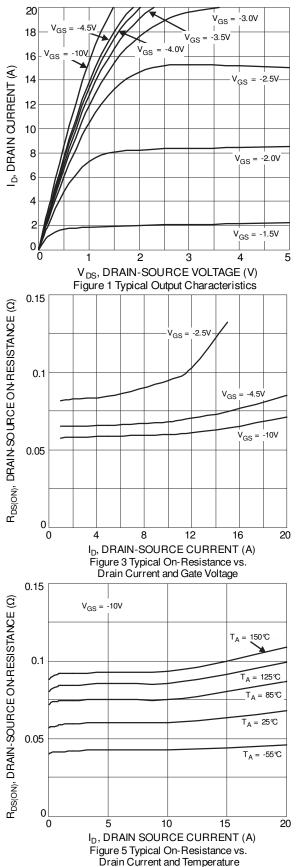
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	-30			V	$V_{GS} = 0V, I_D = -250 \mu A$	
Zero Gate Voltage Drain Current	IDSS			-1	μA	$V_{DS} = -30V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}			±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)	-						
Gate Threshold Voltage	V _{GS(th)}	-0.5		-1.3	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
Static Drain-Source On-Resistance	R _{DS (ON)}	_	57 64 80 107	72 85 120 165	mΩ	$V_{GS} = -10V, I_D = -4.2A \\ V_{GS} = -4.5V, I_D = -4.0A \\ V_{GS} = -2.5V, I_D = -2.0A \\ V_{GS} = -1.8V, I_D = -1.0A$	
Diode Forward Voltage	V _{SD}			-1.2	V	$V_{GS} = 0V, I_{S} = -1.0A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}	—	708	—	pF		
Output Capacitance	Coss		57	_	pF	$V_{DS} = -15V, V_{GS} = 0V, f = 1.0MHz$	
Reverse Transfer Capacitance	Crss	_	47	_	pF		
Gate Resistance	RG	—	14	—	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V _{GS} = -4.5V)	Q _G		7.3	_	nC	$V_{DS} = -15V, I_D = -4A$	
Total Gate Charge (V _{GS} = -10V)	Q _G		15.9	_			
Gate-Source Charge	Q _{GS}		1.2	—	nC	$V_{DS} = -15V, I_{D} = -4A$	
Gate-Drain Charge	Q _{GD}		1.7	—			
Turn-On Delay Time	t _{d(on)}		3.5	_			
Rise Time	tr		15.8	_		V _{DS} = -15V, V _{GS} = -10V,	
Turn-Off Delay Time	t _{d(off)}	—	70.3	—	ns	$I_D = -4A, R_G = 6.0\Omega$	
Fall Time	t _f		33.9				

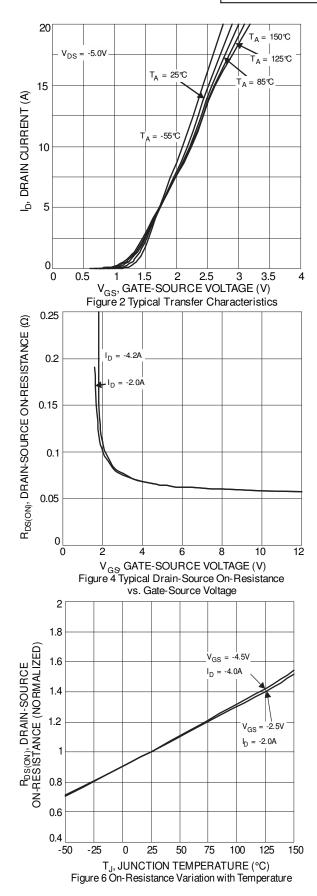
Notes:

Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
 Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1in. square copper plate.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to production testing.

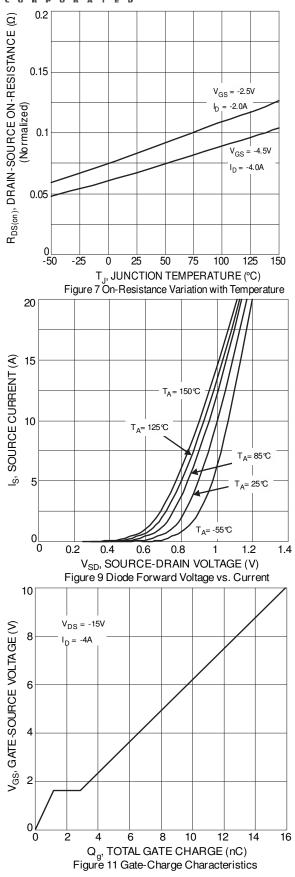


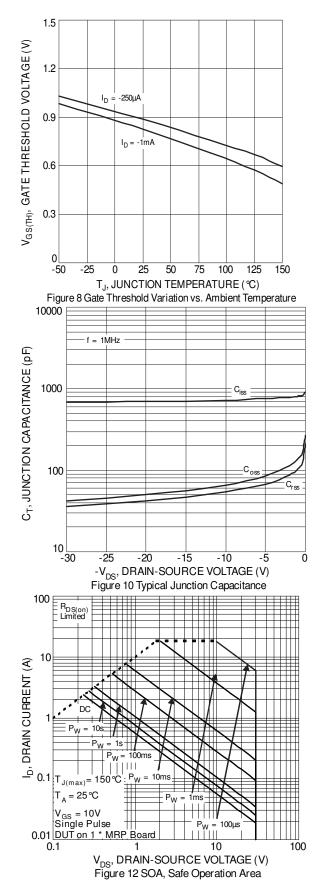














Тур

0.40

1.30

2.40

0.915

0.535

1.83

2.90

0.05

0.975

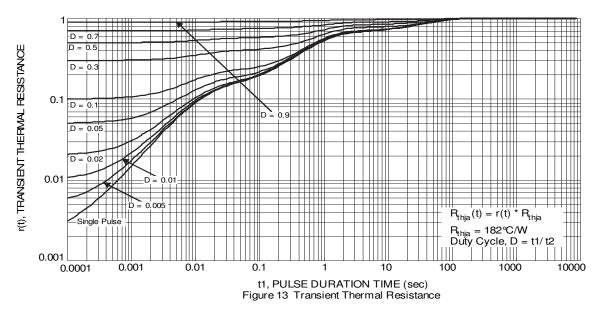
1.025

0.55

0.40

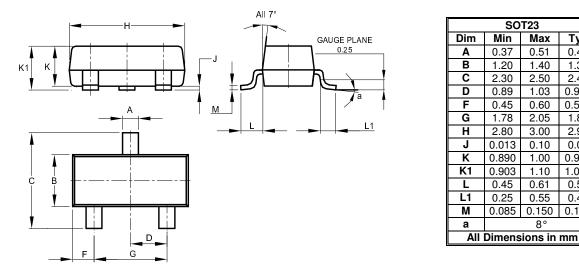
0.110

8°



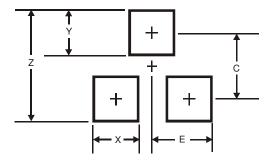
Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest the version.



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
E	1.35



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