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Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China











40V +175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI[®]

Product Summary

BV _{DSS}	R _{DS(ON)} Max	Q _g Typ	I _D T _C = +25°C (Note 9)
40V	2.7mΩ @ V _{GS} = 10V	68.6nC	100A

Description

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}), yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

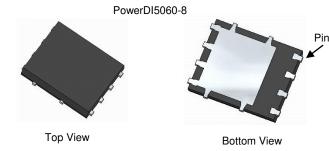
- Engine Management Systems
- **Body Control Electronics**
- **DC-DC Converters**

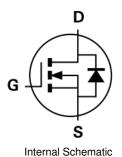
Features

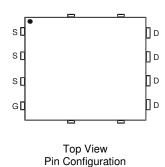
- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching Ensures More Reliable and Robust End Application
- Low R_{DS(ON)} Minimizes Power Losses
- Low Q_a Minimizes Switching Losses
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: PowerDI[®]5060-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.097 grams (Approximate)







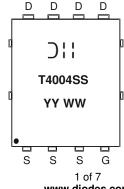
Ordering Information (Note 4)

Part Number	Case	Packaging
DMTH4004SPS-13	PowerDI5060-8	2,500/Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



);; = Manufacturer's Marking T4004SS = Product Type Marking Code YYWW = Date Code Marking YY = Year (ex: 14 = 2014) WW = Week (01 - 53)

PowerDI is a registered trademark of Diodes Incorporated.



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Drain-Source Voltage	V_{DSS}	40	V	
Gate-Source Voltage	V_{GSS}	±20	V	
Continuous Drain Current (Note 5)	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	31 26	А
Continuous Drain Current (Note 6)	$T_{C} = +25^{\circ}C$ (Note 9) $T_{C} = +100^{\circ}C$	I _D	100 100	А
Maximum Continuous Body Diode Forward Current (Note 5)	Is	100	Α	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	200	Α	
Avalanche Current, L=0.2mH	I _{AS}	45	Α	
Avalanche Energy, L=0.2mH	Eas	200	mJ	

Thermal Characteristics

Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 5) $T_A = +25^{\circ}C$		P_{D}	3.6	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	41	°C/W	
Total Power Dissipation (Note 6) $T_C = +25^{\circ}C$		P_{D}	167	W
Thermal Resistance, Junction to Case (Note 6)	R _{eJC}	0.9	°C/W	
Operating and Storage Temperature Range	T_{J} , T_{STG}	-55 to +175	°C	

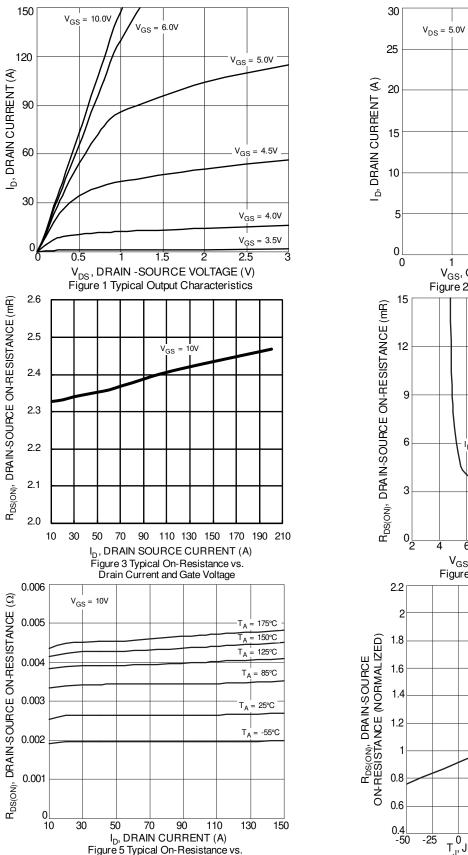
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

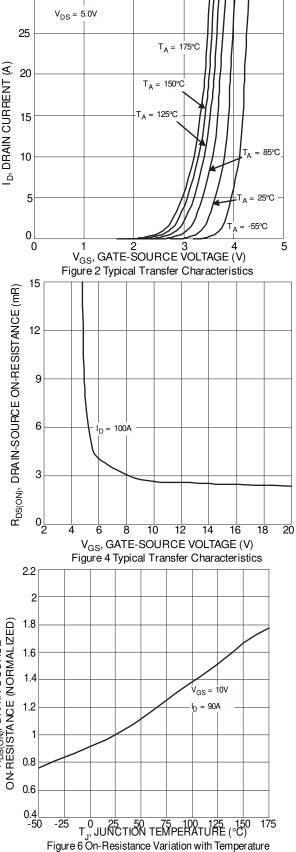
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	40	_	_	V	$V_{GS} = 0V$, $I_D = 1mA$	
Zero Gate Voltage Drain Current	I _{DSS}	-	_	1	μΑ	$V_{DS} = 32V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(th)}	2	_	4	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	2.3	2.7	mΩ	$V_{GS} = 10V, I_D = 90A$	
Diode Forward Voltage	V_{SD}	-	0.9	1.2	V	$V_{GS} = 0V, I_{S} = 20A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}	_	4,305	_		V_{DS} = 25V, V_{GS} = 0V, f = 1MHz	
Output Capacitance	Coss	_	1,441	_	pF		
Reverse Transfer Capacitance	C _{rss}	_	102	_			
Gate Resistance	R_{g}	_	0.77	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Qg	_	68.6	_		$V_{DD} = 20V, I_D = 90A,$ $V_{GS} = 10V$	
Gate-Source Charge	Q _{gs}	_	16.8	_	nC		
Gate-Drain Charge	Q _{gd}	_	14.2	_			
Turn-On Delay Time	t _{D(ON)}	_	9.5	_			
Turn-On Rise Time	t _R		6.7	_	ns	$V_{DD} = 20V, V_{GS} = 10V,$ $I_D = 90A, R_G = 3.5\Omega$	
Turn-Off Delay Time	t _{D(OFF)}	_	26.4	_	115		
Turn-Off Fall Time	t _F		8.1	_			
Body Diode Reverse Recovery Time	t _{RR}	_	52.4	_	ns L 504 II/II 1004/		
Body Diode Reverse Recovery Charge	Q _{RR}	_	78.2	_	nC	$I_F = 50A$, di/dt = 100A/ μ s	

 Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
Thermal resistance from junction to soldering point (on the exposed drain pad).
Short duration pulse test used to minimize self-heating effect.
Guarantee by design. Not subject to production testing. Notes:

- 9. Package limited.

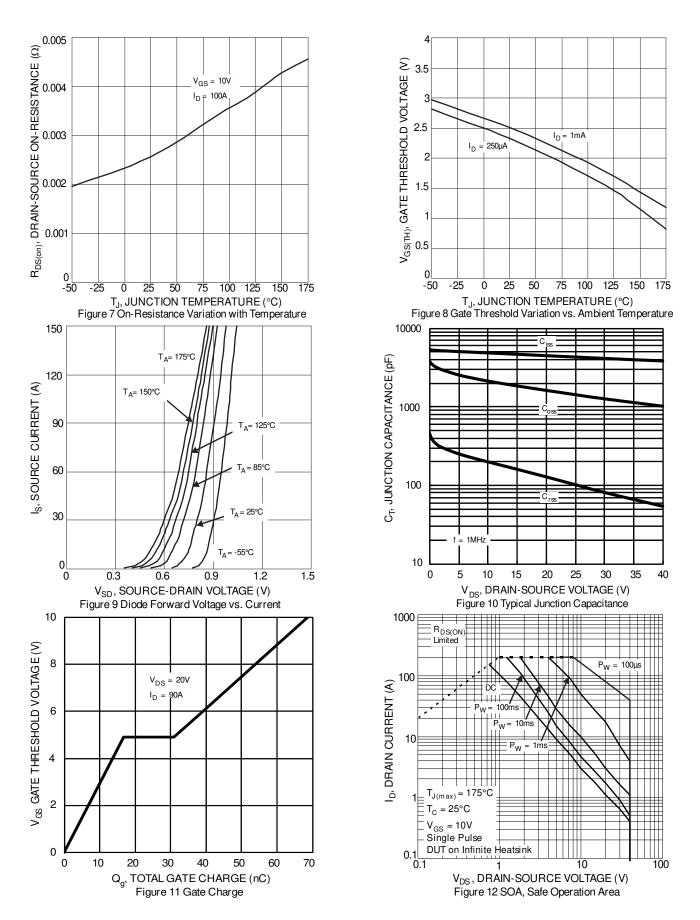




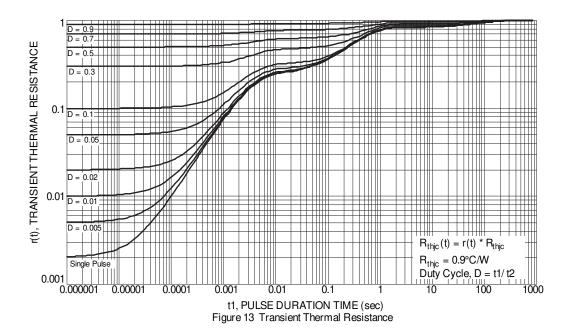


Drain Current and Temperature





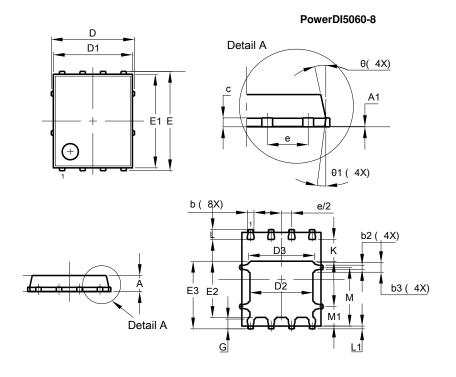






Package Outline

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

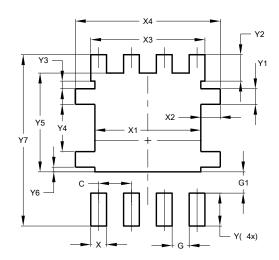


PowerDI5060-8						
			Тур			
Α	0.90	1.10	1.00			
A 1	0.00	0.05	_			
b	0.33	0.51	0.41			
b2	0.200	0.350	0.273			
b3	0.40	0.80	0.60			
C D	0.230	0.330	0.277			
		5.15 BSC	;			
D1	4.70	5.10	4.90			
D2	3.70	4.10	3.90			
D3	3.90	4.30	4.10			
Е	6.15 BSC					
E1	5.60	6.00	5.80			
E2	3.28	3.68	3.48			
E3	3.99	4.39	4.19			
е	-	1.27 BSC	;			
G	0.51	0.71	0.61			
K	0.51	-	_			
L L1	0.51	0.71	0.61			
L1	0.100	0.200	0.175			
М	3.235	4.035	3.635			
M1	1.00	1.40	1.21			
θ	10°	12°	11°			
θ1	6°	8°	7°			
All Dimensions in mm						

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

PowerDI5060-8



Dimensions	Value (in mm)				
С	1.270				
G	0.660				
G1	0.820				
X	0.610				
X1	4.100				
X2	0.755				
Х3	4.420				
X4	5.610				
Υ	1.270				
Y1	0.600				
Y2	1.020				
Y3	0.295				
Y4	1.825				
Y5	3.810				
Y6	0.180				
Y7	6.610				



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