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60V 175°C N-CHANNEL ENHANCEMENT MODE MOSFET POWERDI®

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D T _C = +25°C (Note 8)
601/	3.1mΩ @ V _{GS} = 10V	100A
60V	4.5mΩ @ V _{GS} = 4.5V	100A

Description and Applications

This MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$), yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

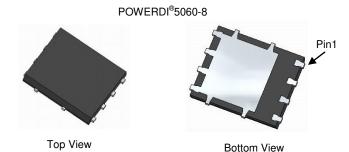
- Primary Switch in Isolated DC-DC
- Synchronous Rectifier
- Load Switch

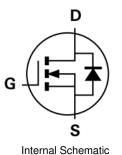
Features

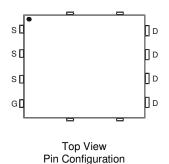
- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching Ensures More Reliable and Robust End Application
- Low R_{DS(ON)} Minimizes Power Losses
- Low Q_a Minimizes Switching Losses
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- An Automotive-Compliant Part is Available Under Separate Datasheet (<u>DMTH6004LPSQ</u>)

Mechanical Data

- Case: POWERDI[®]5060-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe;
 Solderable per MIL-STD-202, Method 208 (2)
- Weight: 0.097 grams (Approximate)







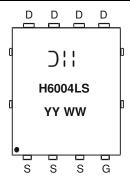
Ordering Information (Note 4)

I		
Part Number	Case	Packaging
DMTH6004LPS-13	POWERDI®5060-8	2,500 / Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



);; = Manufacturer's Marking H6004LS = Product Type Marking Code YYWW = Date Code Marking YY = Year (ex: 14 = 2014) WW = Week (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage		V_{DSS}	60	V
Gate-Source Voltage		V _{GSS}	±20	V
Continuous Drain Current (Note 5)	$T_A = +25$ °C $T_A = +70$ °C	I _D	22 16	А
Continuous Drain Current (Note 6)	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$ (Note 8)	Ι _D	100 100	А
Maximum Continuous Body Diode Forward Current (Note 6)	Is	100	Α	
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)		I _{DM}	120	Α
Avalanche Current, L = 0.2mH		I _{AS}	40	Α
Avalanche Energy, L = 0.2mH		Eas	160	mJ

Thermal Characteristic

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	P_{D}	2.6	W
Thermal Resistance, Junction to Ambient (Note 5)		$R_{\theta JA}$	47	°C/W
Total Power Dissipation (Note 6) $T_C = +25^{\circ}C$		P_{D}	138	W
Thermal Resistance, Junction to Case (Note 6)		R _{eJC}	0.9	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +175	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

O			-			7 .0	
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)	1		1	1		<u> </u>	
Drain-Source Breakdown Voltage	BV _{DSS}	60	-	-	V	$V_{GS} = 0V$, $I_D = 1mA$	
Zero Gate Voltage Drain Current	I _{DSS}	-	-	1	μΑ	$V_{DS} = 48V, V_{GS} = 0V$	
Gate-Source Leakage	IGSS	-	-	±100	nA	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	1	-	3	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance	D	-	2.5	3.1	mΩ	$V_{GS} = 10V, I_D = 25A$	
Static Diani-Source On-nesistance	R _{DS(ON)}	-	3.3	4.5	mΩ	$V_{GS} = 4.5V, I_D = 20A$	
Diode Forward Voltage	V_{SD}	-	-	1.3	V	$V_{GS} = 0V, I_{S} = 25A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}	1	4515	-			
Output Capacitance	Coss	1	1477	-	pF	$V_{DS} = 30V, V_{GS} = 0V,$ f = 1MHz	
Reverse Transfer Capacitance	Crss	-	135.3	-		I = IIVITZ	
Gate Resistance	Rg	1	0.64	-	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 10V)	Q_g	1	96.3	-			
Total Gate Charge (V _{GS} = 4.5V)	Qg	-	47.4	-	nC	V _{DD} = 30V. I _D = 25A	
Gate-Source Charge	Q_{gs}	-	14.1	-	IIC	V _{DD} = 30V, I _D = 23A	
Gate-Drain Charge	Q_{gd}	-	21.4	-			
Turn-On Delay Time	t _{D(ON)}	-	9.9	-			
Turn-On Rise Time	t _R	-	17.7	-		$V_{DD} = 30V, V_{GS} = 10V,$ $I_{D} = 25A, R_{G} = 3.5\Omega$	
Turn-Off Delay Time	t _{D(OFF)}	-	53.5	-	ns		
Turn-Off Fall Time	t _F	-	32.9	-			
Body Diode Reverse Recovery Time	t _{RR}	-	49.7	-	ns	I- 25A di/dt 100A/ug	
Body Diode Reverse Recovery Charge	Q _{RR}	-	78.9	-	nC	$I_F = 25A$, di/dt = 100A/ μ s	

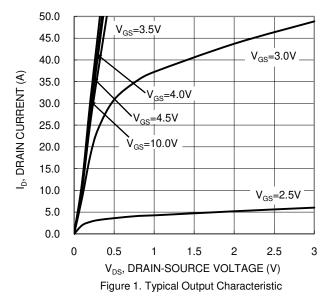
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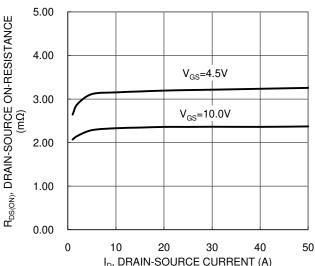
Notes:

- Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
 Thermal resistance from junction to soldering point (on the exposed drain pad).
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to production testing.



DMTH6004LPS





 $\rm I_D,\,DRAIN\text{-}SOURCE\,CURRENT\,(A)$ Figure 3. Typical On-Resistance $\,$ vs. Drain Current and Gate Voltage

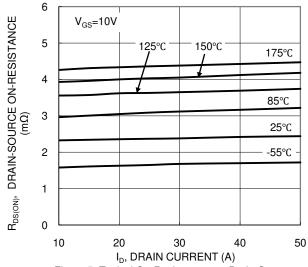
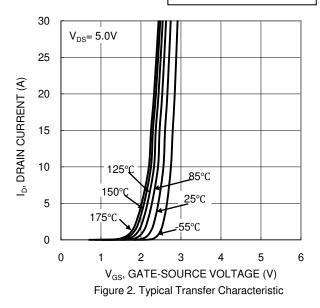
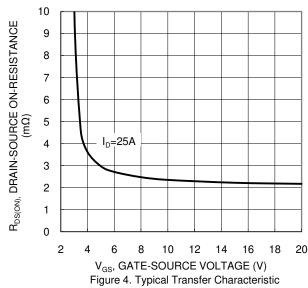


Figure 5. Typical On-Resistance vs. Drain Current and Temperature





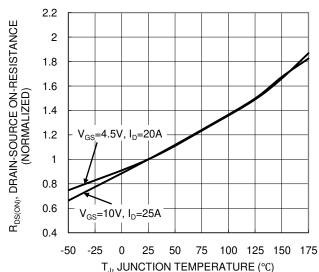
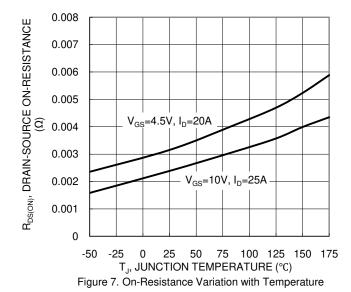
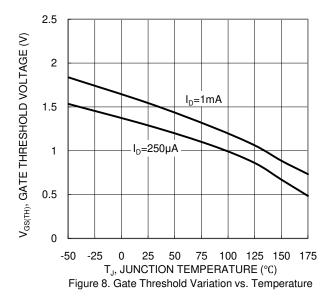


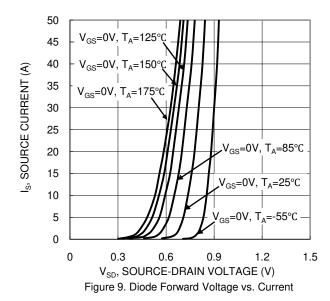
Figure 6. On-Resistance Variation with Temperature

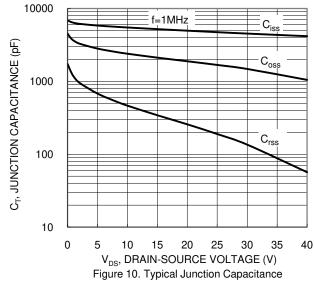


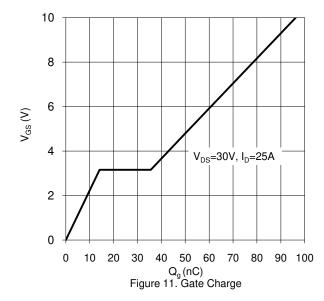


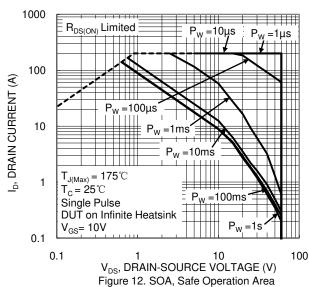














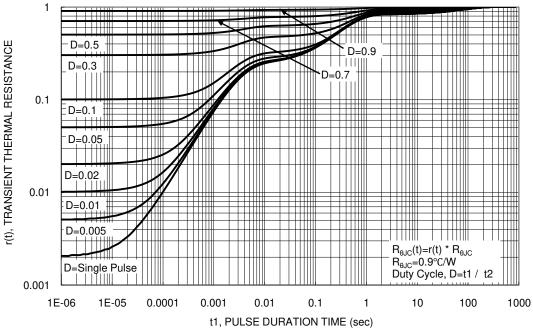
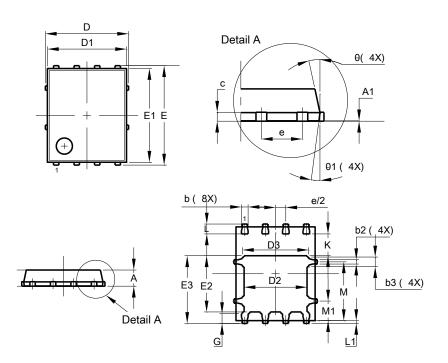


Figure 13. Transient Thermal Resistance



Package Outline Dimensions

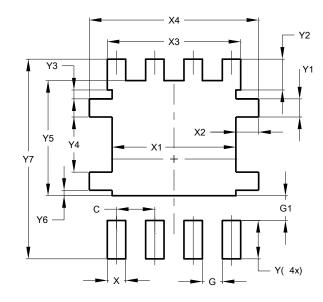
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



POWERDI®5060-8					
Dim	Min Max		Тур		
Α	0.90	1.10	1.00		
A1	0.00	0.05	-		
b	0.33	0.51	0.41		
b2	0.200	0.350	0.273		
b3	0.40	0.80	0.60		
С	0.230	0.330	0.277		
D		5.15 BSC	;		
D1	4.70	5.10	4.90		
D2	3.70	4.10	3.90		
D3	3.90	4.30	4.10		
Е	6.15 BSC				
E1	5.60	6.00	5.80		
E2	3.28	3.68	3.48		
E3	3.99	4.39	4.19		
е	1.27 BSC				
G	0.51	0.71	0.61		
K	0.51	-	-		
L	0.51	0.71	0.61		
L1	0.100	0.200	0.175		
М	3.235	4.035	3.635		
M1	1.00	1.40	1.21		
θ	10⁰	12⁰	11º		
θ1	6º	8º	7º		
All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)			
С	1.270			
G	0.660			
G1	0.820			
X	0.610			
X1	4.100			
X2	0.755			
Х3	4.420			
X4	5.610			
Υ	1.270			
Y1	0.600			
Y2	1.020			
Y3	0.295			
Y4	1.825			
Y5	3.810			
Y6	0.180			
Y 7	6.610			



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