imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

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Transistors with Built-in Resistor DRA3124T0L

DRA3124T0L Silicon PNP epitaxial planar type

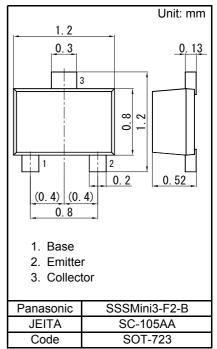
For digital circuits Complementary to DRC3124T DRA9124T in SSSMini3 type package

Features

- · High forward current transfer ratio hFE with excellent linearity
- · Low collector-emitter saturation voltage Vce(sat)
- Halogen-free / RoHS compliant
 (EU RoHS / UL-94 V-0 / MSL:Level 1 compliant)
- Marking Symbol: LH

Packaging

Embossed type (Thermo-compression sealing) : 10 000 pcs / reel (standard)



Internal Connection							
οE							
Resistance value	R1	22	kΩ				

■ Absolute Maximum Ratings Ta = 25 °C

Parameter	Symbol	Rating	Unit
Collector-base voltage (Emitter open)	VCBO	-50	V
Collector-emitter voltage (Base open)	VCEO	-50	V
Collector current	IC	-100	mA
Total power dissipation	PT	100	mW
Junction temperature	Tj	150	°C
Operating ambient temperature	Topr	-40 to +85	°C
Storage temperature	Tstg	-55 to +150	С°

Electrical Characteristics Ta = $25 \circ C \pm 3 \circ C$

Parameter	Symbol	Conditions Min Typ		Тур	Max	Unit
Collector-base voltage (Emitter open)	VCBO	IC = -10 μA, IE = 0	-50			V
Collector-emitter voltage (Base open)	VCEO	IC = -2 mA, IB = 0	-50			V
Collector-base cutoff current (Emitter open)	ICBO	VCB = -50 V, IE = 0			-0.1	μA
Collector-emitter cutoff current (Base open)	ICEO	VCE = -50 V, IB = 0			-0.5	μA
Emitter-base cutoff current (Collector open)	IEBO	VEB = -6 V, IC = 0			-0.01	mA
Forward current transfer ratio	hFE	VCE = -10 V, IC = -5 mA	160		460	-
Collector-emitter saturation voltage	VCE(sat)	IC = -10 mA, IB = -0.5 mA			-0.25	V
Input voltage	Vi(on)	VCE = -0.2 V, IC = -5 mA	-1.8			V
	Vi(off)	VCE = -5 V, IC = -100 μA			-0.4	V
Input resistance	R1		-30%	22	+30%	kΩ

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 Measuring methods for transistors.

Transistors with Built-in Resistor **DRA3124T0L**

> -600 µA -500 µA

-400 uA

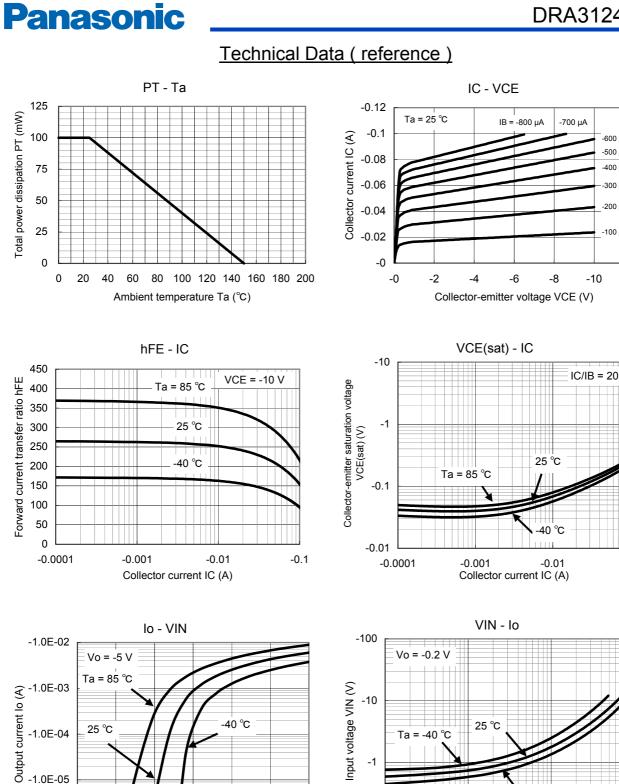
-300 µA

-200 µA

-100 µA

-12

-0.1



-0.1

25 °C

-0.001

85 °C

Output current Io (A)

-0.01

-40 °C

Та

-1

-0.1

-1.5

-0.0001

Established : 2009-10-23 Revised : 2014-02-20

25 °C

-0.5

-1

Input voltage VIN (V)

-1.0E-04

-1.0E-05

-1.0E-06

-0



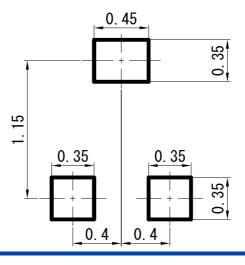
SSSMini3-F2-B

Transistors with Built-in Resistor DRA3124T0L

Unit: mm

1.20 ± 0.05 0.13-0.02 **0. 30**^{+0. 05} 0. 02 3 0.80±0.05 1.20 ± 0.05 20 2 1 **0. 20**+0. 05 -0. 02 0.20 ± 0.05 (0.4) (0.4) 0.80 ± 0.05 (5°) 27) 52 ± 0.03 ġ o' 0 to 0.05

Land Pattern (Reference) (Unit: mm)



Page 3 of 3

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