imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

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DSA7504

Silicon PNP epitaxial planar type

For low frequency amplification

Features

- Low collector-emitter saturation voltage $V_{CE(sat)}$
- Halogen-free / RoHS compliant (EU RoHS / UL-94 V-0 / MSL: Level 1 compliant)

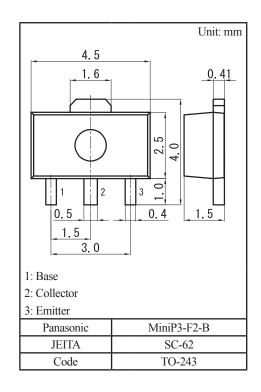
Marking Symbol: 4F

Packaging

DSA7504×0L Embossed type (Thermo-compression sealing): 1 000 pcs / reel (standard)

Absolute Maximum Ratings $T_a = 25^{\circ}C$

Parameter	Symbol	Rating	Unit	
Collector-base voltage (Emitter open)	V _{CBO}	-30	V	
Collector-emitter voltage (Base open)	V _{CEO}	-20	V	
Emitter-base voltage (Collector open)	V _{EBO}	-7	V	
Collector current	I _C	-4	А	
Peak collector current	I _{CP}	-7	А	
Collector power dissipation *1	P _C	1	W	
Junction temperature	Tj	150	°C	
Operating ambient temperature	T _{opr}	-40 to +85	°C	
Storage temperature	T _{stg}	-55 to +150	°C	



Note) *1: Printed circuit board: Copper foil area of 1 cm² or more, and the board thickness of 1.7 mm for the collector portion Absolute maximum rating without heat sink for P_C is 0.5 W

Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_{\rm C} = -10 \ \mu {\rm A}, \ I_{\rm E} = 0$	-30			V
Collector-emitter voltage (Base open)	V _{CEO}	$I_{\rm C} = -1 {\rm mA}, I_{\rm B} = 0$	-20			V
Emitter-base voltage (Collector open)	V _{EBO}	$I_{\rm E} = -10 \ \mu A, I_{\rm C} = 0$	-7			V
Collector-base cutoff current (Emitter open)	I _{CBO}	$V_{CB} = -30 \text{ V}, I_E = 0$			- 0.1	μΑ
Emitter-base cutoff current (Collector open)	I _{EBO}	$V_{\rm EB} = -7$ V, $I_{\rm C} = 0$			- 0.1	
Forward current transfer ratio *1, 2	h _{FE}	$V_{\rm CE} = -2 \text{ V}, I_{\rm C} = -2 \text{ A}$	120		315	
Collector-emitter saturation voltage *1	V _{CE(sat)}	$I_{\rm C} = -3$ A, $I_{\rm B} = -0.1$ A		- 0.7	-1.0	V
Transition frequency	f _T	$V_{CE} = -6 \text{ V}, I_C = -50 \text{ mA}$		180		MHz
Collector output capacitance (Common base, input open circuited)	C _{ob}	$V_{CB} = -20 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		30		pF

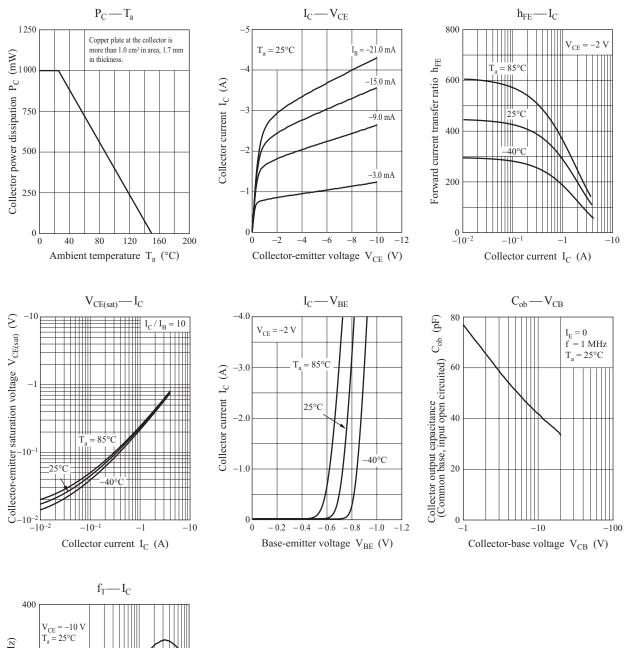
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

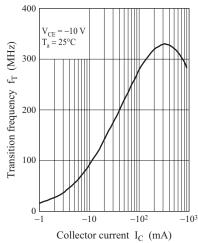
2. *1: Pulse measurement

*2: Rank classification

Code	Q	R	0		
Rank	Q	R	No-rank		
$h_{\rm FE}$	120 to 205	180 to 315	120 to 315		
Marking Symbol	4FQ	4FR	4F		

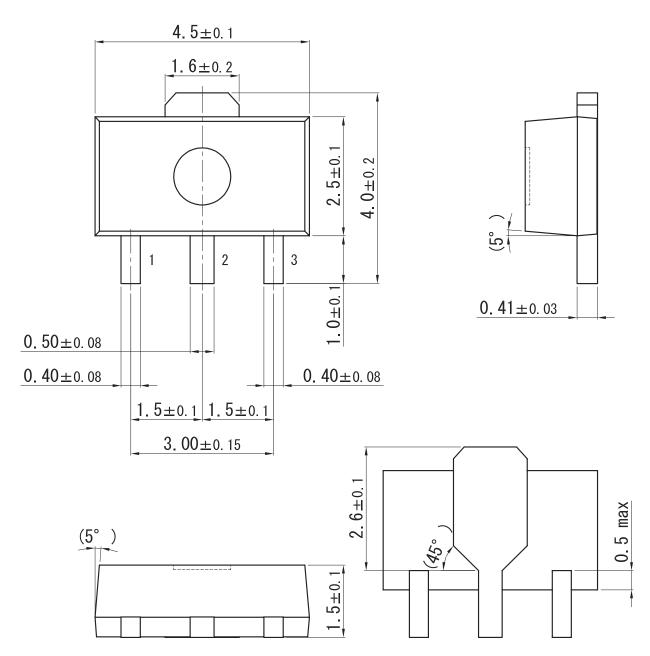
Product of no-rank is not classified and have no marking symbol for rank.



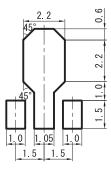


MiniP3-F2-B

Unit: mm



Land Pattern (Reference) (Unit: mm)



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