

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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DSA7506

Silicon PNP epitaxial planar type

For low frequency amplification

■ Features

- \bullet Low collector-emitter saturation voltage $V_{\text{CE(sat)}}$
- Halogen-free / RoHS compliant (EU RoHS / UL-94 V-0 / MSL: Level 1 compliant)

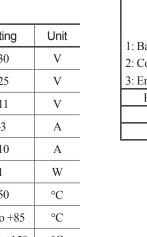
■ Marking Symbol: 4LR

Packaging

DSA7506R0L Embossed type (Thermo-compression sealing): 1 000 pcs / reel (standard)

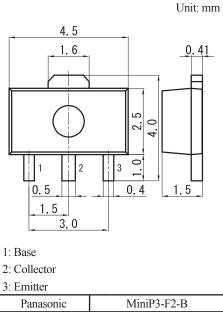
■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit	
Collector-base voltage (Emitter open)	V _{CBO}	V _{CBO} -30		
Collector-emitter voltage (Base open)	V _{CEO}	V		
Emitter-base voltage (Collector open)	V _{EBO}	-11	V	
Collector current	I_{C}	-3	A	
Peak collector current *1	I_{CP}	-10	A	
Collector power dissipation *2	P _C	1	W	
Junction temperature	T_{j}	150	°C	
Operating ambient temperature	T _{opr}	-40 to +85	°C	
Storage temperature	T _{stg}	-55 to +150	°C	



JEITA

Code



SC-62

TO-243

■ Electrical Characteristics $T_a = 25$ °C±3°C

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_C = -10 \mu A, I_E = 0$	-30			V
Collector-emitter voltage (Base open)	V _{CEO}	$I_C = -1 \text{ mA}, I_B = 0$	-25			V
Emitter-base voltage (Collector open)	V_{EBO}	$I_E = -10 \mu\text{A}, I_C = 0$	-11			V
Forward current transfer ratio *1,2	h_{FE}	$V_{CE} = -2 \text{ V}, I_{C} = -1.4 \text{ A}$	130		450	_
Collector-emitter saturation voltage *1	V _{CE(sat)}	$I_{\rm C} = -1.4 \text{A}, I_{\rm B} = -25 \text{mA}$		- 0.2	-0.27	V
Transition frequency	f_T	$V_{CE} = -6 \text{ V}, I_{C} = -50 \text{ mA}$		150		MHz
Collector output capacitance (Common base, input open circuited)	C _{ob}	$V_{CB} = -10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$			85	pF

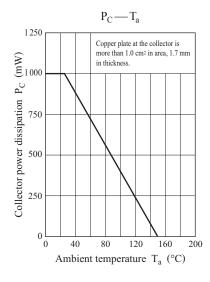
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

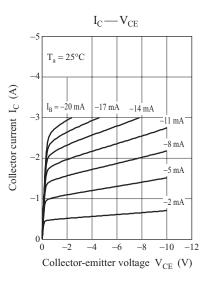
Note) *1: Pulse width ≤ 1ms, Single pulse

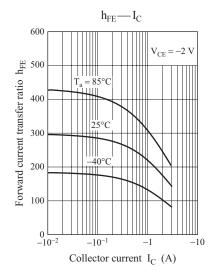
^{*2:} Printed circuit board: Copper foil area of 1 cm² or more, and the board thickness of 1.7 mm for the collector portion

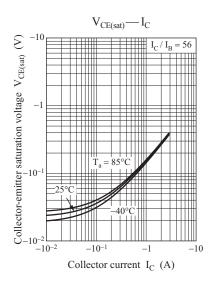
^{2. *1:} Pulse measurement

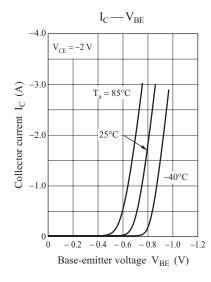
^{*2:} Rank classification: Only R rank producing.

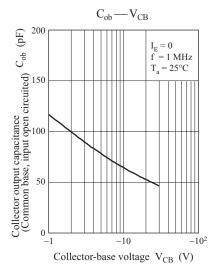


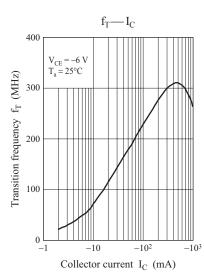








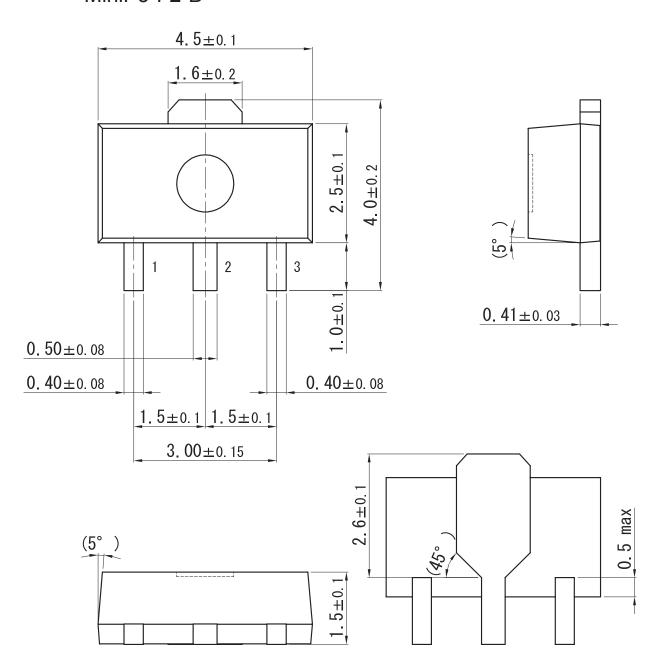




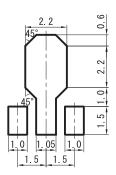
Ver. BED 2

MiniP3-F2-B

Unit: mm



■ Land Pattern (Reference) (Unit: mm)



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