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# Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China





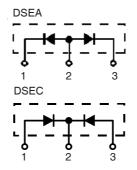


# HiPerFRED™ Epitaxial Diode ISOPLUS220™

### **Electrically Isolated Back Surface**

#### **Preliminary Data Sheet**

V <sub>RSM</sub>	V <sub>RRM</sub>	Туре
600	600	DSEA 59-06BC DSEC 59-06BC



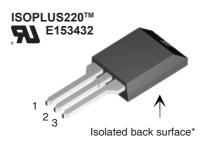
Symbol	Conditions	Maximum Ratings	5
I <sub>FRMS</sub> ①	Lead current limit T <sub>C</sub> = 105°C; rectangular, d = 0.5	45 30	A A
I <sub>FSM</sub>	$T_{VJ} = 45^{\circ}C$ ; $t_p = 10 \text{ ms (50 Hz), sine}$	200	Α
E <sub>AS</sub>	$T_{VJ} = 25^{\circ}\text{C}$ ; non-repetitive $I_{AS} = 1.3 \text{ A}$ ; L = 180 $\mu\text{H}$	0.2	mJ
I <sub>AR</sub>	$V_A = 1.5 \cdot V_R$ typical; f = 10 kHz; repetitive	0.1	Α
T <sub>VJ</sub>		-40+175	°C
$T_{VJM}$		175	°C
$T_{stg}$		-40+150	°C
TL	1.6 mm (0.063 in) from case for 10 s	260	°C
$\mathbf{P}_{tot}$	$T_{C} = 25^{\circ}C$	136	W
V <sub>ISOL</sub>	50/60 Hz RMS; $I_{ISOL} \le 1 \text{ mA}$	2500	٧~
F <sub>c</sub>	Mounting force	1165 / 2.515	N / Ib
Weight	typical	2	g

Symbol	Conditions	Charact typ.	eristic V max.	alues/
I <sub>R</sub> ②	$\begin{split} T_{VJ} &= 25^{\circ}\text{C} & V_{R} = V_{RRM} \\ T_{VJ} &= 150^{\circ}\text{C} & V_{R} = V_{RRM} \end{split}$		250 2	μA mA
<b>V</b> <sub>F</sub> ③	$I_F = 30 \text{ A};$ $T_{VJ} = 150^{\circ}\text{C}$ $T_{VJ} = 25^{\circ}\text{C}$		1.56 2.51	V
R <sub>thJC</sub>		0.6	1.1	K/W K/W
t <sub>rr</sub>	$I_F = 1 \text{ A}; -di/dt = 200 \text{ A/}\mu\text{s};$ $V_R = 30 \text{ V}; T_{VJ} = 25^{\circ}\text{C}$	30		ns
I <sub>RM</sub>	$V_{R} = 100 \text{ V};$ $I_{F} = 50 \text{ A}; -di_{F}/d$ $T_{VJ} = 100 ^{\circ}\text{C}$	t = 100 A/µs 4		Α

Notes: Data given for  $T_{VJ} = 25^{\circ}C$  and per diode unless otherwise specified

- ① Average current per diode may be limited by center lead RMS current limit when both diodes are conducting.
- $\odot$  Pulse test: pulse Width = 5 ms, Duty Cycle < 2.0 %

 $I_{FAV} = 2x30 A$   $V_{RRM} = 600 V$   $t_{rr} = 35 ns$ 



#### **Features**

- Silicon chip on Direct-Copper-Bond substrate
- High power dissipation
- Isolated mounting surface
- 2500V electrical isolation
- Low cathode to tab capacitance (<15pF)</li>
- · Planar passivated chips
- · Very short recovery time
- Extremely low switching losses
- Low I<sub>RM</sub>-values
- · Soft recovery behaviour
- Epoxy meets UL 94V-0

#### **Applications**

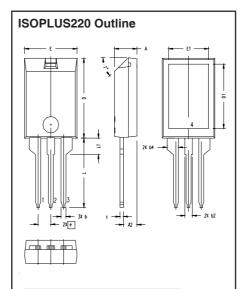
- Antiparallel diode for high frequency switching devices
- · Antisaturation diode
- · Snubber diode
- Free wheeling diode in converters and motor control circuits
- Rectifiers in switch mode power supplies (SMPS)
- · Inductive heating
- Uninterruptible power supplies (UPS)
- · Ultrasonic cleaners and welders

#### **Advantages**

- Avalanche voltage rated for reliable operation
- · Soft reverse recovery for low EMI/RFI
- Low I<sub>RM</sub> reduces:
- Power dissipation within the diode
- Turn-on loss in the commutating switch

See DSEP 29-06B data sheet for characteristic curves





ſ	MY2	INCHES		MILLIMETERS		
		MIN	MAX	MIN	MAX	
	Α	.157	.197	4.00	5.00	
	A2	.098	.118	2.50	3.00	
	b	.035	.051	0.90	1.30	
	b2	.049	.065	1.25	1.65	
ſ	b4	.093	.100	2.35	2.55	
ſ	С	.028	.039	0.70	1.00	
	D	.591	.630	15.00	16.00	
	D1	.472	.512	12.00	13.00	
	Ε	.394	.433	10.00	11.00	
	E1	.295	.335	7.50	8.50	
	е	.100 BASIC		2.55 BASIC		
	L	.512	.571	13.00	14.50	
	L1	.118	.138	3.00	3.50	
	T.			42.5°	47.5°	

#### Notes:

## DSEA 29

- 1. Lead 1 = Cathode
- 2. Lead 2 = Common Anode
- 3. Lead 3 = Cathode

#### DSEC 29

- 1. Lead 1 = Anode 2. Lead 2 = Common Cathode
- 3. Lead 3 = Anode

Back surface 4 is electrically isolated from leads 1, 2 and 3