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TFT LCD Module

Product Specification

DT070BTFT-HB-PTS1
7.0" (1024RGB x 600 DOTS) IPS TFT Module
with Capacitive Touch Screen

July 6, 2016

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Displaytech Ltd.
Tel: (852) 2311 2080 ; Fax: (852) 2722 6998 ; Email: sales@displaytech.com.hk
Address: 31E Billion Plaza 2, No. 10 Cheung Yue Street, Cheung Sha Wan, Kowloon, Hong Kong.
Website: http://www.displaytech.com.hk

Revision Record

REV	CHANGES	DATE
0.0 (Ref. 1.0 20160222)	First release	Mar 17, 2016
1.0 (Ref. 1.2 20160512)	- Increased depth from "7.28" to "7.33" in section 3. General Information; - Changed Weight from "TBD" to "190" in section 3. General Information; - Updated drawing in section 4. Outline Drawing; - Added "Recommend connector number: FH12S-40S-0.5SH" in section 5. Interface Signals. - Added Min View Angles value "70" in section 9. Optical Specification; - Added Min Luminance value "330" in section 9. Optical Specification.	May 23, 2016
1.1 (Ref. 1.3 20160704)	- Corrected Outline Dimension from "164.9 x 100.43 x 7.33" to "164.9 x 100 x 7.33" in 3. General Information; - Corrected from "larger than200mA" to "larger than 200mA" in Note 2 of 7.2 LED Backlight.	Jul 6, 2016

Table of Content

1. Scope

This data sheet is to introduce the specification of DT070BTFT-HB-PTS1 active matrix TFT module. It is composed of a color TFT-LCD panel, driver ICs, FPC, capacitive touch panel and a backlight unit. The 7.0" display area contains 1024x3 (RGB) x 600 pixels.

As to basic specification of the driver IC, refer to the IC specification and datasheet.

2. Application

Digital equipments which need color display, mobile navigator/video systems.

3. General Information

Item	Contents	Unit
Size	7.0	Inch
Resolution	1024x3 (RGB) x 600	
Interface	LVDS	
Technology Type	IPS TFT	
Pixel Configuration	R.G.B. Stripe	
Outline Dimension (W x H x D)	164.9 x 100 x 7.33	mm
Active Area	154.21 x 85.92	mm
Backlight Type	LED	
Controller / driver	EK79001DCGB/EK73217ACGA	
Driver IC of PCAP	FT5426	
Weight	190	g

4. Outline Drawing

NOTES:
1. DISPLAY TYPE: 7.0TFT, NORMALLY BLACK
2. VIEWING DIRECTION : U/L/D/R 80/80/80/80
3. Driver IC : EK79001ECGA/EK73217ACGA
4. Top : -20°C ~ 70°C, Tst : -30°C ~ 80°C
5. GENERAL TOLERANCE: ±0.2
6. BACKLIGHT: LED
7. Driver IC OF CTP: FT5426
8. RoHS Compliance

LED CIRCUIT DIAGRAM:

3*10~30EA   If=200mA

VIEWING ANGLE: ALL

1024 × RGB × 600 DOTS

DETAIL A

DETAIL A(2:1)

STIFFENER T=0.2mm

TP	PIN NAME
1	SCL
2	SDA
3	VDD
4	RESET
5	INT
6	GND

DT070BTFT-HB-PTS1

Displaytech Ltd.

	BY:	DWG NAME:
DRAWN		
CHECKED	BY:	DWG NO:
APPROVED	BY:	
CONFIRMED	BY:	

	TITLE:	
	SCALE:	SHEET NO: OF
	UNIT: mm	

PIN	FUCTION
PIN	SYMBOL
1	VCOM
2	VDD
3	VDD
4	NC
5	RESET
6	STBYB
7	GND
8	RXIN0-
9	RXIN0+
10	GND
11	RXIN1-
12	RXIN1+
13	GND
14	RXIN2-
15	RXIN2+
16	GND
17	RXCLKIN-
18	RXCLKIN+
19	GND
20	RXIN3-
21	RXIN3+
22	GND
23	NC
24	NC
25	GND
26	NC
27	NC
28	SELB(HSD)
29	AVDD
30	AVDD
31	GND
32	LED-
33	LED-
34	L/R
35	U/D
36	VGL
37	NC
38	VGH
39	LED+
40	LED+

5. Interface Signals

Recommend connector number: FH12S-40S-0.5SH

No	Symbol	Description	
1	VCOM	Common voltage	
2	VDD	Power supply	
3	VDD	Power supply	
4	NC	No connection	
5	RESET	Global reset pin. Normally pulled high RESET="1", Normally operation RESET="0", enable the reset	
6	STBYB	Standby mode. Normally pulled high STBYB="1", Normally operation STBYB="0", Timing controller, source driver will turn off, all output are High-Z	
7	GND	Power ground	
8	RXIN0-	Differential Data Input ,CH0 (Negative)	
9	RXIN0+	Differential Data Input ,CH0 (Positive)	
10	GND	Power ground	
11	RXIN1-	Differential Data Input ,CH1 (Negative)	
12	RXIN1+	Differential Data Input ,CH1 (Positive)	
13	GND	Power ground	
14	RXIN2-	Differential Data Input ,CH2 (Negative)	
15	RXIN2+	Differential Data Input ,CH2 (Positive)	
16	GND	Power ground	
17	RXCLKIN-	Differential Clock Input (Negative)	
18	RXCLKIN +	Differential Clock Input (Positive)	
19	GND	Power ground	
20	RXIN3-	Differential Data Input ,CH3 (Negative)	
21	RXIN3+	Differential Data Input ,CH3 (Positive)	
22	GND	Power ground	
23	NC	No connection	
24	NC	No connection	
25	GND	Power ground	
26	NC	No connection	
27	NC	No connection	
28	SELB	In LVDS interface connected HSD to FPC for Pin Setting HSD="L":8 bit ; HSD="H":6 bit	
29	AVDD	Power supply for analog circuits	
30	GND	Power ground	
31	LED-	Power for LED backlight (Cathode)	
32	LED-	Power for LED backlight (Cathode)	
33	L/R	Source Right or Left sequence control.	Note
34	U/D	Gate Up or Down scan control.	Note
35	VGL	Gate OFF Voltage	
36	NC	No connection	
37	NC	No connection	
38	VGH	Gate ON Voltage	
39	LED+	Power for LED backlight (Anode)	
40	LED+	Power for LED backlight (Anode)	

Note:

U/P	L/R	Function
0	1	Normal Display
0	0	Inverse Left and Right
1	1	Inverse Up and Down
1	0	Inverse Left and Right, Inverse Up and Down

TP signal interface

Pin	Symbol	Description
1	SCL	I2C clock signal
2	SDA	I2C data signal
3	VDD	Power supply
4	RESET	Reset Pin
5	INT	Interrupt output Pin
6	GND	Power ground

6. Absolute Maximum Ratings

6.1 Electrical Absolute Max. Ratings

Parameter	Symbol	Min	Max	Unit	Remark
Power supply voltage	VDD	-0.3	5.0	V	
	AVDD	-0.5	15.0	V	
	VGH	-0.3	40.0	V	
	VGL	-20	0.3	V	
Logic signal input / output voltage	V_{IOVCC}	-0.3	VDD+0.5	V	
Current of LED	ILED	0	175	mA	

Notes:
1. If the module is above these absolute maximum ratings. It may become permanently damaged. Using the module within the following electrical characteristic conditions are also exceeded, the module will malfunction and cause poor reliability.
2. Please be sure users are grounded when handing LCD Module.

6.2 Environment Conditions

Item	Symbol	Min	Max	Unit	Remark
Operating Temperature	TOPR	-20	70	℃	
Storage Temperature	TSTG	-30	80	℃	

Notes:
1. The response time will become lower when operated at low temperature.
2. Background color changes slightly depending on ambient temperature. The phenomenon is reversible.
3. Ta<=40℃:85%RH MAX.
   Ta>=40℃:Absolute humidity must be lower than the humidity of 85%RH at 40℃.

7. Electrical Specifications

7.1 Electrical characteristics   GND=0V, Ta=25℃

Item		Symbol	Min	Typ	Max	Unit	Remark
Power supply		VDD	2.6	3.3	3.6	V	
		VCOM	2.85		3.45	V	
		AVDD	9.4	9.6	9.8	V	
		VGH	17	18	19	V	
		VGL	-6.6	-6.0	-5.4	V	
Input voltage	H	V_{IH}	0.8VDD	---	VDD	V	VDD=2.8V
	L	V_{IL}	0	---	0.2VDD		
Current consumption		I_{VDD}	---	30	45	V	
		I_{AVDD}	---	35	45	V	

7.2 LED Backlight   Ta=25℃

Item	Symbol	Min	Typ	Max	Unit	Remark
Forward current	IL	---	200		mA	
Forward voltage	VL	---	9.6		V	Note 1
Operation lifetime	---	---	25,000	---	Hrs	Note 2

Note:
1. The LED Supply Voltage is defined by the number of LED at Ta=25℃ and IL =200mA.
2. The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25℃ and IL =140mA. The LED lifetime could be decreased if operating IL is lager than 200mA.

8. Command/AC Timing

8.1 Timing Characteristics of Input Signals

ITEM			SYMBOL	MIN	TYP	MAX	UNIT
LVDS Input Signal Sequence		CLK Frequency	tclk	45	51.2	57	MHz
LCD Input Signal Sequence (Input LVDS Transmitter)	DENA	Horizontal — Horizontal Total Time	t_H	1324	1344	1364	tCLK
		Horizontal Effective Time	t_{HA}		1024		tCLK
		Horizontal Blank Time	t_{HB}	300	320	340	tCLK
		Vertical — Vertical Total Time	t_V	625	635	645	t_H
		Vertical Effective Time	t_{VA}		600		t_H
		Vertical Blank Time	t_{VB}	25	35	45	t_H

8.2 Timing Sequence (Timing Chart)

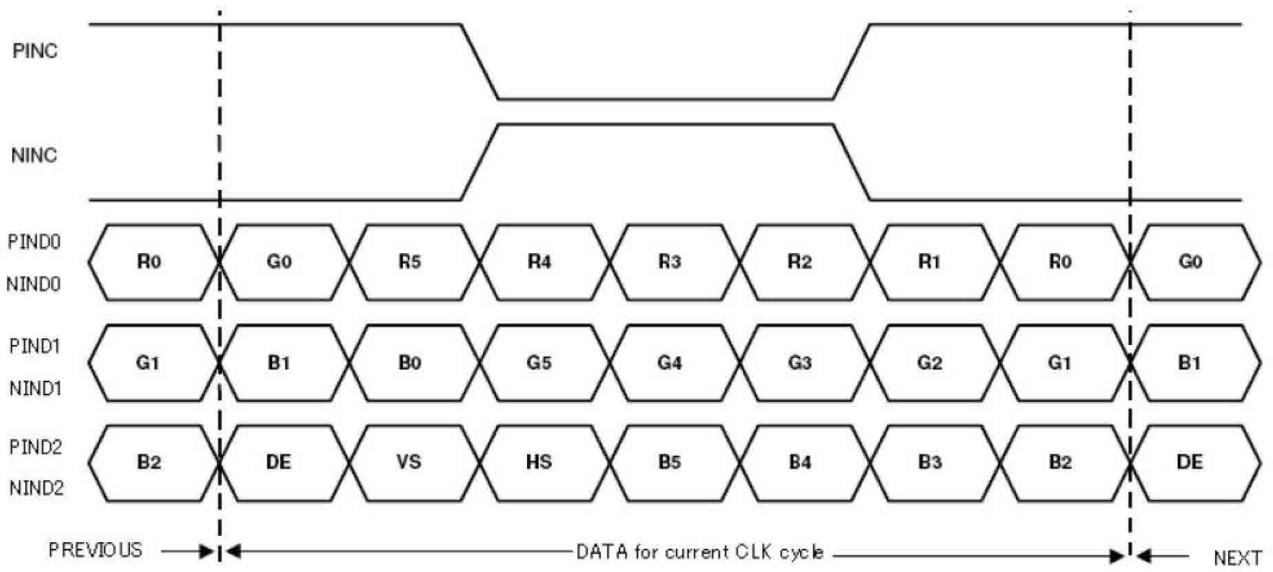8.2.1. Horizontal Timing Sequence
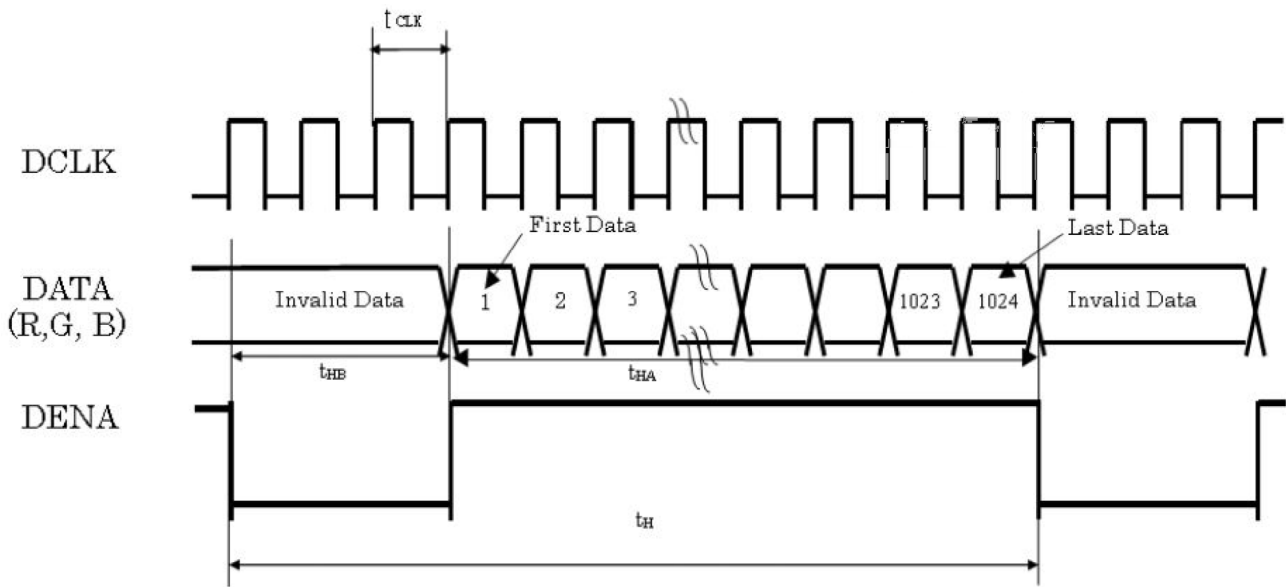
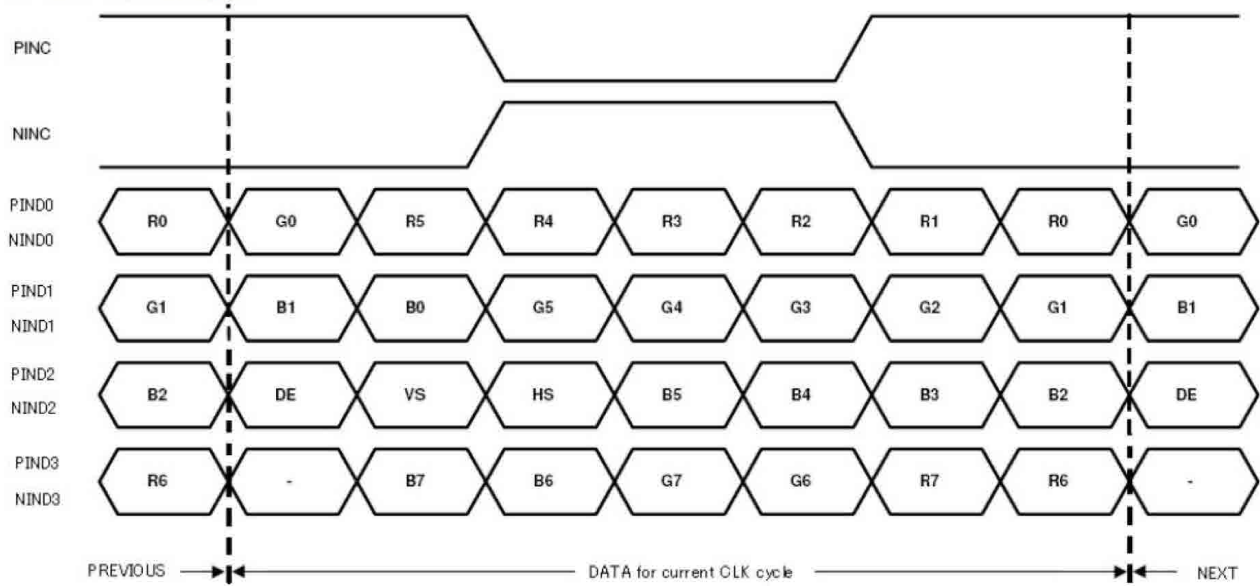

8.2.2 Vertical Timing Sequence
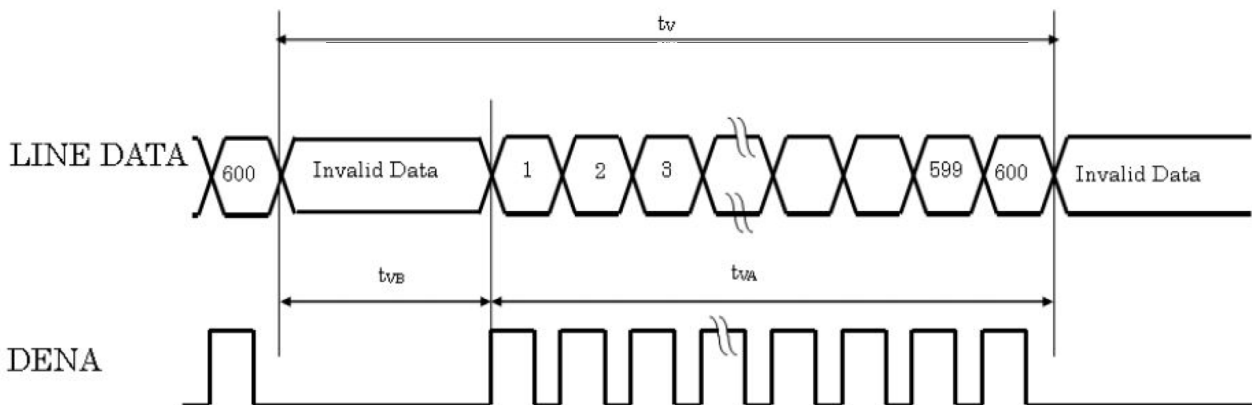
8.2.3 LVDS Input Data Mapping
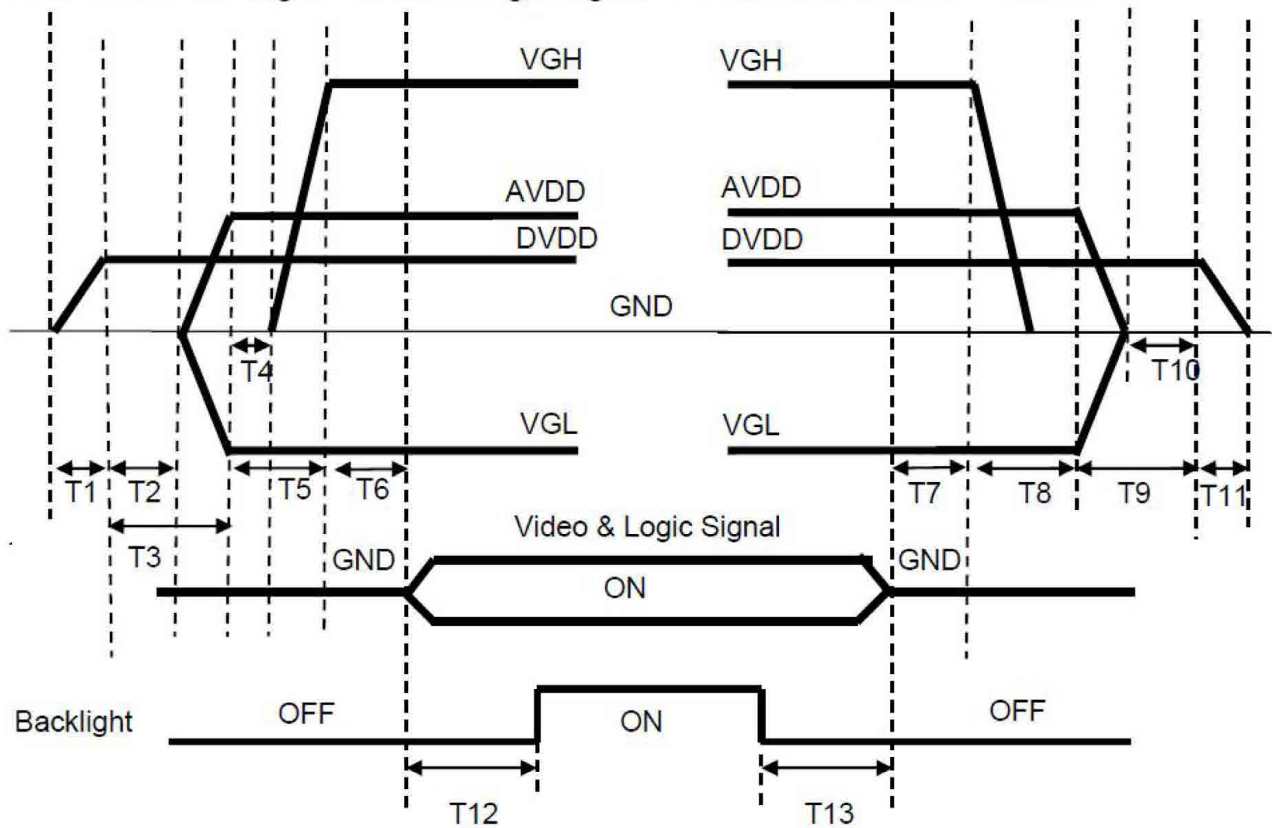
6bits LVDS Input

8bits LVDS Input

8.3 Power ON/OFF Sequence

Power On : DVDD→AVDD/VGL →VGH →Video &Logic Signal→Backlight
Power Off : Backlight→Video &Logic Signal→ VGH→AVDD/VGL→DVDD

0<T1≦10ms
T2>0ms
T3>20ms
T4>0ms
T5>10ms
0<T6≦10ms
T12≧200ms

T7>0ms
T8>0ms
T9>0ms
T10>0ms
0<T11≦10ms
T13≧200ms

9. Optical Specification

Ta=25℃

Item		Symbol	Condition	Min	Typ	Max	Unit	Remark
Contrast Ratio		CR	θ=0°	---	800	---		Note 1 Note 2
Response Time		Tr+Tf	25℃	---	25	---	ms	Note 1 Note 3
View Angles		θT	CR≧10	70	80	---	Degree	Note 4
		θB		70	80	---		
		θL		70	80	---		
		θR		70	80	---		
Chromaticity	White	X	Brightness is on	Typ -0.05	0.290	Typ +0.05		Note 1 Note 5
		Y			0.331			
	Red	X			0.632			
		Y			0.311			
	Green	X			0.297			
		Y			0.536			
	Blue	X			0.140			
		Y			0.154			
Luminance		L		330	430	---	cd/m^2	Note 1 Note 6
Uniformity		U		75	80	---	%	Note 1 Note 7

Note 1: Definition of optical measurement system.

Temperature = 25℃(±3℃)

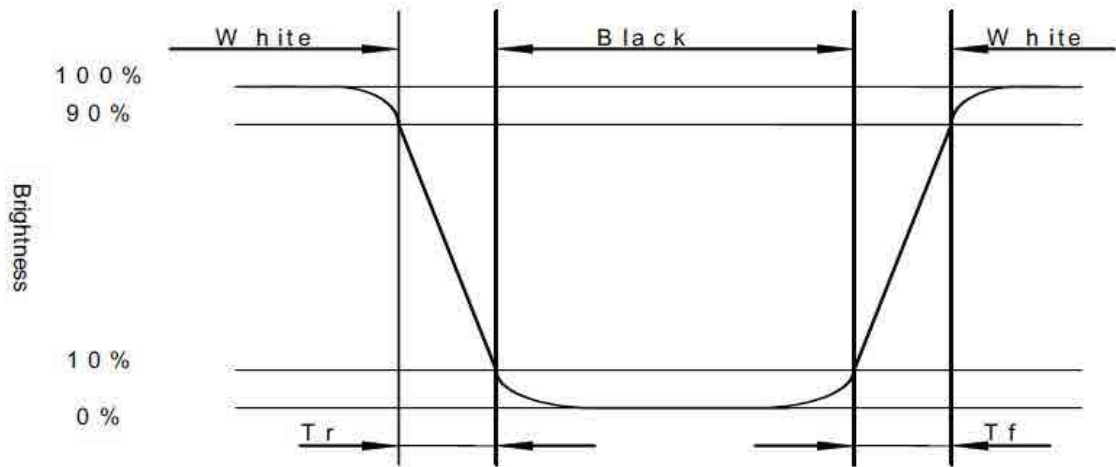LED back-light: ON, Environment brightness < 150 lx
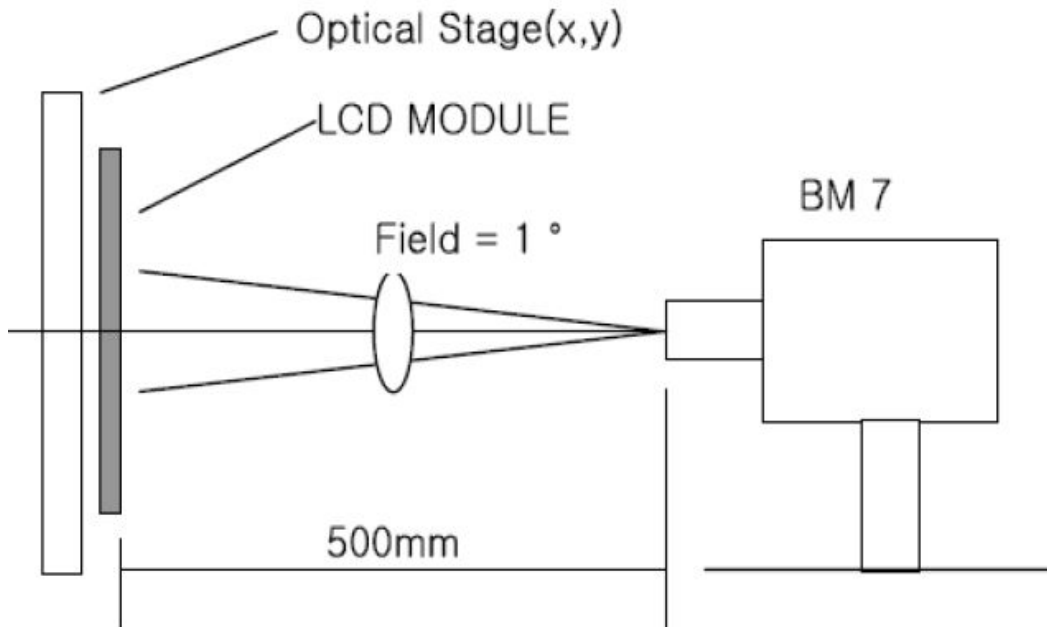
Note 2: Contrast ratio is defined as follow:

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$
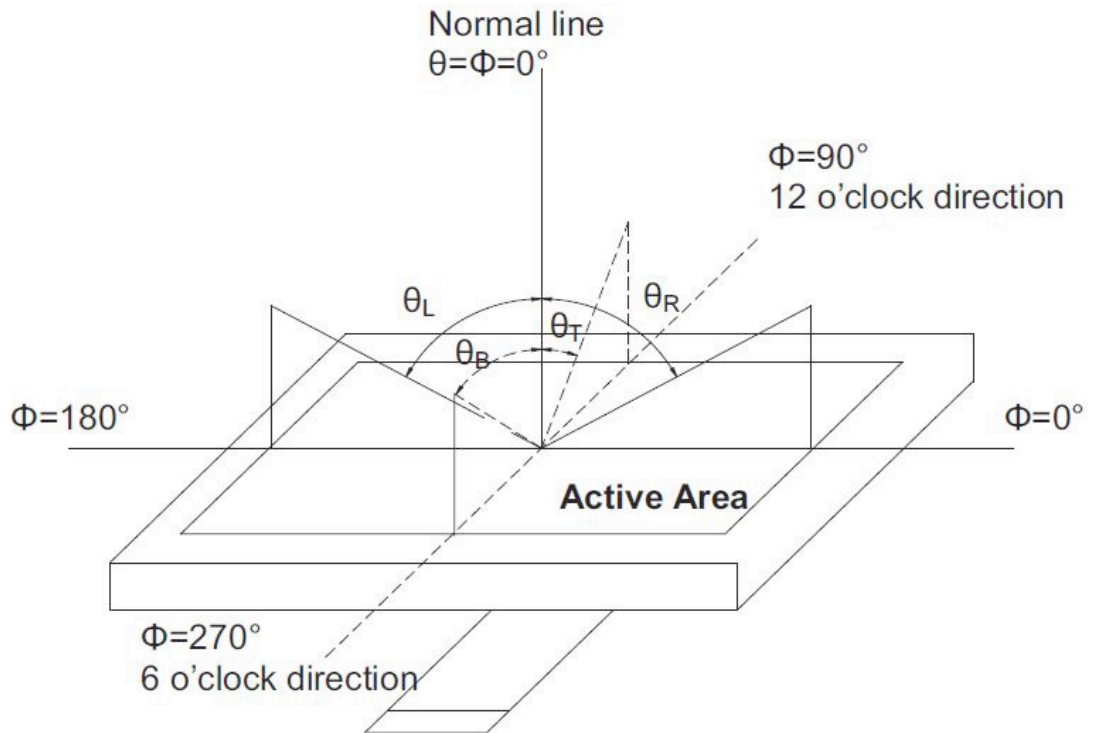
Note 3: Response time is defined as follow:

Response time is the time required for the display to transition from black to white (Rise Time, Tr) and from white to black(Decay Time, Tf).
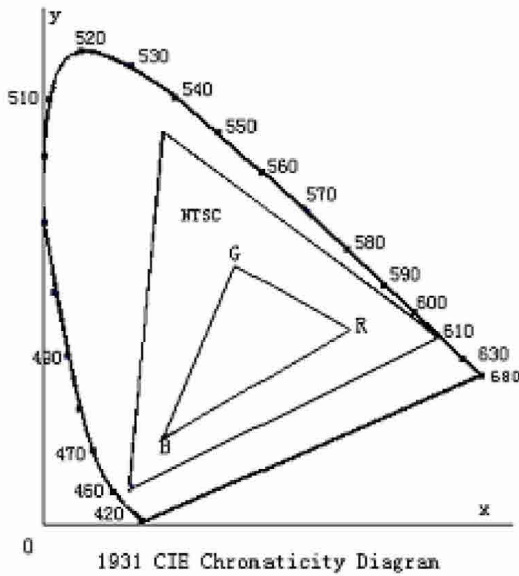

Note 4: Viewing angle range is defined as follow:

Viewing angle is measured at the center point of the LCD.

Note 5: Color chromaticity is defined as follow: (CIE1931)

Color coordinates measured at center point of LCD.

$$S = \frac{area\ of\ RGB\ triangle}{area\ of\ NTSC\ triangle} \times 100\%$$

Note 6: Luminance is defined as follow:

Luminance is defined as the brightness of all pixels "White" at the center of display area on optimum contrast.

Note 7: Luminance Uniformity is defined as follow:

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

$$\text{Uniformity (U)} = \frac{Minimum\ Luminance(\ brightness\ )\ in\ 9\ points}{Maximum\ Luminance(\ brightness\ )\ in\ 9\ points}$$
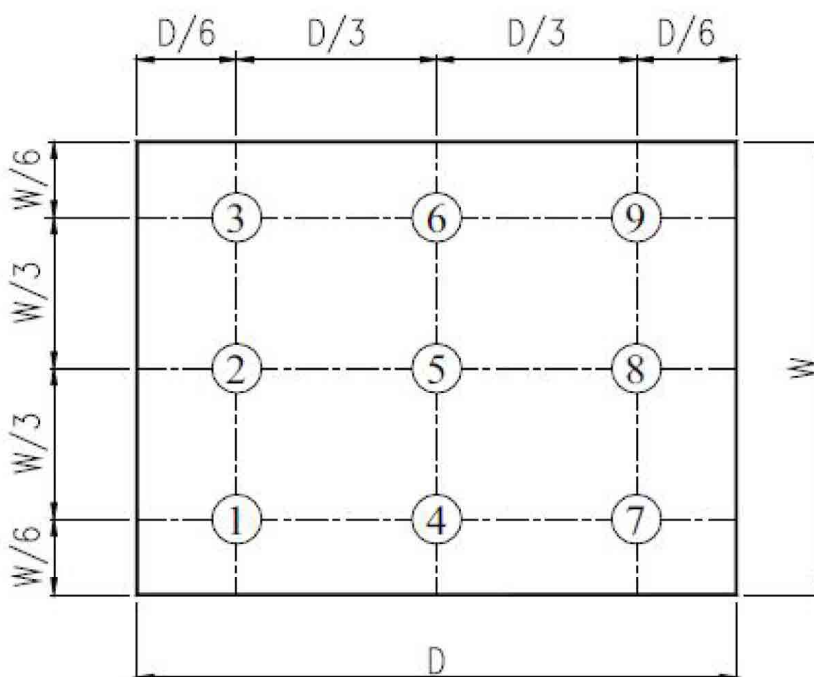

Fig. 2 Definition of uniformity

10. Environmental / Reliability Tests

No	Test Item	Condition	Judgment Criteria
1	High Temp Operation	Ts=+70℃, 96hrs	Per table in below
2	Low Temp Operation	Ta=-20℃, 96hrs	Per table in below
3	High Temp Storage	Ta=+80℃, 96hrs	Per table in below
4	Low Temp Storage	Ta=-30℃, 96hrs	Per table in below
5	High Temp & High Humidity Storage	Ta=+60℃, 90% RH 96 hours	Per table in below (polarizer discoloration is excluded)
6	Thermal Shock (Non-operation)	-30℃ 30 min~+80℃ 30 min, Change time: 5min, 5 Cycles	Per table in below
7	ESD (Operation)	Air discharge: ±8KV; Contact discharge: ±4KV	Per table in below
8	Vibration (Non-operation)	10Hz~150Hz, 100m/s^2, 120min	Per table in below
9	Shock (Non-operation)	Half- sine wave, 300m/s^2, 11ms	Per table in below
10	Package Drop Test	Height: 80 cm, 1 corner, 3 edges, 6 surfaces	Per table in below

Inspection	Criterion (after test)
Appearance	No Crack on the FPC, on the LCD Panel
Alignment of LCD Panel	No Bubbles in the LCD Panel No other Defects of Alignment in Active area
Electrical current	Within device specifications
Function / Display	No Broken Circuit, No Short Circuit or No Black line No Other Defects of Display

11. Precautions for Use of LCD Modules

11.1 Safety

The liquid crystal in the LCD is poisonous. Do not put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.

11.2 Handling

A. The LCD and touch panel is made of plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.

B. Do not handle the product by holding the flexible pattern portion in order to assure the Reliability

C. Transparency is an important factor for the touch panel. Please wear clear finger sacks, gloves and mask to protect the touch panel from finger print or stain and also hold the portion outside the view area when handling the touch panel.

D. Provide a space so that the panel does not come into contact with other components.

E. To protect the product from external force, put a covering lens (acrylic board or similar board) and keep an appropriate gap between them.

F. Transparent electrodes may be disconnected if the panel is used under environmental conditions where dew condensation occurs.

G. Property of semiconductor devices may be affected when they are exposed to light, possibly resulting in IC malfunctions.

H. To prevent such IC malfunctions, your design and mounting layout shall be done in the way that the IC is not exposed to light in actual use.

11.3 Static Electricity

A. Ground soldering iron tips, tools and testers when they are in operation.

B. Ground your body when handling the products.

C. Power on the LCD module before applying the voltage to the input terminals.

D. Do not apply voltage which exceeds the absolute maximum rating.

E. Store the products in an anti-electrostatic bag or container.

11.4 Storage

A. Store the products in a dark place at +25℃±10℃ with low humidity (40% RH to 60% RH). Don't expose to sunlight or fluorescent light.

B. Storage in a clean environment, free from dust, active gas, and solvent.

11.5 Cleaning

A. Do not wipe the touch panel with dry cloth, as it may cause scratch.

B. Wipe off the stain on the product by using soft cloth moistened with ethanol. Do not allow ethanol to get in between the upper film and the bottom glass. It may cause peeling issue or defective operation. Do not use any organic solvent or detergent other than ethanol.

11.6 Cautions for installing and assembling

Bezel edge must be positioned in the area between the Active area and View area. The bezel may press the touch screen and cause activation if the edge touches the active area. A gap of approximately 0.5mm is needed between the bezel and the top electrode. It may cause unexpected activation if the gap is too narrow. There is a tolerance of 0.2 to 0.3mm for the outside dimensions of the touch panel and tail. A gap must be made to absorb the tolerance in the case and connector.