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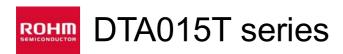
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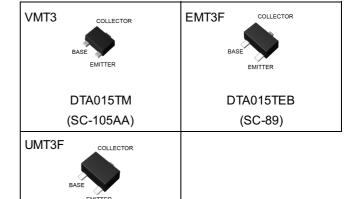
PNP -100mA -50V Digital Transistors (Bias Resistor Built-in Transistors)

Datasheet

Parameter	Value
V <sub>CEO</sub>	-50V
I <sub>C</sub>	-100mA
R <sub>1</sub>	100kΩ

### Features

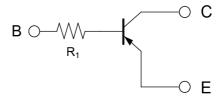
- 1) Built-In Biasing Resistor
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of completely eliminating parasitic effects.
- 4) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 5) Complementary NPN Types: DTC015T series
- 6) Lead Free/RoHS Compliant.



#### •Inner circuit

DTA015TUB (SC-85)

Outline



### Application

Switching circuit, Inverter circuit, Interface circuit,

Driver circuit

**B: BASE** 

C: COLLECTOR

E: EMITTER

#### Packaging specifications

- 1 de Raging e per incade inc							
Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
DTA015TM	VMT3	1212	T2L	180	8	8000	51
DTA015TEB	EMT3F	1616	TL	180	8	3000	51
DTA015TUB	UMT3F	2021	TL	180	8	3000	51

# ● Absolute maximum ratings (T<sub>a</sub> = 25°C)

Parameter			Values	Unit
Collector-base voltage			-50	V
Collector-emitter voltage			-50	V
Emitter-base voltage		V <sub>EBO</sub>	-5	V
Collector current			-100	mA
	DTA015TM		150	
Power dissipation	DTA015TEB	P <sub>D</sub> *1	150	mW
DTA015TUB			200	
Junction temperature		T <sub>j</sub>	150	°C
Range of storage temperature		T <sub>stg</sub>	-55 to +150	°C

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Dougnoston	Currente e l	Conditions	Values			l limit	
Parameter	Symbol	N		Тур.	Max.	Unit	
Collector-base breakdown voltage	BV <sub>CBO</sub>	I <sub>C</sub> = -50μA	-50	-	-	V	
Collector-emitter breakdown voltage	BV <sub>CEO</sub>	I <sub>C</sub> = -1mA	-50	-	-	V	
Emitter-base breakdown voltage	BV <sub>EBO</sub>	I <sub>E</sub> = -50μA	-5	1	-	V	
Collector cut-off current	I <sub>CBO</sub>	V <sub>CB</sub> = -50V	1	1	-0.5	μA	
Emitter cut-off current	I <sub>EBO</sub>	V <sub>EB</sub> = -4V	1	1	-0.5	μA	
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	$I_{C} / I_{B} = -5 \text{mA} / -0.25 \text{mA}$	ı	-0.05	-0.25	V	
DC current gain	h <sub>FE</sub>	$V_{CE} = -10V, I_{C} = -5mA$	100	ı	600	-	
Input resistance	R <sub>1</sub>	-	70	100	130	kΩ	
Transition frequency	f <sub>T</sub> *2	V <sub>CE</sub> = -10V, I <sub>E</sub> = 5mA, f = 100MHz	-	250	-	MHz	

<sup>\*1</sup> Each terminal mounted on a reference footprint

<sup>\*2</sup> Characteristics of built-in transistor

## ● Electrical characteristic curves (T<sub>a</sub> =25°C)

Fig.1 Grounded emitter propagation characteristics

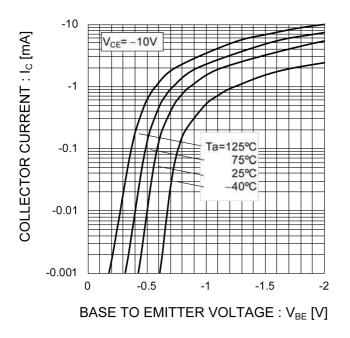


Fig.2 Grounded emitter output characteristics

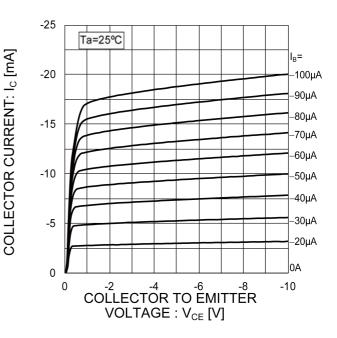


Fig.3 DC Current gain vs. Collector Current

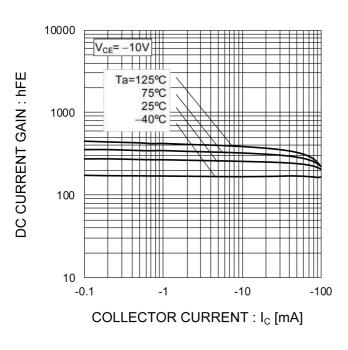
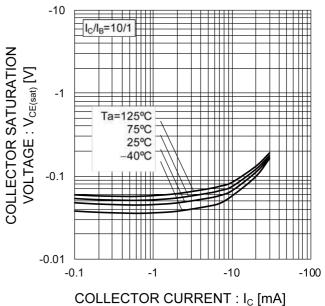


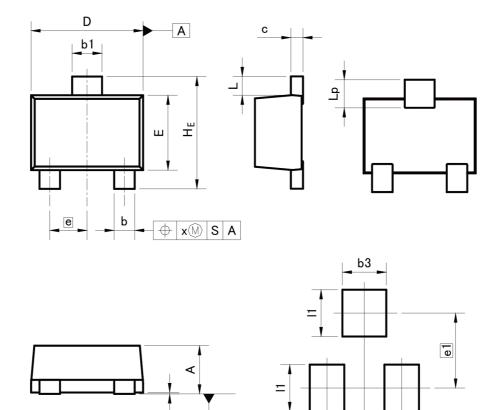
Fig.4 Collector-emitter saturation voltage vs.

Collector Current



### Dimensions

VMT3



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

b2

MIN 0.018 0.000 0.007	0.022 0.004		
0.000			
	0.004		
0.007			
	0.011		
0.011	0.015		
0.003	0.007		
0.043	0.051		
0.028	0.035		
0.0	0.02		
0.043	0.051		
0.004	0.012		
0.008	0.016		
( <u>a</u> )	0.004		
INC	HES		
MIN	MAX		
=	0.015		
-	0.019		
0.0	31		
	0.008 - INC MIN - -		

Dimension in mm/inches

11

A

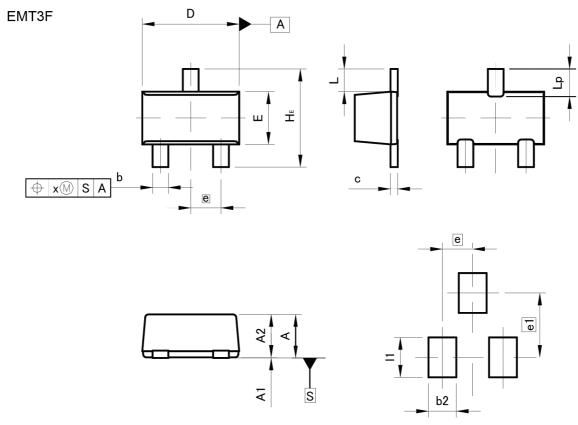
S



0.020

0.50

### Dimensions



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

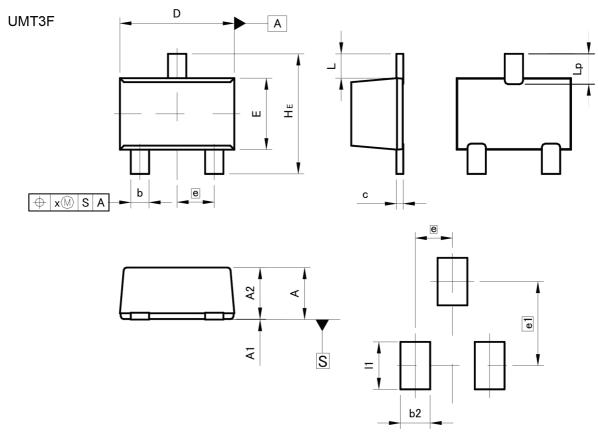
DIM -	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	0.65	0.85	0.026	0.033
A1	0.00	0.10	0.000	0.004
A2	0.60	0.80	0.024	0.031
b	0.21	0.36	0.008	0.014
С	0.08	0.18	0.003	0.007
D	1.50	1.70	0.059	0.067
E	0.76	0.96	0.030	0.038
е	0.9	50	0.0	20
HE	1.50	1.70	0.059	0.067
L	0.37		0.0	15
Lp	0.35	0.55	0.014	0.022
х	=	0.10	<u> =</u> 3	0.004

DIM -	MILIM	ETERS	INCHES	
DIM	MIN	MAX	MIN	MAX
b2	-	0.46	20	0.018
e1	=	1.05	#is	0.041
11	-	0.65	<del></del>	0.026

Dimension in mm/inches



### Dimensions



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM -	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	0.85	1.05	0.033	0.041
A1	0.00	0.10	0.000	0.004
A2	0.80	1.00	0.031	0.039
b	0.27	0.42	0.011	0.017
С	0.08	0.18	0.003	0.007
D	1.90	2.10	0.075	0.083
E	1.15	1.35	0.045	0.053
е	0.0	65	0.0	26
HE	2.00	2.20	0.079	0.087
L	0.43		0.0	17
Lp	0.43	0.63	0.017	0.025
х	-	0.10		0.004

DIM -	MILIM	ETERS	INCHES	
DIM	MIN	MAX	MIN	MAX
b2	-	0.52	221	0.020
e1	1,	47	0.0	058
11	-	0.83	<del>=</del> :	0.033

Dimension in mm/inches



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