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Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China







-100mA / -50V Digital transistors (with built-in resistors) DTA143EM / DTA143EE / DTA143EUA / DTA143EKA / DTA143ESA

Applications

Inverter, Interface, Driver

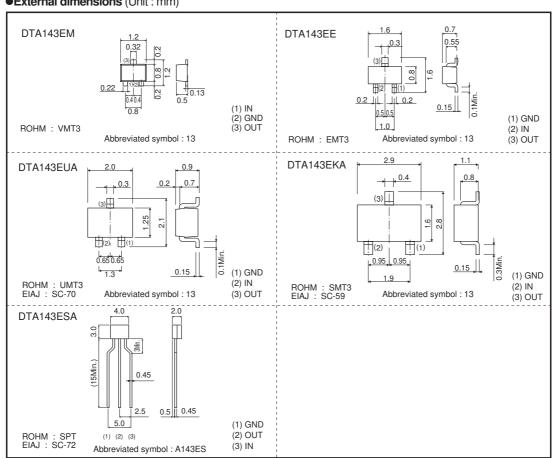
Features

- 1) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 3) Only the on/off conditions need to be set for operation, making the device design easy.

Structure

PNP epitaxial planar silicon transistor (Resistor built-in type)

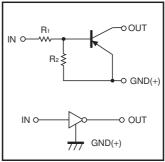
●External dimensions (Unit : mm)



Packaging specifications

	Package	VMT3	EMT3	UMT3	SMT3	SPT
	Packaging type	Taping	Taping	Taping	Taping	Taping
	Code	T2L	TL	T106	T146	TP
Part No.	Basic ordering unit (pieces)	8000	3000	3000	3000	5000
DTA143EM		0	-	-	-	-
DTA143EE		_	0	-	-	_
DTA143EUA		-	-	0	_	-
DTA143EKA		-	-	-	0	-
DTA143ESA	_	-	-	-	0	

●Equivalent circuit



R₁=R₂=4.7kΩ

● Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits			
Farameter		DTA143EM DTA143EE	DTA143EUA DTA143I	EKA DTA143ESA	Unit
Supply voltage	Vcc	–50			V
Input voltage	VIN	−30 to +10			
0	lo	-100			
Output current	IC(Max.)	-100			
Power dissipation	Po	150	200	300	mW
Junction temperature	Tj	150			°C
Storage temperature	Tstg	−55 to +150			°C

●Electrical characteristics (Ta=25°C)

	`	,				
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
lanut valta sa	VI(off)	-	-	-0.5	,,	Vcc=-5V, Io=-100μA
Input voltage	V _{I(on)}	-3	-	-	V	Vo=-0.3V, Io=-20mA
Output voltage	V _{O(on)}	-	-0.1	-0.3	٧	lo/l=-10mA/-0.5mA
Input current	lı .	-	-	-1.8	mA	V=-5V
Output current	IO(off)	_	_	-0.5	μΑ	Vcc=-50V, V⊫0V
DC current gain	Gı	30	-	-	-	Vo=-5V, Io=-10mA
Input resistance	R ₁	3.29	4.7	6.11	kΩ	-
Resistance ratio	R2/R1	0.8	1	1.2	-	-
Transition frequency	f⊤ *	_	250	-	MHz	Vc=-10V, Ie=5mA, f=100MHz

^{*} Characteristics of built-in transistor

Electrical characteristic curves

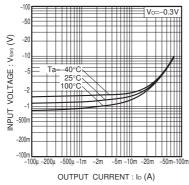


Fig.1 Input voltage vs. output current (ON characteristics)

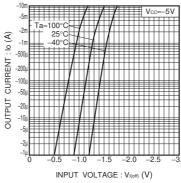


Fig.2 Output current vs. input voltage (OFF characteristics)

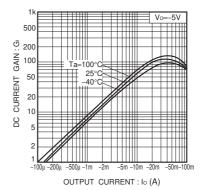


Fig.3 DC current gain vs. output current

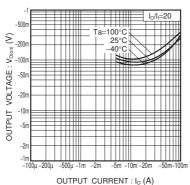


Fig.4 Output voltage vs. output current

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