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NPN 100mA 50V Digital Transistors (Bias Resistor Built-in Transistors)

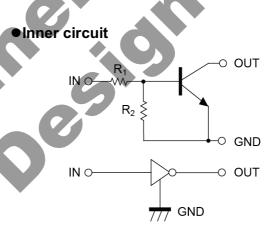
Datasheet

Parameter	Value
V _{CC}	50V
I _{C(MAX.)}	100mA
R ₁	10kΩ
R ₂	4.7kΩ

Features

- 1) Built-In Biasing Resistors
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of completely eliminating parasitic effects.
- 4) Only the on/off conditions need to be set for operation, making the circuit design easy:
- 5) Complementary PNP Types: DTA114W series
- 6) Lead Free/RoHS Compliant.

Outline EMT3 UMT3 OUT GND DTC114WE SOT-416(SC-75A) SMT3 OUT GND OU



DTC114WKA

SOT-346(SC-59)

Application

Switching circuit, Inverter circuit, Interface circuit, Driver circuit

Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
DTC114WE	EMT3	1616	TL	180	8	3000	84
DTC114WUA	UMT3	2021	T106	180	8	3000	84
DTC114WKA	SMT3	2928	T146	180	8	3000	84

● Absolute maximum ratings (T_a = 25°C)

Parameter			Values	Unit	
Supply voltage			50	V	
Input voltage			-10 to 30	V	
Output current			100	mA	
Collector current			100	mA	
	DTC114WE		150		
Power dissipation	DTC114WUA	P _D *2	200	mW	
	DTC114WKA		200		
Junction temperature		Tj	150	°C	
Range of storage temperature		T _{stg}	-55 to +150	°C	

● Electrical characteristics (T_a = 25°C)

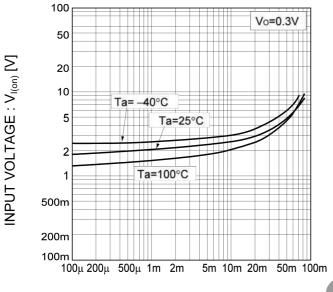
Parameter	Symbol Conditions		Values			Unit	
Parameter			Min.	Тур.	Max.	Offic	
Input valtage	V _{I(off)}	$V_{CC} = 5V, I_{O} = 100 \mu A$	-	-	0.8	\/	
Input voltage	V _{I(on)}	$V_0 = 0.3V, I_0 = 2mA$	3	-	-	V	
Output voltage	V _{O(on)}	$I_0/I_1 = 10 \text{mA} / 0.5 \text{mA}$	-	0.1	0.3	V	
Input current	I	V ₁ = 5V	1	-	0.88	mA	
Output current	I _{O(off)}	$V_{CC} = 50V, V_{I} = 0V$	-	-	0.5	μA	
DC current gain	Gi	$V_{O} = 5V, I_{O} = 10mA$	24	-	-	-	
Input resistance	R ₁	-	7	10	13	kΩ	
Resistance ratio	R ₂ /R ₁	-	0.37	0.47	0.57	-	
Transition frequency	f _T *1	V _{CE} = 10V, I _E = -5mA, f = 100MHz	-	250	-	MHz	

^{*1} Characteristics of built-in transistor

^{*2} Each terminal mounted on a reference footprint

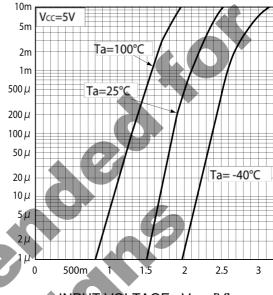
● Electrical characteristic curves (T_a =25°C)

Fig.1 Input voltage vs. output current (ON characteristics)



OUTPUT CURRENT : Io [A]

Fig.2 Output current vs. input voltage (OFF characteristics)



OUTPUT CURRENT : I_o [A]

INPUT VOLTAGE: V_{I(off)} [V]

Fig.3 Output current vs. output voltage

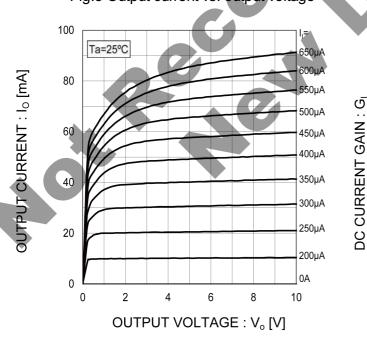
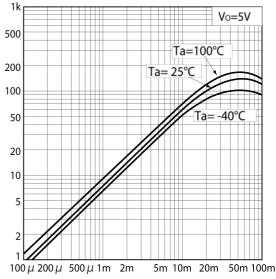


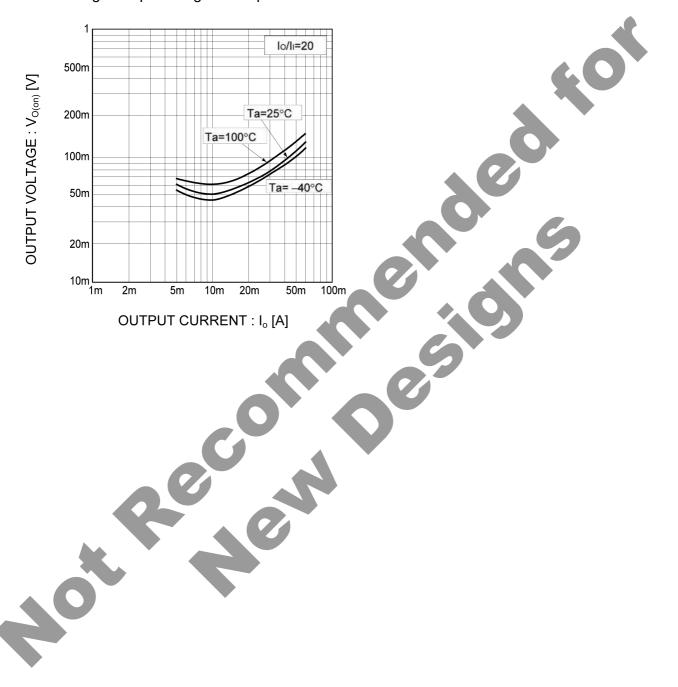
Fig.4 DC current gain vs. output current



OUTPUT CURRENT : Io [A]

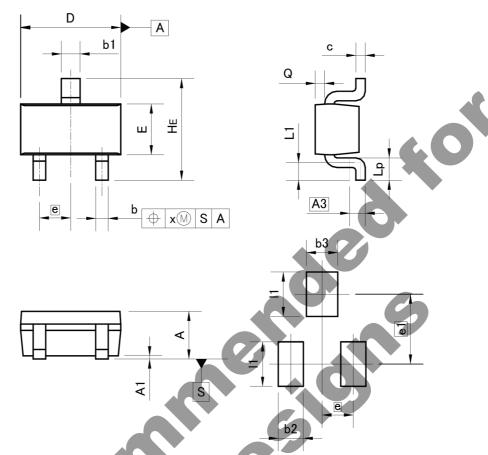
● Electrical characteristic curves (T_a =25°C)

Fig.5 Output voltage vs. output current



Dimensions

EMT3



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIME	TERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
A	0.60	0.80	0.024	0.031	
A1	0.00	0.10	0.000	0.004	
A3	0.2	5	0.0	10	
ь	0.15	0.30	0.006	0.012	
b1	0.25	0.40	0.010	0.016	
C	0.10	0.20	0.004	0.008	
D	1.50	1.70	0.059	0.067	
E	0.70	0.90	0.028	0.035	
е	0.5	0	0.020		
HE	1.40	1.80	0.055	0.071	
L1	0.10	=:	0.004	-	
Lp	0.15	as:	0.006	sa k	
Q	0.05	0.25	0.002	0.010	
X	=:	0.10	_	0.004	

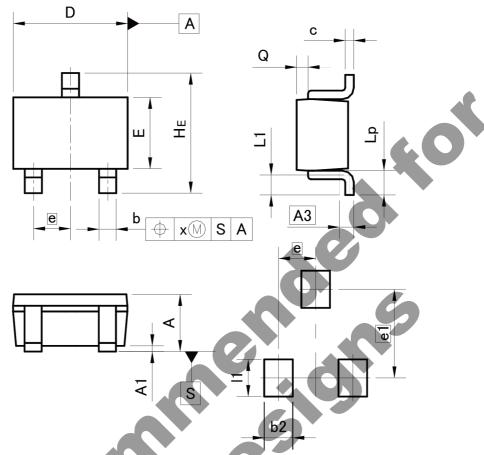
DIM	MILIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
b2	777//	0.40	54	0.016
b3		0.50	-	0.020
e1	1.10		0.0	043
11	= 0	0.70	-:	0.028

Dimension in mm/inches



Dimensions

UMT3



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIM	MILIMETERS		HES
DIM	MIN	MAX	MIN	MAX
A	0.80	1.00	0.031	0.039
A1	0.00	0.10	0.000	0.004
A3	0.	25	0.0	10
Ь	0.15	0.30	0.006	0.012
C	0.10	0.20	0.004	0.008
D	1.90	2.10	0.075	0.083
E	1.15	1.35	0.045	0.053
е	0.	65	0.026	
HE	2.00	2.20	0.079	0.087
L1	0.20	0.50	0.008	0.020
Lp	0.25	0.55	0.010	0.022
Q	0.10	0.30	0.004	0.012
×	=	0.10	223	0.004

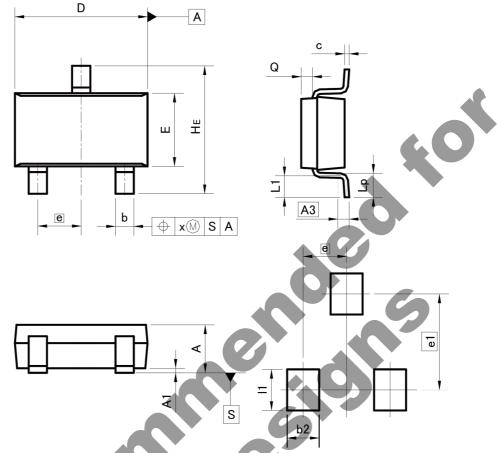
DIM	MILIMETERS		INCHES	
DIM L	MIN	MAX	MIN	MAX
b2	-	0.50	-	0.020
e1	1.55		0.0	061
11	-	0.65	_	0.026

Dimension in mm/inches



Dimensions

SMT3



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
A	1.00	1.30	0.039	0.051
(A1	0.00	0.10	0.000	0.004
A3	0.3	25	0.0	10
b	0.35	0.50	0.014	0.020
С	0.09	0.25	0.004	0.010
D	2.80	3.00	0.110	0.118
E	1.50	1.80	0.059	0.071
е	0.9	95	0.037	
HE	2.60	3.00	0.102	0.118
L1	0.30	0.60	0.012	0.024
Lp	0.40	0.70	0.016	0.028
Q	0.20	0.30	0.008	0.012
x	(22)	0.10	<u> </u>	0.004
У	<u>—</u>	0.10	12	0.004
DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
b2	_	0.60	-	0.024

Dimension in mm/inches

e1

0.035

0.083

0.90

2.10

Notes

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