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## DTC144TKA

## **Features**

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit)
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects
- Only the on/off conditions need to be set for operation, making device design easy

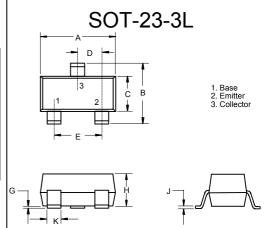
### **Absolute Maximum Ratings**

Parameter	Symbol	Value	Unit
Collector-Base Voltage	$V_{CBO}$	50	V
Collector-Emitter Voltage	$V_{CEO}$	50	V
Emitter-Base voltage	$V_{EBO}$	5	V
Collector Current-Continuous	Ic	100	mA
Collector Dissipation	Pc	200	mW
Junction Temperature	TJ	150	$^{\circ}$
Storage Temperature Range	$T_{STG}$	-55~150	$^{\circ}$

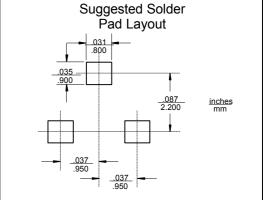
#### **Electrical Characteristics**

Sym	Parameter	Min	Тур	Max	Unit
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage $(I_C=50uA, I_E=0)$	50			V
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage (I <sub>C</sub> =1mA, I <sub>B</sub> =0)	50			V
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage (I <sub>E</sub> =50uA, I <sub>C</sub> =0)	5			V
I <sub>CBO</sub>	Collector Cut-off Current $(V_{CB}=50V, I_{E}=0)$			0.5	uA
I <sub>EBO</sub>	Emitter Cut-off Current (V <sub>EB</sub> =4V, I <sub>C</sub> =0)			0.5	uA
h <sub>FE</sub>	DC Current Gain (V <sub>CE</sub> =5V, I <sub>C</sub> =1mA)	100	300	600	
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage (I <sub>C</sub> =10mA, I <sub>B</sub> =1mA)			0.3	V
R <sub>1</sub>	Input resistance	32.9	47	61.1	ΚΩ
f <sub>T</sub>	Transition Frequency (V <sub>CE</sub> =10V, I <sub>C</sub> =-5mA, f=100MHz)		250		MHz

# **NPN Digital Transistor**



DIMENSIONS							
	INCHES		MM				
DIM	MIN	MAX	MIN	MAX	NOTE		
Α	.113	.117	2.87	2.97			
В	.108	.112	2.75	2.85			
O	.061	.065	1.55	1.65			
D	.036	.038	.925	.975			
П	.073	.077	1.85	1.95			
G	.0016	.0039	.04	.100			
I	.044	.049	1.12	1.25			
J	.006	.007	.14	.17			
Κ	.013	.015	.34	.37			

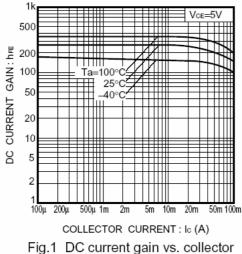


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# DTC144TKA





current

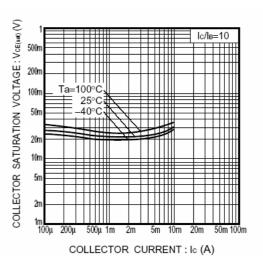
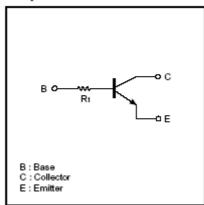


Fig.2 Collector-emitter saturation voltage vs. collector current

### Equivalent circuit



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