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NPN 500mA 50V Digital Transistors (Bias Resistor Built-in Transistors)

Parameter	Value	
V _{CEO}	50V	
I _C	500mA	
R	10kΩ	

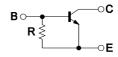
Outline



Features

- 1) Built-In Biasing Resistors
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of completely eliminating parasitic effects.
- 4) Complementary PNP Types :DTB114GK
- 5) Lead Free/RoHS Compliant.

•Inner circuit



Application

Switching circuit, Inverter circuit, Interface circuit, Driver circuit

Packaging specifications

Part No.	Package	Package size (mm)	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit (pcs)	Marking
DTD114GK	SMT3	2928	T146	180	8	3,000	L24

● **Absolute maximum ratings** (Ta = 25°C)

Parameter	Symbol	Values	Unit
Collector-base voltage	V_{CBO}	50	V
Collector-emitter voltage	V _{CEO}	50	V
Emitter-base voltage	V _{EBO}	5	V
Collector current	Ic	500	mA
Power dissipation	P _d *2	200	mW
Junction temperature	T _j	150	°C
Range of storage temperature	T _{stg}	−55 to +150	°C

●Electrical characteristics(Ta = 25°C)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Collector-base breakdown voltage	BV_CBO	I _C = 50μA	50	-		V
Collector-emitter breakdown voltage	BV_CEO	I _C = 1mA	50	-	-	V
Emitter-base breakdown voltage	BV_{EBO}	I _E = 720μA	5	-	1	V
Collector cut-off current	I _{CBO}	V _{CB} = 50V	-	-	0.5	μΑ
Emitter cut-off current	I _{EBO}	$V_{EB} = 4V$	300	-	580	μΑ
Collector-emitter saturation voltage	$V_{CE(sat)}$	$I_{C} / I_{B} = 50 \text{mA} / 2.5 \text{mA}$	-	-	0.3	V
DC current gain	h _{FE}	V_{CE} = 5V , I_{C} = 50mA	56	1	1	-
Emitter-base resistance	R	-	7	10	13	kΩ
Transition frequency	f _T *1	$V_{CE} = 10V, I_{E} = -5mA,$ f = 100MHz	-	200	-	MHz

^{*1} Characteristics of built-in transistor

^{*2} Each terminal mounted on a reference footprint

●Electrical characteristic curves(Ta = 25°C)

Fig.1 Grounded emitter propagation characteristics

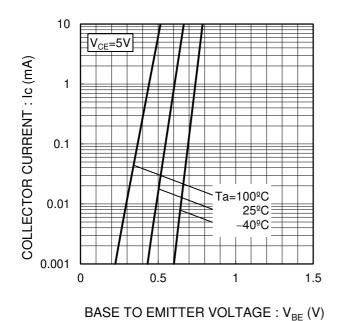


Fig.2 Grounded emitter output characteristics

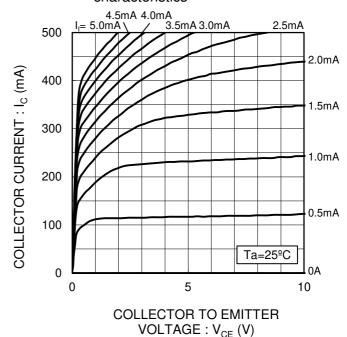


Fig.3 DC Current gain vs. Collector Current

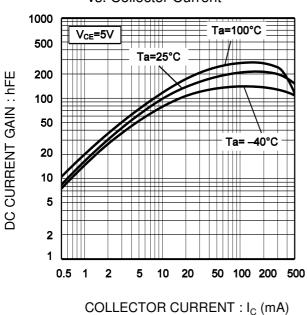
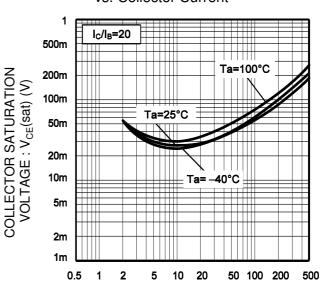


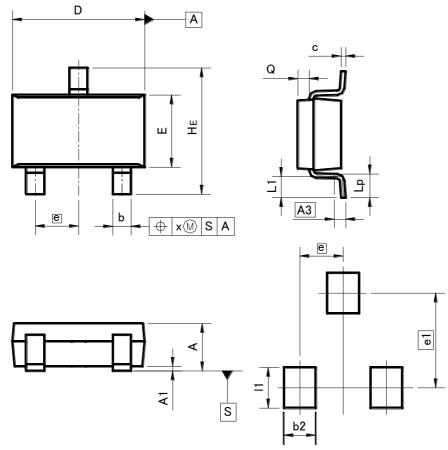
Fig.4 Collector-emitter saturation voltage vs. Collector Current



COLLECTOR CURRENT : I_C (mA)

●Dimensions (Unit : mm)





Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIMI	ETERS	INCHES		
DIW	MIN	MAX	MIN	MAX	
Α	1.00	1.30	0.039	0.051	
A1	0.00	0.10	0.000	0.004	
A3	0.3	25	0.0	10	
b	0.35	0.50	0.014	0.020	
С	0.09	0.25	0.004	0.010	
D	2.80	3.00	0.110	0.118	
E	1.50	1.80	0.059	0.071	
е	0.9	95	0.0	37	
HE	2.60	3.00	0.102	0.118	
L1	0.30	0.60	0.012	0.024	
Lp	0.40	0.70	0.016	0.028	
Q	0.20	0.30	0.008	0.012	
Х	_	0.10	_	0.004	
У	_	0.10	_	0.004	

DIM	MILIM	ETERS	INCHES		
MIN		MAX	MIN	MAX	
b2	_	0.60	_	0.024	
e1	2.10		0.0	83	
11	_	0.90	_	0.035	

Dimension in mm / inches

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