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QUICK START GUIDE

DVK-2

Version 2.0

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1. DVK-2 Introduction

1.1. DVK-2 base-kit

The DVK-2 is designed as quick start solution for the development of applications with AXSEM micro-controllers and AXSEM radio-chips. The DVK-2 base-kit is the development platform for applications based on the AX8052F1xx micro-controller family. With the available radio add-on-kits it becomes the ideal platform for radio performance evaluation and radio system development. The DVK-2 is compatible with AX-RadioLab, AX-MicroLab and AxCode::Blocks development software for the PC.

The **DVK-2** comes with:

- 2x mainboards with radio-module connectors for one radio add-on-kit module, AX8052F100 20 MIPS ultra-low-power micro-controller, LCD display, 4 buttons, 2 LED, AX debug connector and battery connector for 3 AA batteries
- 1x **AXDBG** debug adapter
- 1x Mini USB cable
- 1x AX-Debug cable (Ethernet cable).

1.2. DVK-2 add-on-kits

For evaluation of **AXSEM** radio IC a variety of **add-on-kits** are available. A radio **add-on-kit** consists of a pair of radio modules (and depending on region, frequency, distributor a pair of antennas). Depending on the radio product the kit can consist of two

identical or two different¹ modules. The pictures below show the two different modules of the AX5043-868 **add-on-kit**.

The left module has a TX/RX port and the right module a TX/RX port with SMA connector and a single ended TX-only port without SMA connector.

The following radio **add-on-kits** are available:

- AX5043-868 add-on-kit (2 modules)
- AX5043-433 add-on-kit (2 modules)
- AX5043-169 add-on-kit (2 modules)
- AX5051-868 add-on-kit (2 modules)
- AX5151-868 add-on-kit (2 modules)
- AX5042-868 add-on-kit (2 modules)

For transmitter only evaluation the following transmitter modules are available

- AX5031-868 module
- AX5131-868 module

¹ AX5043 **add-on-kits** are normally shipped with two different modules, a module with an RX/TX RF port and a module with an RX/TX plus a TX-only port.

2. AXDBG Debug Adapter Overview

The **AXDBG** debug adapter is the interface between the PC and the mainboards. It can be used for programming and debugging the **AX8052F1xx** family of micro-controllers. It interfaces on the computer side with low level drivers and the **AXSDB** software interface, which is then used by other **AXSEM** software products. The **AXSDB** can also be used in mass production with the scriptable **AXSDB** software.

The **AXDBG** has the following interfaces and indicators:

- AX debug link RJ45 connector
- mini USB PC interface
- LED red indicating the debug link is active, MCU in debug mode
- LED green indicating a program is executed on the MCU
- Optional module connectors (not used by actual AXSEM software prducts)

3. Main Board Overview (MB)

A DVK-2 mainboard is the core of the DVK-2 development system. It uses an AX8052F100 as a versatile ultra-low-power micro-controller. The mainboard is designed to evaluate AXSEM radio IC as well as being a platform for code development and testing.

The **DVK-2** mainboard is equipped with the following components:

- 1. LCD display with 2 lines of 16 characters each
- 2. Power indicator LED

- 3. DVK-2 radio module connectors
- 4. Programmable indicator LED
- 5. AX8052F100 micro-controller
- 6. AX debug link RJ45 connector
- 7. Power supply switch (select between debug link powered and battery powered operation)
- 8. 4 dip switches
- 9. 4 power-supply jumpers:
 - a. Power for the LCD-display backlight
 - b. Power of the radio-module
 - c. Auxiliary power of the radio-module (TCXO, module recognition)
 - d. Power for the AX8052F100 micro-controller on the mainboard
- 10.4 buttons
- 11.32.768 kHz crystal
- 12. UART connect
- 13. GND test point

4. Batteries and Modules

4.1. Batteries

Three AA batteries can be inserted on the bottom side of the **DVK-2** mainboard. The polarity is indicated on the picture below. The power switch toggles between battery powered and debug-link powered operation. There is no OFF position if batteries are inserted and the debug-link is connected.

4.2. Modules

One radio module can be connected to the main board. The **DVK-2** modules have to be connected to the main board with the SMA connector facing outside.

Note: There is no safeguard for polarity or wrong insertion of modules.

5. Various Antennas

Different types of antennas can be used with the **AXSEM** radio modules. The kits come without antenna but any 50 Ohm antenna designed for a frequency matching the radio module and being equipped with a male SMA connector can be used. Be aware that antenna performance can vary a lot between different makes of antennas.

Antennas in the picture:

- 1. 868 MHz dipole (two grey rings, often used by **AXSEM**)
- 2. 915 MHz dipole (two yellow rings, often used by AXSEM)
- 3. 868 MHz monopole
- 4. 868 MHz monopole
- 5. 433 MHz monopole
- 6. 868 MHz monopole
- 7. 868 MHz monopole

6. Software required to work with the DVK-2

Depending on your application and add-on-kit you need to use different AXSEM software.

| Kit | Matching Software |
|----------------------------------------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| DVK-2 base-kit, no module attached | AX8052IDE package AXCode::Blocks AXSDB LibMF SDCC AX-MicroLab |
| DVK-2 base-kit with AX5043 add-on | AX8052IDE package AXCode::Blocks AXSDB LibMF SDCC AX-RadioLab AX-RadioLab-POCSAG (not free of charge) Pre-installed PER-test |
| DVK-2 base-kit with AX5051 add-on or AX5151 add-on | AX8052IDE package AXCode::Blocks AXSDB LibMF SDCC AX-RadioLab51 Pre-installed PER-test |
| DVK-2 base-kit with AX5042 add-on, AX5031 add-on or AX5131-add-on | AX8052IDE package AXCode::Blocks AXSDB LibMF SDCC AX-ParamCalc AX-Easy_packet Pre-installed PER-test² |

Note: **AX-MicroLab** and **AX-RadioLAB** use the **AX8052IDE** software package in the background. Therefore the **AX8052IDE** software package has to be installed and configured before installation of the other software. For configuration run **AxCode::Blocks** and set **SDCC** as default compiler.

² Note: **AX5031** and **AX5131** modules are transmitters only and do not work on mainboards programmed as receiver

7. Configuration of AxCode::Blocks

After starting **AxCode::Blocks** for the first time the user has to select **SDCC** as the default compiler. The following window will pop-up. After this configuration process you are ready to go.

Note:To delete an **AxCode::Blocks** configuration you have to delete the directory C:\Documents and Settings\user\Application Data\axcodeblocks.

| Compiler | Status | Set as default |
|-----------------------------------|--------------------|--------------------|
| GNU GCC Compiler | Detected | |
| Microsoft Visual C++ Toolkit 2003 | Invalid (not set!) | 1 |
| Microsoft Visual C++ 2005/2008 | Invalid (not set!) | |
| Microsoft Visual C++ 2010 | Invalid (not set!) | N |
| Borland C++ Compiler (5.5, 5.82) | Invalid (not set!) | Ν |
| Digital Mars Compiler | Invalid (not set!) | |
| OpenWatcom (W32) Compiler | Invalid (not set!) | |
| GNU GCC Compiler for MSP430 | Invalid (not set!) | |
| Cygwin GCC | Invalid (not set!) | |
| LCC Compiler | Invalid (not set!) | |
| Keil C51 Compiler | Invalid (not set!) | |
| Keil CX51 Compiler | Invalid (not set!) | |
| IAR 8051 Compiler | Invalid (not set!) | |
| Intel C/C++ Compiler | Invalid (not set!) | |
| SDCC Compiler | Detected | |

| Compiler | Status | <u> </u> | Set as default |
|-----------------------------------|--------------------|--------------|----------------|
| GNU GCC Compiler | Detected | | |
| Microsoft Visual C++ Toolkit 2003 | Invalid (not set!) | | |
| Microsoft Visual C++ 2005/2008 👘 | Invalid (not set!) | | |
| Microsoft Visual C++ 2010 | Invalid (not set!) | | |
| Borland C++ Compiler (5.5, 5.82) | Invalid (not set!) | | |
| Digital Mars Compiler | Invalid (not set!) | | |
| OpenWatcom (W32) Compiler | Invalid (not set!) | | |
| SNU GCC Compiler for MSP430 | Invalid (not set!) | | |
| Eygwin GCC | Invalid (not set!) | | |
| .CC Compiler | Invalid (not set!) | | |
| Keil C51 Compiler | Invalid (not set!) | | |
| Keil CX51 Compiler | Invalid (not set!) | | |
| AR 8051 Compiler | Invalid (not set!) | | |
| intel C/C++ Compiler | Invalid (not set!) | | |
| 5DCC Compiler | Detected | | |

1.

3.

8. Connecting the DVK-2 to a PC

Step 1: To interface with the user the DVK-2 uses a suite of software on a PC.

- AX80521DE package (Installs AxCode::Blocks, AXSDB, LibMF support libraries, the SDCC C-compiler and low-level drivers)
- AX-RadioLab
- AX-RadioLab51
- AX-MicroLab

Make sure that the **AX8052IDE** software is installed before connecting the **AXSDB** to the computer and before installing the other **AXSEM** software. Otherwise you may experience difficulties in installing the driver later. **AX-RadioLab**, **AX-RadioLab51** or **AX-MicroLab** may be installed later, depending on the users intentions.

All necessary software can be downloaded at <u>www.axsem.com</u> .

Step 2: Connect the **AXDBG** via the USB cable to the computer. Since the driver is already installed the computer should report 2 "USB Serial Converters" in your Device Manager.

| 🚇 Geräte-Manager | |
|----------------------------------------------------------------|---|
| Vorgang Ansicht $ $ ← → $ $ 📾 📧 $ $ 😫 $ $ 🕄 | |
| | |
| 🛶 🚔 Intel(R) ICH10 Family USB Enhanced Host Controller - 3A3C | |
| 🖙 🚔 Intel(R) ICH10 Family USB Universal Host Controller - 3A34 | |
| 🛶 Intel(R) ICH10 Family USB Universal Host Controller - 3A35 | |
| 🛶 Intel(R) ICH10 Family USB Universal Host Controller - 3A36 | |
| 🛶 Intel(R) ICH10 Family USB Universal Host Controller - 3A37 | |
| 🛶 Intel(R) ICH10 Family USB Universal Host Controller - 3A38 | |
| 🛶 Intel(R) ICH10 Family USB Universal Host Controller - 3A39 | |
| 🛶 Standard-USB-Hub | |
| USB 2.0-Root-Hub | |
| USB 2.0-Root-Hub | |
| 🗧 – 🙀 USB Serial Converter A | |
| USB Serial Converter B | |
| USB-Druckerunterstützung | |
| USB-Massenspeicher | |
| SB-Root-Hub | |
| SB-Root-Hub | |
| USB-Root-Hub | |
| SB-Root-Hub | |
| USB-Root-Hub | |
| USB-Root-Hub | |
| USB-Verbundgerät | |
| USB-Verbundgerät | • |
| | |
| j j | J |

Step 3: Connect a **DVK-2** mainboard via the blue RJ45 cable to the **AXDBG** debug adapter.

The base system is now ready for testing e.g. with **AX-MicroLab** for a start.

Note: If powered via the debug adapter the user can see voltage changes on the mainboard if the current consumption changes. This is caused by a cascade of two 3.3V regulators, on the AXSDB and on the mainboard. The effect is visible in changes of the back light of the LCD display. It does not occur with battery powered operation. This is fixed on the newest versions of the DVK-2.

9. Operation with AX-MicroLab

AX-MicroLab is a great starting point for micro-controller code development with the **AX8052F1xx** family of micro-controllers. It creates a framework of code for different applications and helps to configure the functions of the micro-controller.

| AX_MicroLab ¥1.5 AX80 AX AX | 952F100 - t1 | | | |
|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------------------|-------------------------------|-------------------------------|-----------------------------|
| Project Help About! | | | | |
| Pin | 27 GPI PA4 | Pin 25 🚽 GPI PA2 | Pin 23 🛢 GPI PA0 🧲 Pull-Up | _ |
| Pin | 28 🛱 GPI PA5 C Pull-Up | Pin 26 GPI PA3 | Pin 24 👮 GPI PA1 🤆 Pull-Up | - |
| Pin 1 RIRQ | 0 | 28 27 26 25 24 2 | 3 22 | RESET_N Pin 21 |
| Pin 3 RM09 | 51 | | 21 | DBG_EN Pin 20 |
| Pin 4 BMIS | 2 | | 20 | GPI PB7 Pin 19 |
| Pin 5 BCIK | 4 | AX8052F1 | 00 18 | GPI PB6 Pin 18 |
| Pind Rect | 5 | | 17 | GPI PB5 Pin 17 GPI PUI |
| | 6 7 | | 16 15 | GPLPB4 Pin 16 |
| Pin / Pin / | | | 3 14 | GPI PB3 Pin 15 |
| Pin | 8 \$GPI PC3 | Pin 10 1 GPI PC1 | Pin 13 GPI PB1 | C Pull-Up |
| Pin | C Pull-Up g d∰ GPI PC2 | C Pull-Up Pin 11 🔮 GPI PC0 | C Pull-Up Pin 14 🚽 GPI PB2 | 2 |
| | C Pull-Up | C Pull-Up Pin 12 🗳 GPI PB0 | C Pull-Up | (c) AXSEM AG, 2012 |
| | | C Pull-Up | | distribution prohibited |
| Hardware Configura | tion System <u>C</u> lock | Badio Chip Configuration | | |
| C Debug Link enable C LCD | <u>E</u> xternal IRQ | Power Configuration | | DVK-2 Board Examples |
| Functions Configura | ltion | | | |
| Iner Contiguistion | Analog Comparator | SPI Carligutation | nfiguration [PwM C | orliguation Input Capture |
| Signa Qalta | <u>ADC, Temperature</u> | t VDDIO Measure Wakes | p Timer Watch | dog Timer [IPQ Contiguemon] |
| EMA Consideration | | | | |
| Accept Hardware Con | liguration | Sove and C | ompute Registers | Jpen AXCode::Blocks |

AX-MicroLab has a "DVK-2 Board" button (see arrow) to create a **DVK-2** compatible configuration quickly. For details look in the **AX-MicroLab** documentation, which is available at <u>www.axsem.com</u>.

10. Pre-installed PER-test

The DVK-2 mainboards come with a pre-installed software for tests with DVK-2 radio add-on modules. The pre-installed software recognises most AXSEM radio add-on modules and automatically starts testing packet error rate. One DVK-2 mainboard is programmed with a transmitter the other with a receiver. Just plug in batteries, attach radio modules and set the power supply switch to battery. For longer range use antennas. The transmitter indicates the number of the last transmitted packet, while the receiver indicates the packet error rate (PER), the RSSI and the frequency offset. Walk around and check out for the great distance you can achieve.

Note: The SW does not accurately sense AX5043 modules for 433 and 169 MHz and modules with 48 MHz TCXO. For those modules it is recommended to use AX-RadioLab.

The pre-installed SW running on the main boards can be downloaded from the **AXSEM** web-page <u>www.axsem.com</u>. Open slave.cbp in **AxCode::blocks** to download the receiver and master.cbp to download the transmitter. The project main.cbp allows to toggle between transmitter and receiver by pressing SW4 during power-up.

The radio parameters of this software cannot be changed. It is recommended to use **AX-RadioLab** or **AX-RadioLab51** for in depth testing. With **AX5042** and **AX5031** we recommend **AX-ParamCalc** with **AX-Easy_Packet** as development and testing platform.

10.1. Transmitter side

The transmitter sends a standard **AXSEM** HDLC packet every second.

The LCD display shows:

AXSEM RF: AX50xx 0051 (the last transmitted packet number)

Transmit parameter:

868.3 MHz 100 kbps max power FSK, deviation h=0.5 duty cycle ~1s

10.2. Receiver side

The application on the **AX8052F100** receives the data packets from the transmitter and shows packet error rate, signal strength and frequency offset.

The LCD display shows:

P:0014 O:00397 L:0000 R: -034

P is the number of received packets
L is the number of lost packets
O is the frequency offset in Hz
R is the receiver strength indicator (RSSI) in dBm

Note: The receiver screen updates only if a valid packet was received.

Note: If the transmitter is reset during the test, the P and L values become invalid.

11. Working with AX-RadioLab

AX-RadioLab is the most advanced development tool for SRD radio applications. It offers a variety of transmitter and receiver options such as

- Periodic transmission of packets timed with the RC oscillator of the AX5043
- Periodic transmission of packets timed with the 32 kHz XTAL oscillator of the MCU
- Transmit on push-button
- Wake-on-radio reception with programmable wake-up interval
- Synchronous transmit and receive with programmable wake-up interval
- Optional acknowledge package send for all modes

| out | | | | | | |
|------------------------------------------------------------------------------------------------|-------------------------------------------|-----------------------------------------------|--|--|--|--|
| K TX periodic (LPOSC), every € 100.00 s | RX C RX continous (RX always on) | (c) AXSEM AG, 2013 distribution prohibited | | | | |
| TX periodic (LPXOSC), every | Wake on Radio Check for signal every | 1000 ms | | | | |
| TX on demand (press button SW/04, south-east) | C RX Synchronized with TX Sync. Tenang | | | | | |
| | Send acknowledge | | | | | |
| Approximate current consumption | | | | | | |
| <pre>< peak current [mA]</pre> | RX peak current [mA] | | | | | |
| K average current [uA] 605 | RX average current [uA] 6 (idle curre | nt) | | | | |
| (TX currents are estimated assuming a matching network optimized for the selected power level) | | | | | | |
| | | | | | | |
| itions | | | | | | |
| Pin Configuration Calculate Registers | Save & Write Output Basic & Regulatory Te | sts | | | | |
| PHY Expert Settings | Edit MASTER Compile & Download MAt | STER | | | | |
| changes active | | | | | | |
| Framing | Edit SLAVE Compile & Download SL | AVE | | | | |

On top of this **AX-RadioLab** also allows to configure the packet format, as well as the PHY.

Since **AX-RadioLab** is a source code generator the developer inherits the entire C-code necessary to run his application. This makes radio code development quick and reliable.

AX-RadioLab also estimates the power consumption of the radio device. For most modes the **AX8052F100** power consumption is negligible. Average and peak currents are indicated for transmitter and receiver.

12. Working with AxCode::Blocks

AxCode::Blocks is the graphical IDE for code development on **AXSEM** microcontrollers. It enables the developer to access all the degugging features of the AXSEM micro-controllers, in particular:

- Unlimited³ number of break points
- Access to all AX8052F1xx MCU registers as well as to all AXSEM radio chip registers
- Debug link UART in a window for debugging printf-style without the need of extra hardware
- **SDCC** C-compiler pre-installed and ready to go

³ Limited only by memory size

13. Installation of Software

Download AX8052-IDE-setup.exe.zip, AX-RadioLab.zip and AX-MicroLAB.zip from <u>www.axsem.com</u>.

The AX8052-IDE-setup.exe.zip has to be unzipped and then comes as windows installer executable, which needs administrator privileges to be executed. It installs the **AxCode::Blocks** IDE, the **SDCC** C-compiler, the LibMF support libraries, the **AXSDB** command line debugger and low level drivers. Check the support page of <u>www.axsem.com</u> for tips and tricks and problem solving with the installation of the IDE, especially in conjunction with 64-bit versions of operating systems.

The **AX-RadioLab** and **AX-MicroLAB** come as zip-file which have to be unzipped into a temporary directory. Run setup.exe in that temporary directory. The temporary directory can be deleted after installation.

Note: The installer requests reboots of the computer, which is not necessary.

Note: If you already have a version installed, the setup.exe first uninstalls the existing version of the software. You have to run the installer a second time to install then the software.

The default location for **AX-RadioLab**, **AX-MicroLAB**, **AxCode::Blocks** and AXSDB is in the *c:\program files\AXSEM* directory. The **SDCC** C-compiler is typically installed directly into the *c:\program* files directory. LibMF and the supporting libraries are installed in the **AXSDB** directory.

Please check the respective documentations for details.

14. Different SW packages playing together

AxCode::Blocks IDE

The AX8052-IDE-setup.exe contains all tools for developing and debugging C-source code applications for the **AX8052F1xx** family of micro-controllers.

The **AX-RadioLab** GUI is a code generator, which creates C-code, which can then be compiled and downloaded directly from the **AX-RadioLab** or it can be edited, modified and debugged in the **AxCode::Blocks** IDE.

The user normally starts in the **AX-RadioLab** GUI and creates all necessary configuration and program files. The project can optionally be viewed and modified in **AxCode::Blocks**. Be aware that there are no protective mechanisms in the **AxCode::Blocks** to prevent mal function of modified C-code.

For details look into the documentation of **AX-RadioLab** and **AxCode::Blocks**.

15. Glossary

AxCode::Blocks: An IDE for AX8052F1xx micro-controllers

AX-RadioLab: An application C-code and settings generator for the **AX5043** radio chip running on the **AX8052F100** micro-controller

AX-RadioLab: An application C-code and settings generator for the **AX5051** radio chip running on the **AX8052F100** micro-controller

AX-MicroLab: A C-code and settings generator for **AX8052F1xx** family of microcontrollers

Debug link: The interface between **AXDBG** debug adapter and **AX8052F1xx** microcontrollers

AXDBG: The USB debug adapter for **AX8052F1xx** micro-controllers

AXSDB: A command line debugger using **AXDBG**. It is also fully suitable for use in mass production

SDCC: A C-compiler for AX8052F1xx micro-controllers

Main board: The DVK-2 development board with AX8052F100 micro-controller and LCD display

DVK-2: The AXSEM development system for micro-controller and radio applications

DVK-2 base-kit: Two mainboards, an AXDBG and cables

DVK-2 add-on: Set of two modules with AXSEM radio chips

add-on-kit or add-on: Set of two modules with AXSEM radio chips

Radio module: Plug-in module for mainboards with AXSEM radio chips

AX8052F100: The **AXSEM** ultra-low-power micro-controller used on the **DVK-2** mainboards

LibMF: Support libraries for the **AX8052F100** and the **DVK-2**. It normally if not otherwise mentioned includes the libraries libax5031, libax5031, libax5042, libax5051, libax5043, which are in separate directories

LibMFCrypto: A library which enables the use of the AES or DES functionality of the AX8052F1xx micro-controllers

AX-Easy_packet: Software example programs for AX5042, AX5051, AX5151, AX5031, AX5131, AX50424

Pre-installed PER test: A software which is programmed on the DVK-2 mainboards

AX-ParamCalc: GUI application to create configuration files for AX5042, AX5051, AX5151, AX5031, AX5131, AX50424. The configuration files are compatible with AX-Easy_Packet but can also be used in customer specific code

16. Contact Information

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