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Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



FEATURES

- Advanced Bus Converter industry standard quarter-brick with digital PMBus interface
- Optional standard five pin quarter-brick
- High efficiency, 95.2% (typ)
- Superior thermal performance
- Fast dynamic response
- ± 2% Vout setting accuracy
- 2250Vdc input to output isolation voltage (functional)
- Optional baseplate (B option)
- PMBus™ Revision 1.2 compliant
- Voltage droop load sharing for parallel operation
- Certified to UL/EN/IEC 60950-1, CAN/CSA-C22.2 No. 60950-1, 2nd Edition, safety approvals and EN55022/CISPR22 standards

PRODUCT OVERVIEW

Murata Power Solutions is introducing the first in a series of digitally controlled DC-DC converters that are based on a 32-bit ARM processor. The DBQ series provides a fully regulated, digitally controlled DC output in a ¼-brick format that will support the evolving Advanced Bus Converter (ABC) industry standard footprint for isolated board mounted power modules. The DBQ series supports advances in power conversion technology including a digital interface supporting the PMBus protocol for communications to power modules.

The DBQ series also incorporates a “droop” load sharing option that allows connecting two or more

units together in parallel for demanding power-hungry applications or to provide redundancy in high reliability applications. The converter also offers high input to output isolation of 2250 VDC as required for Power over Ethernet (PoE) applications.

The DBQ series is suitable for applications covering MicroTCA, servers and storage applications, networking equipment, telecommunications equipment, Power over Ethernet (PoE), fan trays, wireless networks, wireless pre-amplifiers, and industrial and test equipment, along with other applications requiring a regulated 12V.

Power Management (PMBus Options)

- Configurable soft-start/stop
- Configurable output voltage (Vout) and voltage margins (Margin low and Margin high)
- Configurable protection limits for OVP, input over voltage, input under voltage, over current, on/off, and temperature.
- Module Status monitor Vout, Iout, Vin, Temp, Power good, and On/Off.
- System status monitor (Vout, Iout, Vin and Temp over time)

Applications

- Distributed power architectures
- Intermediate bus voltage applications
- Servers and storage applications
- Network equipment



Architects of
Modern Power

| Performance Specifications Summary and Ordering Guide ① | | | | | | | | | | | | | | | |
|---|-------------------------|------------------------------|-----------------------|---------------------------|------|-------------------|-----------|-----------------------------|--------------|---------------------------------------|--------------------------------------|------------|----------|-------------------------------|------------------|
| Root Model | Output | | | | | | | Input | | | | Efficiency | | Dimensions (open frame, max.) | |
| | V _{OUT} (V) | I _{OUT} (A, max) | Total Power (W) | Ripple & Noise (mVp-p) | | Regulation (max.) | | V _{IN} Nom. (V) | Range (V) | I _{IN, min.} load (mA) | I _{IN, full} load (A) | | | | |
| | | | | Typ. | Max. | Line (mV) | Load (mV) | | | | Min. | Typ. | (inches) | (mm) | |
| DBQ0360V2 | 3.3 | 60 | 198 | 40 | 150 | 20 | 15 | 48 | 36-75 | 50 | 4.43 | 92.0% | 93.2% | 2.3x1.45x0.48 max. | 58.4x36.83x12.19 |
| DBQ0260V2 | 5 | 60 | 300 | 50 | 150 | 30 | 25 | 48 | 36-75 | 50 | 6.59 | 93.0% | 94.8% | 2.3x1.45x0.48 max. | 58.4x36.83x12.19 |
| DBQ0135V2 | 12 | 35 | 420 | 100 | 150 | 75 | 45 | 48 | 36-75 | 75 | 9.19 | 94% | 95.2% | 2.3x1.45x0.48 max. | 58.4x36.83x12.19 |
| DVQ0360V2 ② | 3.3 | 60 | 198 | 40 | 150 | 20 | 15 | 48 | 36-75 | 50 | 4.43 | 92.0% | 93.2% | 2.3x1.45x0.48 max. | 58.4x36.83x12.19 |
| DVQ0260V2 ② | 5 | 60 | 300 | 50 | 150 | 30 | 25 | 48 | 36-75 | 50 | 6.59 | 93.0% | 94.8% | 2.3x1.45x0.48 max. | 58.4x36.83x12.19 |
| DVQ0135V2 ② | 12 | 35 | 420 | 100 | 150 | 75 | 45 | 48 | 36-75 | 75 | 9.19 | 94% | 95.2% | 2.3x1.45x0.48 max. | 58.4x36.83x12.19 |

① Typical at TA = +25°C under nominal line voltage and full-load conditions. All models are specified with external 1μF and 10μF capacitors in parallel across their output pins.

② DVQ models do not have the PMBus feature.

DIGITAL CONTROL BRICK PART NUMBER FORMAT

| Description | Part Number Structure | | | | | | | | Definition and Options | | | |
|---------------------------------|-----------------------|---|---|---|---|---|---|--|---|--|--|--|
| Product Family | D | X | | | | | | | DB = Full Featured Digital Bus converter, DV = NO PMBus, NO Sense & Trim Pins | | | |
| Form Factor | | | Q | | | | | | Q = Quarter Brick | | | |
| Vout ① | | | 0 | 1 | | | | | 01 = 12Vout, 02 = 5Vout, 03 = 3.3Vout (Without PMBus Vout cannot be changed) | | | |
| Output Current | | | | 3 | 5 | | | | Max Iout in Amps | | | |
| Vin Range | | | | | V | 2 | | | V2 = 36-75V | | | |
| Logic ① | | | | | | N | | | N = Negative, P = Positive | | | |
| Pin Length ② | | | | | | X | | | 1 = 0.110" (cut), 2 = 0.145"(cut), Omit for standard shown in the mechanical drawings | | | |
| Mechanical Configuration | | | | | | B | | | B = Baseplate, Omit for Open Frame (Standard Configuration) | | | |
| Load Sharing | | | | | | S | | | S = Load Sharing, Omit for Standard (Standard Configuration) | | | |
| Specific Customer Configuration | | | | | | X | X | | Customer Code, Omit for Standard | | | |
| RoHS | | | | | | | C | | RoHS 6/6 Compliant | | | |

① PMBus Configurable

② Minimum order quantity is required. Samples available with standard pin length only.

Note: Some model number combinations may not be available. See website or contact your local Murata sales representative.

Part Number Example

DBQ0135V2NBSC = Full Featured Digital 1/4 Brick, 12Vout, 35A, 36-75Vin, Negative logic, Baseplate, Load Sharing, RoHS 6/6 compliant

EVALUATION BOARD AVAILABLE FROM MURATA POWER SOLUTIONS

| Part Number | Application Note | USB Adaptor Part Number | GUI Software Application Note |
|-------------|--|-------------------------|--|
| MPS-TD001 | www.murata-ps.com/data/apnotes/dcan-63.pdf | MPS-AD001 | www.murata-ps.com/data/apnotes/dcan-63.pdf |

Contact your local Murata sales representative for ordering details.

DBQ0360V2: 3.3V/60A FUNCTIONAL SPECIFICATIONS (VOLTAGE APPLIED TO SCL SDA SMBALERT AND ON/OFF 2, MIN: -0.3V MAX: 3.6V)

| ABSOLUTE MAXIMUM RATINGS | Conditions ① | Minimum | Typical/Nominal | Maximum | Units |
|---------------------------|---|---------|-----------------|---------|-------|
| Input Voltage, Continuous | | 0 | | 80 | Vdc |
| Input Voltage, Transient | 100 mS max. duration | | | 100 | Vdc |
| Isolation Voltage | Input to output | | | 2250 | Vdc |
| On/Off Remote Control | Power on, referred to -Vin | 0 | | 13.5 | Vdc |
| Output Power | | 0 | | 200 | W |
| Output Current | Current-limited, no damage, short-circuit protected | 0 | | 60 | A |
| Storage Temperature Range | Vin = Zero (no power) | -55 | | 125 | °C |

Absolute maximums are stress ratings. Exposure of devices to greater than any of these conditions may adversely affect long-term reliability. Proper operation under conditions other than those listed in the Performance/Functional Specifications Table is not implied nor recommended.

| INPUT | | | | | |
|---|---|----|------------|------|-------------------------|
| Operating voltage range (V2) | | 36 | 48 | 75 | Vdc |
| Start-up threshold | (Default, configurable via PMBus) | 33 | 34 | 35 | Vdc |
| Undervoltage shutdown | (Default, configurable via PMBus) | 31 | 32 | 34 | Vdc |
| Oversupply shutdown | (Default, configurable via PMBus) | NA | NA | NA | Vdc |
| Oversupply Recover | (Default, configurable via PMBus) | NA | NA | NA | Vdc |
| Internal Filter Type | | Pi | | | |
| External Input fuse | | 20 | | | A |
| Input current | | | | | |
| Full Load Conditions | Vin = nominal | | 4.43 | 4.48 | A |
| Low Line input current | Vin = minimum | | 5.79 | 5.97 | A |
| Inrush Transient | Vin = 48V. | | TBD | TBD | A ² -Sec. |
| Short Circuit input current | | | 0.05 | 0.1 | A |
| No Load input current | Iout = minimum, unit=ON | | 50 | 100 | mA |
| Shut-Down input current(Off, UV, OT) | | | TBD | TBD | mA |
| Back Ripple Current | | | TBD | TBD | mAp-p |
| GENERAL and SAFETY | | | | | |
| Efficiency | Vin=48V, full load (V2) | 92 | 93.2 | | % |
| | Input to output | | | 2250 | Vdc |
| Isolation Voltage | Input to Baseplate | | | 1500 | Vdc |
| | Output to Baseplate | | | 1500 | Vdc |
| Insulation Safety Rating | | | functional | | |
| Isolation Resistance | | | 10 | | MΩ |
| Isolation Capacitance | | | 1000 | | pF |
| Safety | Certified to UL-60950-1, CSA-C22.2 No.60950-1, IEC/EN60950-1, 2nd edition | | Yes | | |
| Calculated MTBF | Per Telcordia SR-332, Issue 2, Method 1, Class 1, Ground Fixed, Tcase=+25°C | | 1800 | | Hours x 10 ³ |
| DYNAMIC CHARACTERISTICS | | | | | |
| Switching Frequency (Configurable via PMBus) | | | | | |
| Fixed Frequency Control | | | 150 | | KHz |
| Variable Frequency Control (Default) | | | NA | | KHz |
| Turn On Time (Default, Configurable via PMBus) | | | | | |
| Vin On to Vout Regulated | | | | 45 | μS |
| Remote On to Vout Regulated | | | | 25 | μS |
| Vout Rise Time (Default, Configurable via PMBus) | | | | | |
| From 0%~100% | | | 20 | | μS |
| Vout Fall Time of Regulated Off (Default, Configurable via PMBus) | | | | | |
| From 100%~0% | | | 13 | | μS |
| Dynamic Load Response | 50-75-50%, 1A/us,within 1% of Vout | | TBD | TBD | μSec |
| Dynamic Load Peak Deviation | same as above | | TBD | TBD | mV |

DBQ0360V2 FUNCTIONAL SPECIFICATIONS (CONT.)

| FEATURES and OPTIONS | Conditions ① | Minimum | Typical/Nominal | Maximum | Units |
|---|---|---------|-----------------|---------|---------------|
| Remote On/Off Control | | | | | |
| Primary On/Off control (designed to be driving with an open collector logic, Voltages referenced to -Vin) | | | | | |
| “P” suffix: | | | | | |
| Positive Logic, ON state | ON = pin open or external voltage | 3.5 | | 13.5 | V |
| Positive Logic, OFF state | OFF = ground pin or external voltage | 0 | | 0.8 | V |
| Control Current | open collector/drain | | 0.1 | 0.2 | mA |
| “N” suffix: | | | | | |
| Negative Logic, ON state | ON = ground pin or external voltage | -0.1 | | 0.8 | V |
| Negative Logic, OFF state | OFF = pin open or external voltage | 3.5 | | 13.5 | V |
| Control Current | open collector/drain | | 0.1 | 0.2 | mA |
| OUTPUT | | | | | |
| Total Output Power | | 0 | 198 | 200 | W |
| Voltage | | | | | |
| Setting Accuracy | At 100% load, no trim, all conditions | 3.27 | 3.3 | 3.33 | Vdc |
| Output Adjust Range | Configurable via PMBus | 2.2 | | 3.6 | Vdc |
| Overvoltage Protection | Configurable via PMBus | | 4 | | Vdc |
| Voltage Droop | Default, configurable via PMBus | | 0 | | mΩ |
| Current | | | | | |
| Output Current Range | | 0 | 60 | 60 | A |
| Minimum Load | | | No minimum load | | |
| Current Limit Inception ② | 98% of Vnom., after warmup, Configurable via PMBus | | | 70 | A |
| Short Circuit | | | | | |
| Short Circuit Current | Hiccup technique, autorecovery within 1% of Vout | | 0.4 | 1 | A |
| Short Circuit Duration (remove short for recovery) | Output shorted to ground, no damage | | Continuous | | |
| Short circuit protection method, default response, configurable via PMBus | Hiccup current limiting | | Non-latching | | |
| Regulation ③ | | | | | |
| Line Regulation (V2) | Vin = 36-75, Vout = nom., full load | | | 20 | mV |
| Load Regulation (V2) | Iout = min. to max., Vin = nom. | | | 15 | mV |
| Ripple and Noise | 5 Hz- 20 MHz BW, Cout = 1µF paralleled with 10µF | | 40 | 150 | mV pk-pk |
| Temperature Coefficient | At all outputs | | 0.01 | 0.02 | % of Vnom./°C |
| Maximum Output Capacitance | Low ESR | | | 10,000 | µF |
| Power Good—Negative logic (Configurable via PMBus) | | | | | |
| Power good high stage voltage | | 2.4 | | 3.6 | Vdc |
| Power good low stage voltage | | 0 | | 0.4 | Vdc |
| Out voltage for power good off triggering | Configurable via PMBus | 2 | 2.3 | 2.5 | Vdc |
| Out Voltage for power good on triggering | Configurable via PMBus | 2.6 | 2.8 | 3 | Vdc |

DBQ0360V2 FUNCTIONAL SPECIFICATIONS (CONT.)

| PMBus | Conditions ① | Minimum | Typical/Nominal | Maximum | Units |
|---|---|---------|-----------------|---------|-------|
| PMBus GENERAL | | | | | |
| PMBus REV. 1.2. SMBALERT# is supported. PEC is supported. Linear data format used. | | | | | |
| Bus speed | | | | 400 | kHz |
| Logic high input | | 2 | | 3.3 | Vdc |
| Logic low input | | 0 | | 0.8 | Vdc |
| Logic high output | | 2.4 | | | Vdc |
| Logic low output | | | | 0.4 | Vdc |
| PMBus ADDRESSING | | | | | |
| If the calculated PMBus address is 0d, 11d or 12d, SA0 or SA1 lefts open, default PMBus address 119d is assigned instead. PMBus address = 8x(SA0 value) + (SA1 value). SA0, SA1 value VS resistor connected to GND. | | | | | |
| 0 | | | 10 | | kΩ |
| 1 | | | 22 | | kΩ |
| 2 | | | 33 | | kΩ |
| 3 | | | 47 | | kΩ |
| 4 | | | 68 | | kΩ |
| 5 | | | 100 | | kΩ |
| 6 | | | 150 | | kΩ |
| 7 | | | 220 | | kΩ |
| PMBus MONITORING ACCURACY | | | | | |
| VIN_READ | | -2 | | 2 | % |
| VOUT_READ | | -1 | | 1 | % |
| IOUT_READ (> = 10A) | | -5 | | 5 | % |
| IOUT_READ (<10A) | | -1 | | 1 | A |
| TEMP_READ | | 5 | | 5 | °C |
| DIGITAL INTERFACE SPECIFICATIONS (PMBUS MONITORING & FUNCTIONAL DESCRIPTION) | | | | | |
| Fault Protection Specifications | | | | | |
| Output Voltage, Over Voltage protection, OVP | Factory default | | 4 | | V |
| VOUT_OV_FAULT_LIMIT, Configurable via PMBus | >VOUT_OV_WARM_LIMIT | 3 | | 4 | V |
| | Restart delay (default, Configurable via PMBus) | | 500 | | ms |
| Input Voltage, Input Over Voltage Protection | Factory default | | NA | | V |
| | Setpoint accuracy | | | | % |
| VIN_OV_FAULT_LIMIT (Configurable via PMBus) ④ | >VIN_OV_WARM_LIMIT | | NA | | V |
| | Restart delay (default, Configurable via PMBus) | | | | ms |
| Input Voltage, Input Under Voltage Protection, UVLO | Factory default | | 32 | | V |
| | Setpoint accuracy | -2 | | 2 | % |
| VIN_UV_FAULT_LIMIT (Configurable via PMBus) ④ | <VIN_UV_WARM_LIMIT <VIN_OV_FAULT_LIMIT | | 32 | 75 | V |
| | Restart delay (default, Configurable via PMBus) | | 100 | | ms |
| Over Current Protection, OCP | Setpoint accuracy (Io) | -2.5 | | 2.5 | % |
| | (factory default) | | 70 | | A |
| IOUT_OC_FAULT_LIMIT (Configurable via PMBus) ④ | >IOUT_OC_WARN_LIMIT | 0 | | 75 | A |
| | Restart delay (default, Configurable via PMBus) | | 100 | | ms |
| Over Temperature Protection, OTP | OT_FAULT_LIMIT (factory default) | | 120 | | °C |
| OT_FAULT_LIMIT (Configurable via PMBus) ④ | >OT_WARM_LIMIT | 0 | 30 | 150 | °C |
| | OTP accuracy (factory default) | 5 | | 5 | °C |
| | Restart delay (default, Configurable via PMBus) | | 500 | | ms |

DBQ0360V2 FUNCTIONAL SPECIFICATIONS (CONT.)

| MECHANICAL | Conditions ① | Minimum | Typical/Nominal | Maximum | Units |
|--|-------------------------------|-------------------|----------------------|---------|----------|
| Outline Dimensions (open frame) (Please refer to outline drawing) | L x W x H | 2.3 x 1.45 x 0.48 | 58.4 x 36.83 x 12.19 | | Inches |
| Outline Dimensions (with baseplate) | | 2.3 x 1.45 x 0.50 | 58.4 x 36.80 x 12.7 | | mm |
| | | | | | Inches |
| Weight (open frame) | | TBD | | | Ounces |
| | | | | | Grams |
| Weight (with baseplate) | | TBD | | | Ounces |
| | | | | | Grams |
| Through Hole Pin Diameter | | 0.04 & 0.062 | | | Inches |
| | | 1.016 & 1.575 | | | mm |
| Digital Interface Pin Diameter | | 0.02 | | | |
| | | 0.5 | | | |
| Through Hole Pin Material | | Copper alloy | | | |
| TH Pin Plating Metal and Thickness | Nickel subplate | 98.4-299 | | | μ-inches |
| | Gold overplate | 4.7-19.6 | | | μ-inches |
| ENVIRONMENTAL | | | | | |
| Operating Ambient Temperature Range | with derating | -40 | 85 | | °C |
| Operating Baseplate Temperature | | -40 | 110 | | °C |
| Storage Temperature | Vin = Zero (no power) | -55 | 125 | | °C |
| Thermal Protection/Shutdown (with "B" Suffix, default value, Configurable via PMBUS) | Configurable Via PMBus | | 120 | | °C |
| Electromagnetic Interference | External filter required; see | | B | | Class |
| Conducted, EN55022/CISPR22 | emissions performance test. | | | | |
| RoHS rating | | | RoHS-6 | | |

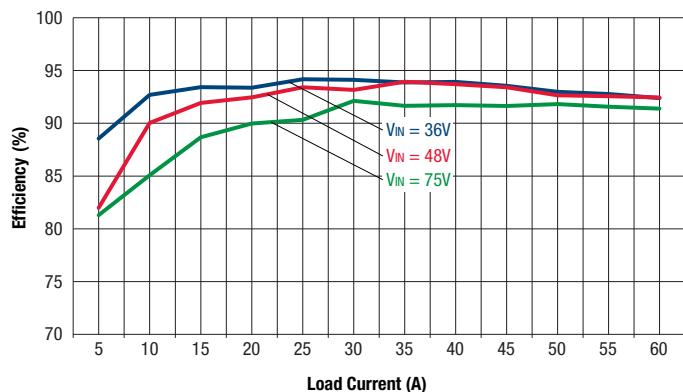
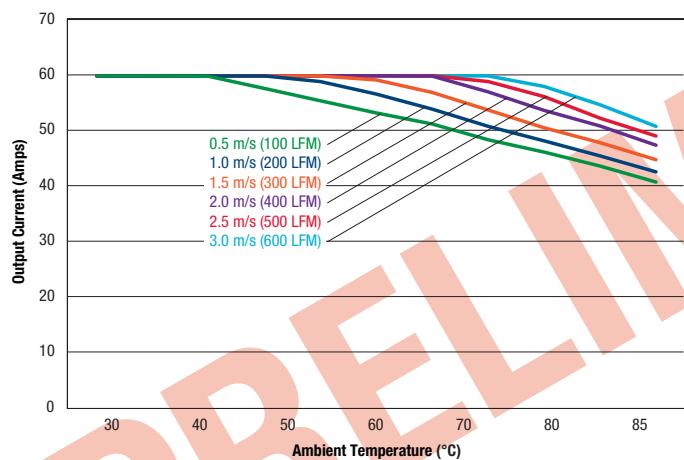
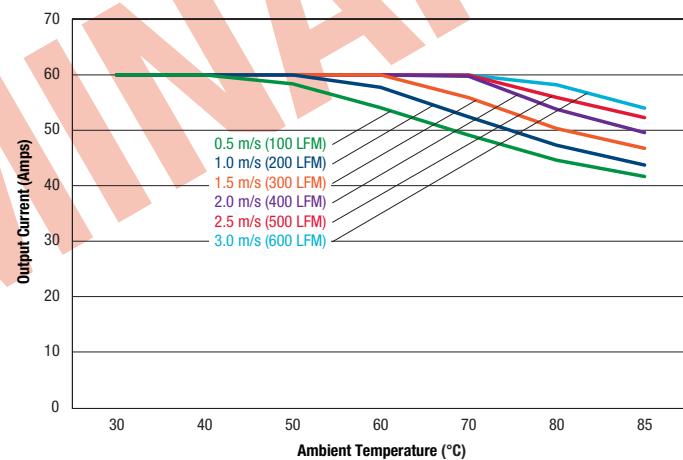
Notes

- ① Typical at TA = +25°C under nominal line voltage and full-load conditions. All models are specified with external 1µF and 10µF capacitors in parallel across their output pins. All values are default, unless otherwise noted.
 ② Over-current protection is non-latching with auto recovery (hiccup).

- ③ Regulation specifications describe the output voltage changes as the line voltage or load current is varied from its nominal or midpoint value to either extreme.
 ④ See Operating information section.

PERFORMANCE DATA

Efficiency vs. Line Voltage and Load Current @ +25°C

Maximum Current Temperature Derating with baseplate
(Vin = 48V, airflow from Vin to Vout)Maximum Current Temperature Derating with baseplate
(Vin = 48V, airflow from Vin- to Vin+)

DBQ0260V2: 5V/60A FUNCTIONAL SPECIFICATIONS (VOLTAGE APPLIED TO SCL SDA SMBALERT AND ON/OFF 2, MIN: -0.3V MAX: 3.6V)

| ABSOLUTE MAXIMUM RATINGS | Conditions ① | Minimum | Typical/Nominal | Maximum | Units |
|--|---|---------|-----------------|---------|-------------------------|
| Input Voltage, Continuous | | 0 | | 80 | Vdc |
| Input Voltage, Transient | 100 mS max. duration | | | 100 | Vdc |
| Isolation Voltage | Input to output | | | 2250 | Vdc |
| On/Off Remote Control | Power on, referred to -Vin | 0 | | 13.5 | Vdc |
| Output Power | | 0 | | 303 | W |
| Output Current | Current-limited, no damage, short-circuit protected | 0 | | 60 | A |
| Storage Temperature Range | Vin = Zero (no power) | -55 | | 125 | °C |
| Absolute maximums are stress ratings. Exposure of devices to greater than any of these conditions may adversely affect long-term reliability. Proper operation under conditions other than those listed in the Performance/Functional Specifications Table is not implied nor recommended. | | | | | |
| INPUT | | | | | |
| Operating voltage range (V2) | | 36 | 48 | 75 | Vdc |
| Start-up threshold | (Default, configurable via PMBus) | 33 | 34 | 35 | Vdc |
| Undervoltage shutdown | (Default, configurable via PMBus) | 31 | 32 | 34 | Vdc |
| Oversupply shutdown | (Default, configurable via PMBus) | NA | NA | NA | Vdc |
| Oversupply Recover | (Default, configurable via PMBus) | NA | NA | NA | Vdc |
| Internal Filter Type | | PI | | | |
| External Input fuse | | 20 | | | A |
| Input current | | | | | |
| Full Load Conditions | Vin = nominal | | 6.59 | 6.72 | A |
| Low Line input current | Vin = minimum | | 8.77 | 8.86 | A |
| Inrush Transient | Vin = 48V. | | TBD | TBD | A ² -Sec. |
| Short Circuit input current | | | 0.05 | 0.1 | A |
| No Load input current | Iout = minimum, unit=ON | | 50 | 100 | mA |
| Shut-Down input current(Off, UV, OT) | | | TBD | TBD | mA |
| Back Ripple Current | | | TBD | TBD | mAp-p |
| GENERAL and SAFETY | | | | | |
| Efficiency | Vin=48V, full load (V2) | 93 | 94.8 | | % |
| Isolation Voltage | Input to output | | | 2250 | Vdc |
| | Input to Baseplate | | | 1500 | Vdc |
| | Output to Baseplate | | | 1500 | Vdc |
| Insulation Safety Rating | | | functional | | |
| Isolation Resistance | | | 10 | | MΩ |
| Isolation Capacitance | | | 1000 | | pF |
| Safety | Certified to UL-60950-1, CSA-C22.2 No.60950-1, IEC/EN60950-1, 2nd edition | | Yes | | |
| Calculated MTBF | Per Telcordia SR-332, Issue 2, Method 1, Class 1, Ground Fixed, Tcase=+25°C | | 1800 | | Hours x 10 ³ |
| DYNAMIC CHARACTERISTICS | | | | | |
| Switching Frequency (Configurable via PMBus) | | | | | |
| Fixed Frequency Control | | | 150 | | KHz |
| Variable Frequency Control (Default) | | | NA | | KHz |
| Turn On Time (Default, Configurable via PMBus) | | | | | |
| Vin On to Vout Regulated | | | | 45 | mS |
| Remote On to Vout Regulated | | | | 25 | mS |
| Vout Rise Time (Default, Configurable via PMBus) | | | | | |
| From 0%~100% | | | 20 | | mS |
| Vout Fall Time of Regulated Off (Default, Configurable via PMBus) | | | | | |
| From 100%~0% | | | 13 | | mS |
| Dynamic Load Response | 50-75-50%, 1A/us,within 1% of Vout | | TBD | TBD | μSec |
| Dynamic Load Peak Deviation | same as above | | TBD | TBD | mV |

DBQ0260V2 FUNCTIONAL SPECIFICATIONS (CONT.)

| FEATURES and OPTIONS | Conditions ① | Minimum | Typical/Nominal | Maximum | Units |
|---|---|---------|-----------------|---------|---------------|
| Remote On/Off Control | | | | | |
| Primary On/Off control (designed to be driving with an open collector logic, Voltages referenced to -Vin) | | | | | |
| "P" suffix: | | | | | |
| Positive Logic, ON state | ON = pin open or external voltage | 3.5 | | 13.5 | V |
| Positive Logic, OFF state | OFF = ground pin or external voltage | 0 | | 0.8 | V |
| Control Current | open collector/drain | | 0.1 | 0.2 | mA |
| "N" suffix: | | | | | |
| Negative Logic, ON state | ON = ground pin or external voltage | -0.1 | | 0.8 | V |
| Negative Logic, OFF state | OFF = pin open or external voltage | 3.5 | | 13.5 | V |
| Control Current | open collector/drain | | 0.1 | 0.2 | mA |
| OUTPUT | | | | | |
| Total Output Power | | 0 | 300 | 303 | W |
| Voltage | | | | | |
| Setting Accuracy | At 100% load, no trim, all conditions | 4.95 | 5 | 5.05 | Vdc |
| Output Adjust Range | Configurable via PMBus | 3 | | 5.5 | Vdc |
| Overvoltage Protection | Configurable via PMBus | | 6 | | Vdc |
| Voltage Droop | Default, configurable via PMBus | | 0 | | mΩ |
| Current | | | | | |
| Output Current Range | | 0 | 60 | 60 | A |
| Minimum Load | | | No minimum load | | |
| Current Limit Inception ② | 98% of Vnom., after warmup, Configurable via PMBus | | | 70 | A |
| Short Circuit | | | | | |
| Short Circuit Current | Hiccup technique, autorecovery within 1% of Vout | | 0.4 | 1 | A |
| Short Circuit Duration (remove short for recovery) | Output shorted to ground, no damage | | Continuous | | |
| Short circuit protection method, default response, configurable via PMBus | Hiccup current limiting | | Non-latching | | |
| Regulation ③ | | | | | |
| Line Regulation (V2) | Vin = 36-75, Vout = nom., full load | | | 30 | mV |
| Load Regulation (V2) | Iout = min. to max., Vin = nom. | | | 25 | mV |
| Ripple and Noise | 5 Hz- 20 MHz BW, Cout = 1µF paralleled with 10µF | | 50 | 150 | mV pk-pk |
| Temperature Coefficient | At all outputs | | 0.01 | 0.02 | % of Vnom./°C |
| Maximum Output Capacitance | Low ESR | | | 10,000 | µF |
| Power Good—Negative logic (Configurable via PMBus) | | | | | |
| Power good high stage voltage | | 2.4 | | 3.6 | Vdc |
| Power good low stage voltage | | 0 | | 0.4 | Vdc |
| Out voltage for power good off triggering | Configurable via PMBus | 3 | 3.5 | 4 | Vdc |
| Out Voltage for power good on triggering | Configurable via PMBus | 3.75 | 4.25 | 4.75 | Vdc |

DBQ0260V2 FUNCTIONAL SPECIFICATIONS (CONT.)

| PMBus | Conditions ① | Minimum | Typical/Nominal | Maximum | Units |
|---|---|------------|-----------------|-----------|--------|
| PMBus GENERAL | | | | | |
| PMBus REV. 1.2. SMBALERT# is supported. PEC is supported. Linear data format used. | | | | | |
| Bus speed | | | | 400 | kHz |
| Logic high input | | 2 | | 3.3 | Vdc |
| Logic low input | | 0 | | 0.8 | Vdc |
| Logic high output | | 2.4 | | | Vdc |
| Logic low output | | | | 0.4 | Vdc |
| PMBus ADDRESSING | | | | | |
| If the calculated PMBus address is 0d, 11d or 12d, SA0 or SA1 lefts open, default PMBus address 119d is assigned instead. PMBus address = 8x(SA0 value) + (SA1 value). SA0, SA1 value VS resistor connected to GND. | | | | | |
| 0 | | | 10 | | kΩ |
| 1 | | | 22 | | kΩ |
| 2 | | | 33 | | kΩ |
| 3 | | | 47 | | kΩ |
| 4 | | | 68 | | kΩ |
| 5 | | | 100 | | kΩ |
| 6 | | | 150 | | kΩ |
| 7 | | | 220 | | kΩ |
| PMBus MONITORING ACCURACY | | | | | |
| VIN_READ | | -2 | | 2 | % |
| VOUT_READ | | -1 | | 1 | % |
| IOUT_READ (>= 10A) | | -5 | | 5 | % |
| IOUT_READ (<10A) | | -1 | | 1 | A |
| TEMP_READ | | 5 | | 5 | °C |
| DIGITAL INTERFACE SPECIFICATIONS (PMBUS MONITORING & FUNCTIONAL DESCRIPTION) | | | | | |
| Fault Protection Specifications | | | | | |
| Output Voltage, Over Voltage protection, OVP | Factory default | | 6 | | V |
| VOUT_OV_FAULT_LIMIT, Configurable via PMBus | >VOUT_OV_WARM_LIMIT | 3 | | 6 | V |
| | Restart delay (default, Configurable via PMBus) | | 500 | | μS |
| Input Voltage, Input Over Voltage Protection | Factory default | | NA | | V |
| | Setpoint accuracy | | | | % |
| VIN_OV_FAULT_LIMIT (Configurable via PMBus) ④ | >VIN_OV_WARM_LIMIT | | NA | | V |
| | Restart delay (default, Configurable via PMBus) | | | | μS |
| Input Voltage, Input Under Voltage Protection, UVLO | Factory default | | 32 | | V |
| | Setpoint accuracy | -2 | | 2 | % |
| VIN_UV_FAULT_LIMIT (Configurable via PMBus) ④ | <VIN_UV_WARM_LIMIT <VIN_OV_FAULT_LIMIT | | 32 | 75 | V |
| | Restart delay (default, Configurable via PMBus) | | 100 | | μS |
| Over Current Protection, OCP | Setpoint accuracy (Io) (factory default) | -2.5 70 | | 2.5 75 | % A |
| IOUT_OC_FAULT_LIMIT (Configurable via PMBus) ④ | >IOUT_OC_WARN_LIMIT | 0 | | 75 | A |
| | Restart delay (default, Configurable via PMBus) | | 100 | | μS |
| Over Temperature Protection, OTP | OT_FAULT_LIMIT (factory default) | | 120 | | °C |
| OT_FAULT_LIMIT (Configurable via PMBus) ④ | >OT_WARM_LIMIT | 0 | 30 | 150 | °C |
| | OTP accuracy (factory default) | 5 | | 5 | °C |
| | Restart delay (default, Configurable via PMBus) | | 500 | | μS |

DBQ0260V2 FUNCTIONAL SPECIFICATIONS (CONT.)

| MECHANICAL | Conditions ① | Minimum | Typical/Nominal | Maximum | Units |
|--|-------------------------------|-------------------|----------------------|---------|----------|
| Outline Dimensions (open frame) (Please refer to outline drawing) | L x W x H | 2.3 x 1.45 x 0.48 | 58.4 x 36.83 x 12.19 | | Inches |
| Outline Dimensions (with baseplate) | | 2.3 x 1.45 x 0.50 | 58.4 x 36.80 x 12.7 | | mm |
| Weight (open frame) | | TBD | | | Ounces |
| Weight (with baseplate) | | TBD | | | Grams |
| Through Hole Pin Diameter | | 0.04 & 0.062 | | | Inches |
| Digital Interface Pin Diameter | | 0.02 | 1.016 & 1.575 | | mm |
| Through Hole Pin Material | | 0.5 | Copper alloy | | |
| TH Pin Plating Metal and Thickness | Nickel subplate | 98.4-299 | | | μ-inches |
| | Gold overplate | 4.7-19.6 | | | μ-inches |
| ENVIRONMENTAL | | | | | |
| Operating Ambient Temperature Range | with derating | -40 | | 85 | °C |
| Operating Baseplate Temperature | | -40 | | 110 | °C |
| Storage Temperature | Vin = Zero (no power) | -55 | | 125 | °C |
| Thermal Protection/Shutdown (with "B" Suffix, default value, Configurable via PMBus) | Configurable Via PMBus | | 120 | | °C |
| Electromagnetic Interference | External filter required; see | | | | |
| Conducted, EN55022/CISPR22 | emissions performance test. | | B | | Class |
| RoHS rating | | | RoHS-6 | | |

Notes

- ① Typical at TA = +25°C under nominal line voltage and full-load conditions. All models are specified with external 1µF and 10µF capacitors in parallel across their output pins. All values are default, unless otherwise noted.
 ② Over-current protection is non-latching with auto recovery (hiccup).

- ③ Regulation specifications describe the output voltage changes as the line voltage or load current is varied from its nominal or midpoint value to either extreme.
 ④ See Operating information section.

DBQ0135V2: 12V/35A FUNCTIONAL SPECIFICATIONS (VOLTAGE APPLIED TO SCL SDA SMBALERT AND ON/OFF 2, MIN: -0.3V MAX: 3.6V)

| ABSOLUTE MAXIMUM RATINGS | Conditions ① | Minimum | Typical/Nominal | Maximum | Units |
|---|---|------------|-----------------|---------|-------------------------|
| Input Voltage, Continuous | | 0 | | 80 | Vdc |
| Input Voltage, Transient | 100 mS max. duration | | | 100 | Vdc |
| Isolation Voltage | Input to output | | | 2250 | Vdc |
| On/Off Remote Control | Power on, referred to -Vin | 0 | | 13.5 | Vdc |
| Output Power | | 0 | | 428.4 | W |
| Output Current | Current-limited, no damage, short-circuit protected | 0 | | 35 | A |
| SCL / SDA / SMBALERT / ON/OFF 2 | | -0.3 | | 3.6 | Vdc |
| Storage Temperature Range | Vin = Zero (no power) | -55 | | 125 | °C |
| Absolute maximums are stress ratings. Exposure of devices to greater than any of these conditions may adversely affect long-term reliability. Proper operation under conditions other than those listed in the Performance/Functional Specifications Table is not implied or recommended. | | | | | |
| INPUT | | | | | |
| Operating voltage range (V2) | | 36 | 48 | 75 | Vdc |
| Start-up threshold | (Default, configurable via PMBus) | 33 | 34 | 35 | Vdc |
| Undervoltage shutdown | (Default, configurable via PMBus) | 31 | 32 | 34 | Vdc |
| Internal Filter Type | | Pi | | | |
| External Input fuse | | | 20 | | A |
| Input current | | | | | |
| Full Load Conditions | Vin = nominal | | 9.19 | 9.44 | A |
| Low Line input current | Vin = minimum | | 12.25 | 12.59 | A |
| Inrush Transient | Vin = 48V. | | 1.3 | 2.6 | A ² -Sec. |
| Short Circuit input current | | | 0.05 | 0.1 | A |
| No Load input current | Iout = minimum, unit=ON | | 75 | 112 | mA |
| Shut-Down input current(Off, UV, OT) | | | 20 | 30 | mA |
| Back Ripple Current | | | 20 | 40 | mA _{p-p} |
| GENERAL and SAFETY | | | | | |
| Isolation Voltage | Input to output | | | 2250 | Vdc |
| | Input to Baseplate | | | 1500 | Vdc |
| | Output to Baseplate | | | 1500 | Vdc |
| Insulation Safety Rating | | functional | | | |
| Isolation Resistance | | 10 | | | MΩ |
| Isolation Capacitance | | 1500 | | | pF |
| Safety | Certified to UL-60950-1, CSA-C22.2 No.60950-1, IEC/EN60950-1, 2nd edition | | Yes | | |
| Calculated MTBF | Per Telcordia SR-332, Issue 2, Method 1, Class 1, Ground Fixed, Tcase=+25°C | | 1800 | | Hours x 10 ³ |
| DYNAMIC CHARACTERISTICS | | | | | |
| Switching Frequency (Configurable via PMBus) | | | | | |
| Fixed Frequency Control | | | 175 | | KHz |
| Turn On Time (Default, Configurable via PMBus) | | | | | |
| Vin On to Vout Regulated | | | | 60 | mS |
| Remote On to Vout Regulated | | | | 25 | mS |
| Vout Rise Time (Default, Configurable via PMBus) | | | | | |
| From 0%~100% | | | 20 | | mS |
| Vout Fall Time of Regulated Off (Default, Configurable via PMBus) | | | | | |
| From 100%~0% | | | 20 | | mS |
| Dynamic Load Response | 50-75-50%, 0.1A/us,within 1% of Vout | | 200 | 300 | μSec |
| Dynamic Load Peak Deviation | same as above | | ±300 | ±500 | mV |

DBQ0135V2 FUNCTIONAL SPECIFICATIONS (CONT.)

| FEATURES and OPTIONS | Conditions ① | Minimum | Typical/Nominal | Maximum | Units |
|--|---|-----------------------------|------------------|-----------------------------|---------------|
| Remote On/Off Control | | | | | |
| Primary On/Off control (designed to be driving with an open collector logic, Voltages referenced to -Vin) | | | | | |
| “P” suffix: | | | | | |
| Positive Logic, ON state | ON = pin open or external voltage | 3.5 | | 13.5 | V |
| Positive Logic, OFF state | OFF = ground pin or external voltage | 0 | | 0.8 | V |
| Control Current | open collector/drain | | 0.1 | 0.2 | mA |
| “N” suffix: | | | | | |
| Negative Logic, ON state | ON = ground pin or external voltage | -0.1 | | 0.8 | V |
| Negative Logic, OFF state | OFF = pin open or external voltage | 3.5 | | 13.5 | V |
| Control Current | open collector/drain | | 0.1 | 0.2 | mA |
| Secondary On/Off control (Pull up to 3.3V internally; ignored by default configuration; see technical notes section) Voltages referenced to -Vout) | | | | | |
| “P” suffix: | | | | | |
| Positive Logic, ON state | ON = pin open or external voltage | 1.5 | | 3.3 | V |
| Positive Logic, OFF state | OFF = ground pin or external voltage | 0 | | 0.8 | V |
| Control Current | open collector/drain | | 0.03 | 0.06 | mA |
| “N” suffix: | | | | | |
| Negative Logic, ON state | ON = ground pin or external voltage | 0 | | 0.8 | V |
| Negative Logic, OFF state | OFF = pin open or external voltage | 1.5 | | 3.3 | V |
| Control Current | open collector/drain | | 0.03 | 0.06 | mA |
| Remote Sense Compliance | Sense pins connected externally to respective Vout pins | | | | |
| OUTPUT | | | | | |
| Total Output Power | | 0 | 420 | 428.4 | W |
| Voltage | | | | | |
| Initial Output Voltage (Default, Configurable via PMBus) | @VIN = 48V Iout = 0A temp = 25C, both with/without "S" suffix | 11.990 | | 12.030 | Vdc |
| Output Voltage (Default, Configurable via PMBus) | @All conditions, without "S" suffix, VOUT_DROOP = 0Ω | 11.760 | 12.000 | 12.240 | Vdc |
| Output Voltage (Default, Configurable via PMBus) | @All conditions , with "S" suffix, VOUT_DROOP = 10mΩ | (12.000- Iout*0.01)*0.98 | 12.000-Iout*0.01 | (12.000- Iout*0.01)*1.02 | Vdc |
| Over-Voltage Protection (Default, Configurable via PMBus) | Direct feedback Fault response is configurable via PMBus | 13.80 | 14.40 | 15.60 | Vdc |
| Voltage Droop | Default, Configurable via PMBus | | | | |
| Without "S" suffix | | 0 | | | mΩ |
| With "S" suffix | | 10 | | | mΩ |
| Current | | | | | |
| Output Current Range | | 0 | | 35 | A |
| Minimum Load | | | No minimum load | | |
| Current Limit Inception ② | 90% of Vnom., after warmup, Configurable via PMBus | | 42 | | A |
| Short Circuit | | | | | |
| Short Circuit Current | Hiccup technique, autorecovery within 1% of Vout | | 0.4 | 1 | A |
| Short Circuit Duration (remove short for recovery) | Output shorted to ground, no damage | | Continuous | | |
| Short circuit protection method, default response, configurable via PMBus | Hiccup current limiting | | Non-latching | | |
| Regulation ③ | | | | | |
| Line Regulation (V2) | Vin = 36-75, Vout = nom., full load Vin=nom. | | | 75 | mV |
| Load Regulation (V2) | Without S suffix: Vout@min_load-Vout@max_load With S suffix: Vout@min_load-Vout@max_load-Iout*VOUT_DROOP | | | 45 | mV |
| Ripple and Noise | 5 Hz- 20 MHz BW, Cout = 1μF paralleled with 10μF | | 100 | 150 | mV pk-pk |
| Temperature Coefficient | At all outputs | | 0.01 | 0.02 | % of Vnom./°C |
| Maximum Output Capacitance | Low ESR | | | 10,000 | μF |
| Power Good—Negative logic (Configurable via PMBus) | | | | | |
| Power good high stage voltage | | 2.4 | | 3.6 | Vdc |
| Power good low stage voltage | | 0 | | 0.4 | Vdc |
| Out voltage for power good off triggering | Configurable via PMBus | 8.64 | 9.6 | 10.56 | Vdc |
| Out Voltage for power good on triggering | Configurable via PMBus | 9.72 | 10.8 | 11.88 | Vdc |

DBQ0135V2 FUNCTIONAL SPECIFICATIONS (CONT.)

| PMBus | Conditions ① | Minimum | Typical/Nominal | Maximum | Units |
|---|--------------|---------|-----------------|---------|-------|
| PMBus GENERAL | | | | | |
| PMBus REV. 1.2. SMBALERT# is supported. PEC is supported. Linear data format used. | | | | | |
| Bus speed | | | | 400 | kHz |
| Logic high input | | 2 | | 3.3 | Vdc |
| Logic low input | | 0 | | 0.8 | Vdc |
| Logic high output | | 2.4 | | 3.6 | Vdc |
| Logic low output | | -0.1 | | 0.4 | Vdc |
| PMBus ADDRESSING | | | | | |
| If the calculated PMBus address is 0d, 11d or 12d, SA0 or SA1 lefts open, default PMBus address 119d is assigned instead. PMBus address = 8x(SA0 value) + (SA1 value). SA0, SA1 value VS resistor connected to GND. | | | | | |
| 0 | | | 10 | | kΩ |
| 1 | | | 22 | | kΩ |
| 2 | | | 33 | | kΩ |
| 3 | | | 47 | | kΩ |
| 4 | | | 68 | | kΩ |
| 5 | | | 100 | | kΩ |
| 6 | | | 150 | | kΩ |
| 7 | | | 220 | | kΩ |
| PMBus MONITORING ACCURACY | | | | | |
| VIN_READ | | 2.5 | | 2.5 | % |
| VOUT_READ | | -1 | | 1 | % |
| IOUT_READ | | -1 | | 1 | A |
| TEMP_READ | | -5 | | 5 | °C |

DIGITAL INTERFACE SPECIFICATIONS (PMBUS MONITORING & FUNCTIONAL DESCRIPTION)

| | | | | | |
|---|---|-----|------|------|----|
| Fault Protection Specifications | | | | | |
| Output Voltage, Over Voltage protection, OVP | Factory default | | 14.4 | | V |
| VOUT_OV_FAULT_LIMIT, Configurable via PMBus | >VOUT_OV_WARM_LIMIT | 8.1 | | 15.6 | V |
| | Restart delay (default, Configurable via PMBus) | | 500 | | mS |
| VIN_OV_FAULT_LIMIT (Configurable via PMBus) ④ | >VIN_OV_WARM_LIMIT | | 34 | 110 | V |
| | Restart delay (default, Configurable via PMBus) | | 200 | | mS |
| Input Voltage, Input Under Voltage Protection, UVLO | Factory default | | 32 | | V |
| | Setpoint accuracy | -2 | | 2 | % |
| VIN_UV_FAULT_LIMIT (Configurable via PMBus) ④ | <VIN_UV_WARM_LIMIT <VIN_OV_FAULT_LIMIT | | 32 | 75 | V |
| | Restart delay (default, Configurable via PMBus) | | 200 | | mS |
| Over Current Protection, OCP | Setpoint accuracy (Io) | -3 | | 3 | % |
| | (factory default) | | 42 | | A |
| IOUT_OC_FAULT_LIMIT (Configurable via PMBus) ④ | >IOUT_OC_WARN_LIMIT | 0 | | 50 | A |
| | Restart delay (default, Configurable via PMBus) | | 500 | | mS |
| Over Temperature Protection, OTP | OT_FAULT_LIMIT (factory default) | | 120 | | °C |
| OT_FAULT_LIMIT (Configurable via PMBus) ④ | >OT_WARM_LIMIT | 0 | 30 | 150 | °C |
| | OTP accuracy (factory default) | 5 | | 5 | °C |
| | Restart delay (default, Configurable via PMBus) | | 500 | | mS |

DBQ0135V2 FUNCTIONAL SPECIFICATIONS (CONT.)

| MECHANICAL | Conditions ① | Minimum | Typical/Nominal | Maximum | Units |
|---|--|-------------------|----------------------|---------|----------|
| Outline Dimensions (open frame) (Please refer to outline drawing) | L x W x H | 2.3 x 1.45 x 0.48 | 58.4 x 36.83 x 12.19 | | Inches |
| Outline Dimensions (with baseplate) | | 2.3 x 1.45 x 0.52 | 58.4 x 36.80 x 13.21 | | mm |
| Weight (open frame) | | 1.85 | 52.5 | | Ounces |
| Weight (with baseplate) | | 2.35 | 66.8 | | Grams |
| Through Hole Pin Diameter | | 0.04 & 0.062 | 0.04 & 0.062 | | Inches |
| Digital Interface Pin Diameter | | 0.020 | 0.020 | | mm |
| Through Hole Pin Material | | 0.5 | Copper alloy | | |
| TH Pin Plating Metal and Thickness | Nickel subplate | 98.4-299 | | | μ-inches |
| | Gold overplate | 4.7-19.6 | | | μ-inches |
| ENVIRONMENTAL | | | | | |
| Operating Ambient Temperature Range | with derating | -40 | | 85 | °C |
| Operating Baseplate Temperature | | -40 | | 110 | °C |
| Storage Temperature | Vin = Zero (no power) | -55 | | 125 | °C |
| Thermal Protection/Shutdown (with "B" Suffix, default value, Configurable via PMBus) | Configurable Via PMBus | | 125 | | °C |
| Electromagnetic Interference Conducted, EN55022/CISPR22 | External filter required; see emissions performance test. | | B | | Class |
| RoHS rating | | | RoHS-6 | | |

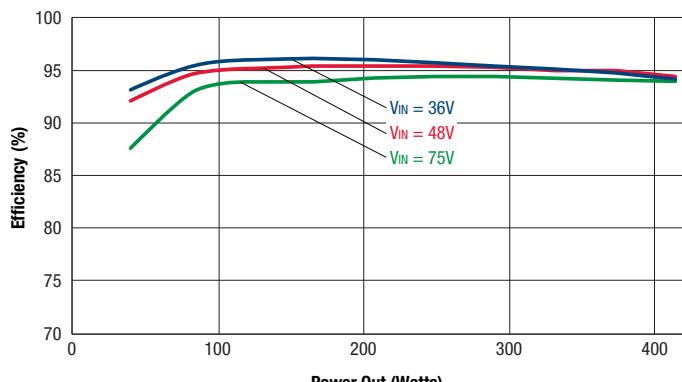
Notes

- ① Typical at TA = +25°C under nominal line voltage and full-load conditions. All models are specified with external 1µF and 10µF capacitors in parallel across their output pins. All values are default, unless otherwise noted.
- ② Over-current protection is non-latching with auto recovery (hiccup).

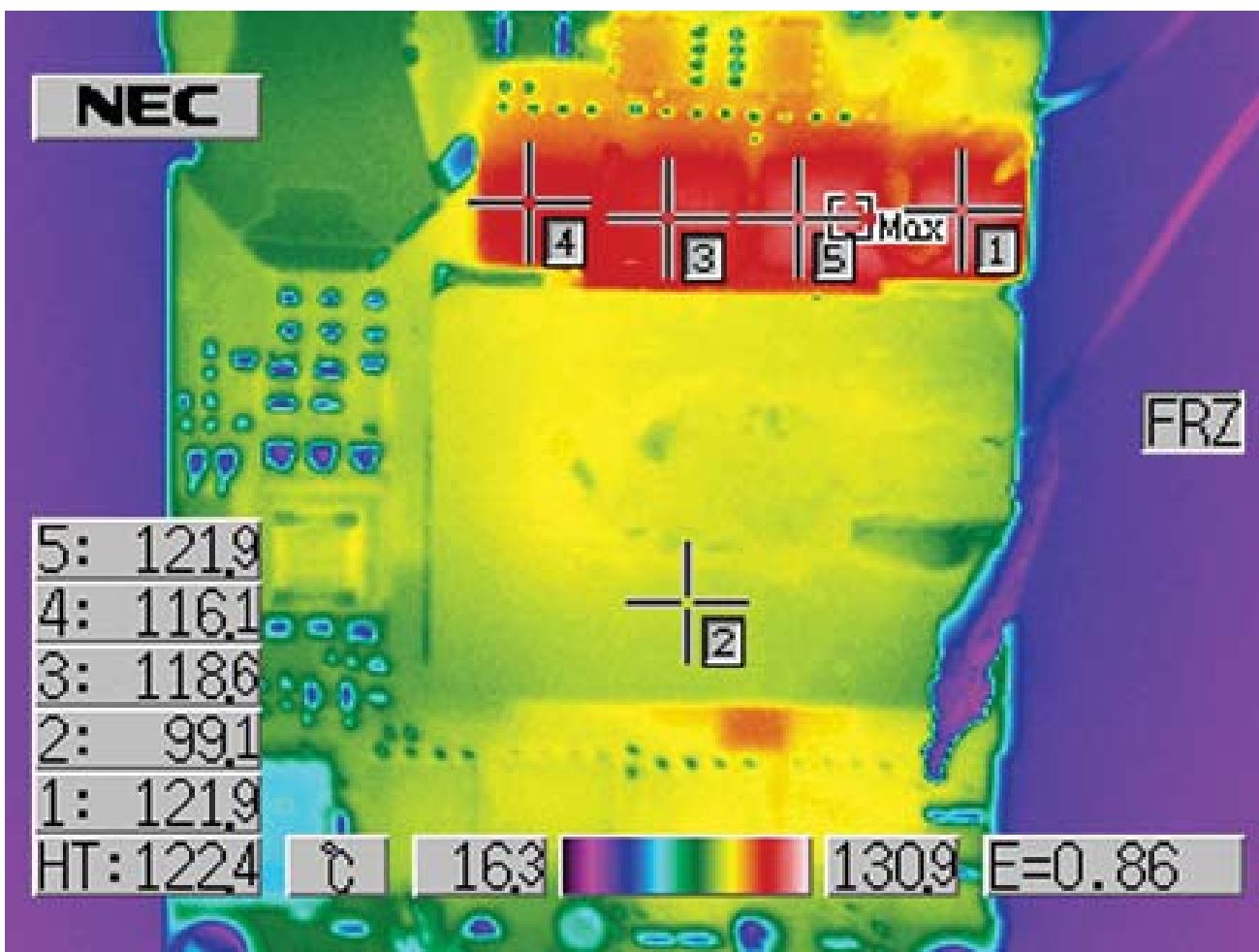
- ③ Regulation specifications describe the output voltage changes as the line voltage or load current is varied from its nominal or midpoint value to either extreme.
- ④ See Operating information section.

PERFORMANCE DATA

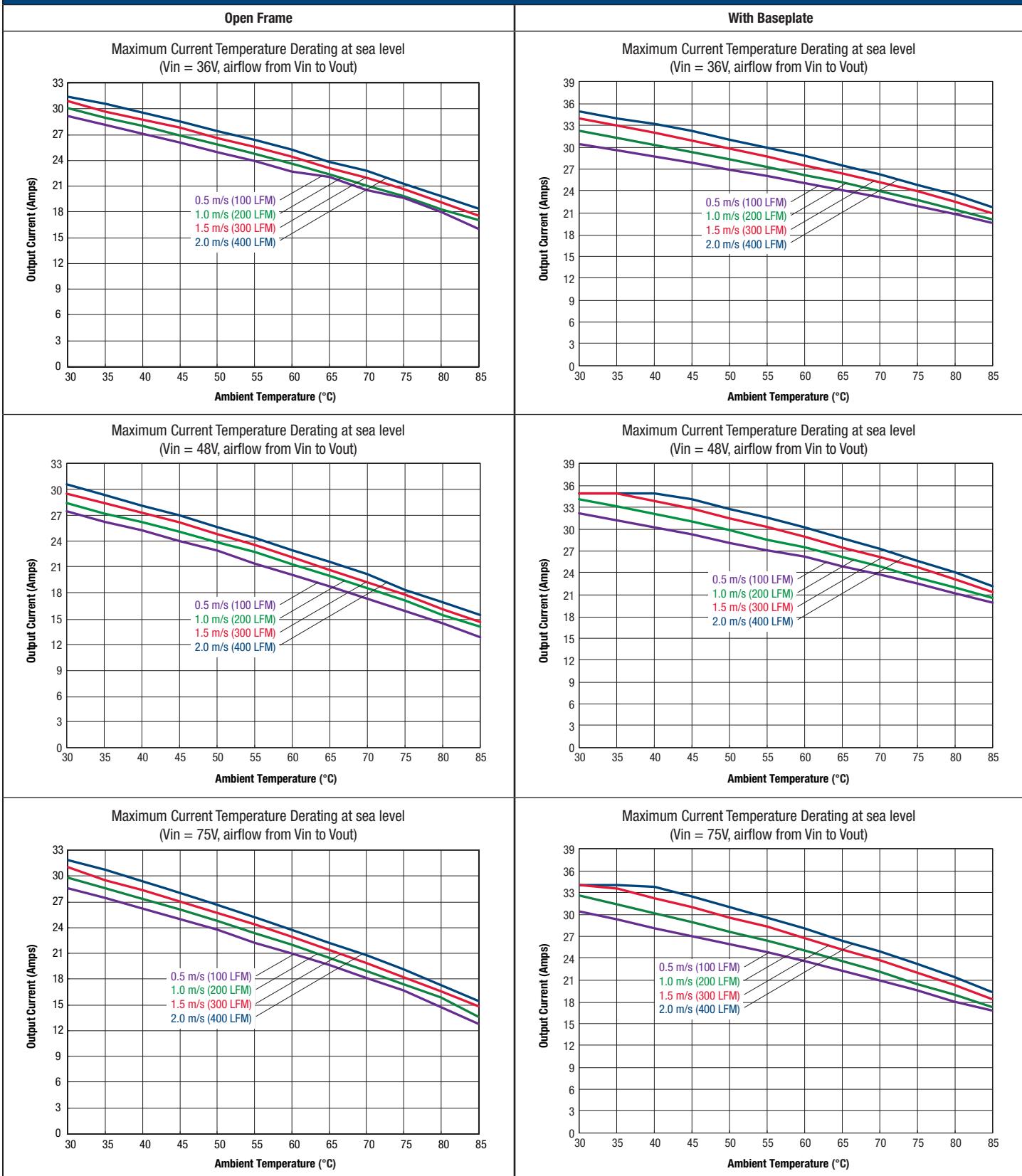
Efficiency vs. Line Voltage and Load Current @ +25°C



Thermal image at 35A current with 48V input voltage, 30°C ambient temperature, and 100LFM air flow. Identifiable and recommended maximum value to be verified in application.

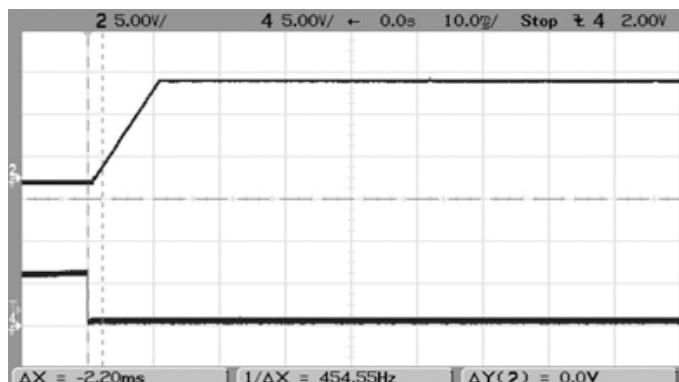


PERFORMANCE DATA: TEMPERATURE DERATING



PERFORMANCE DATA

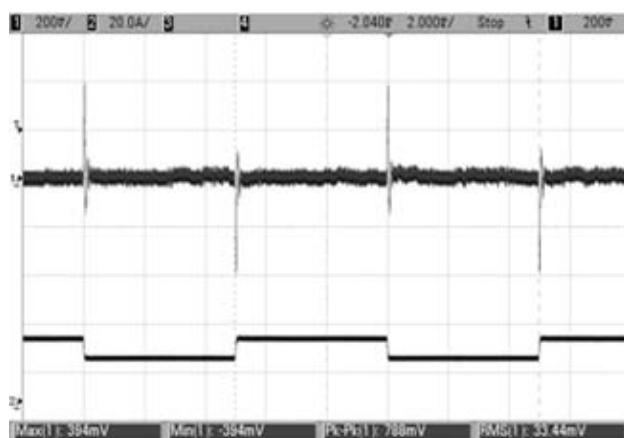
Enable Startup Delay (Vin = 48V, Iout = 35A, Cout = 1000μF, Ta = +25°C)
Ch2 = Vout Ch4 = Enable



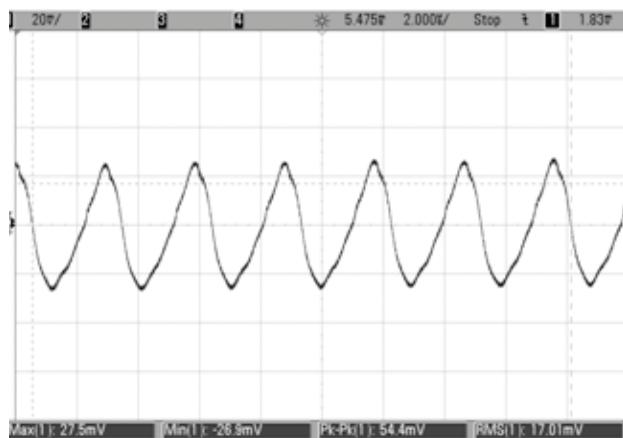
Startup Delay (Vin = 48V, Iout = 35A, Cout = 1000μF, Ta = +25°C)
Ch1 = Vin, Ch2 = Vout



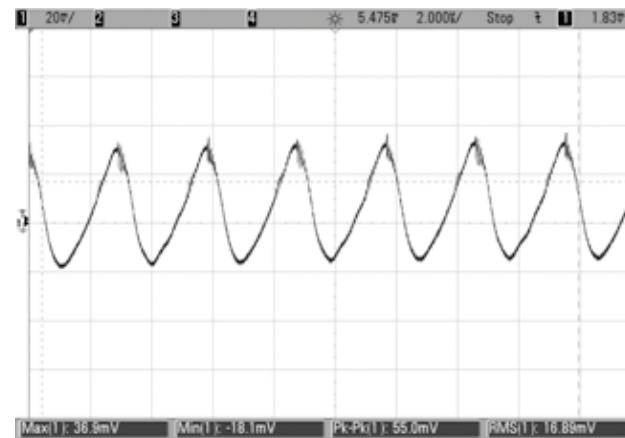
Stepload Transient Response (Vin = 48V, Iout = 50-75-50% of Iout, Cload = 1μF || 10μF,
slew rate: 0.1A/us, Ta = +25°C)



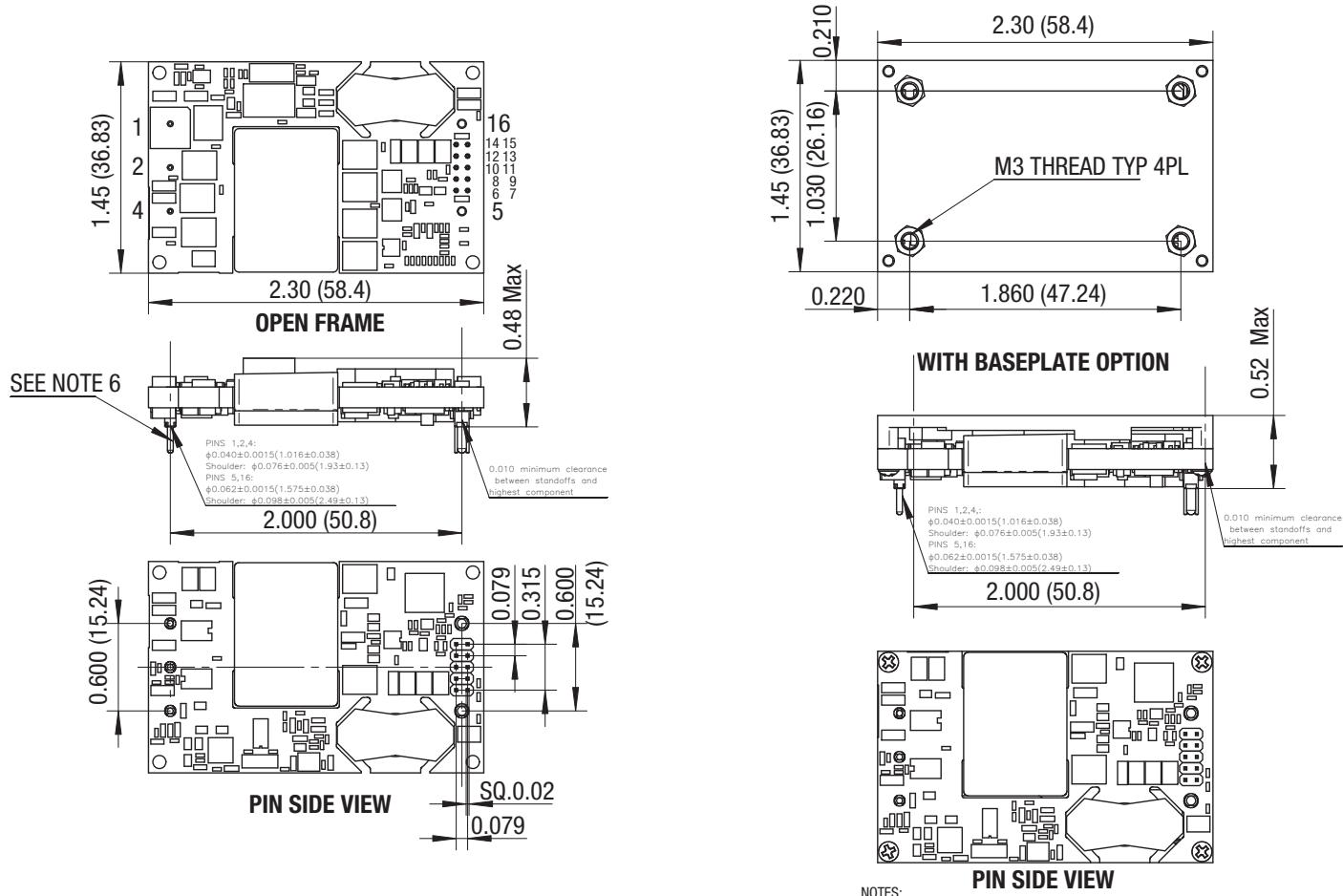
Output Ripple & Noise (Vin = 48V, Iout = 0A, Cout = 1μF || 10μF, Ta = +25°C)



Output Ripple & Noise (Vin = 48V, Iout = 35A, Cout = 1μF || 10μF, Ta = +25°C)



MECHANICAL SPECIFICATIONS



NOTES:

UNLESS OTHERWISE SPECIFIED:
1:M3 SCREW USED TO BOLT UNIT'S BASEPLATE TO OTHER SURFACES
(SUCH AS HEATSINK) MUST NOT EXCEED 0.100"(2.54mm) DEPTH BELOW
THE SURFACE OF BASEPLATE.

2:APPLIED TORQUE ON SCREW SHOULD NOT EXCEED 5.3in-lb(0.6Nm);

3:ALL DIMENSION ARE IN INCHES(MILLIMETER);

4:ALL TOLERANCES: .xx±0.01in(x,xx±0.02in(x,xx,±0.5mm)

x,xxin,±0.01in(x,xx,±0.25mm)

5:COMPONENT WILL VARY BETWEEN MODELS

6:STANDARD PIN LENGTH: 0.180 Inch

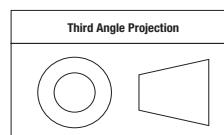
FOR L1 PIN LENGTH OPTION IN MODEL NAME,

THE L LENGTH SHOULD BE 0.110 INCH

FOR L2 PIN LENGTH OPTION IN MODEL NAME.,

USE STANDARD L2 PIN WITH PIN LENGTH TO 0.145 Inch

Dimensions are in inches (mm shown for ref. only).



Tolerances (unless otherwise specified):

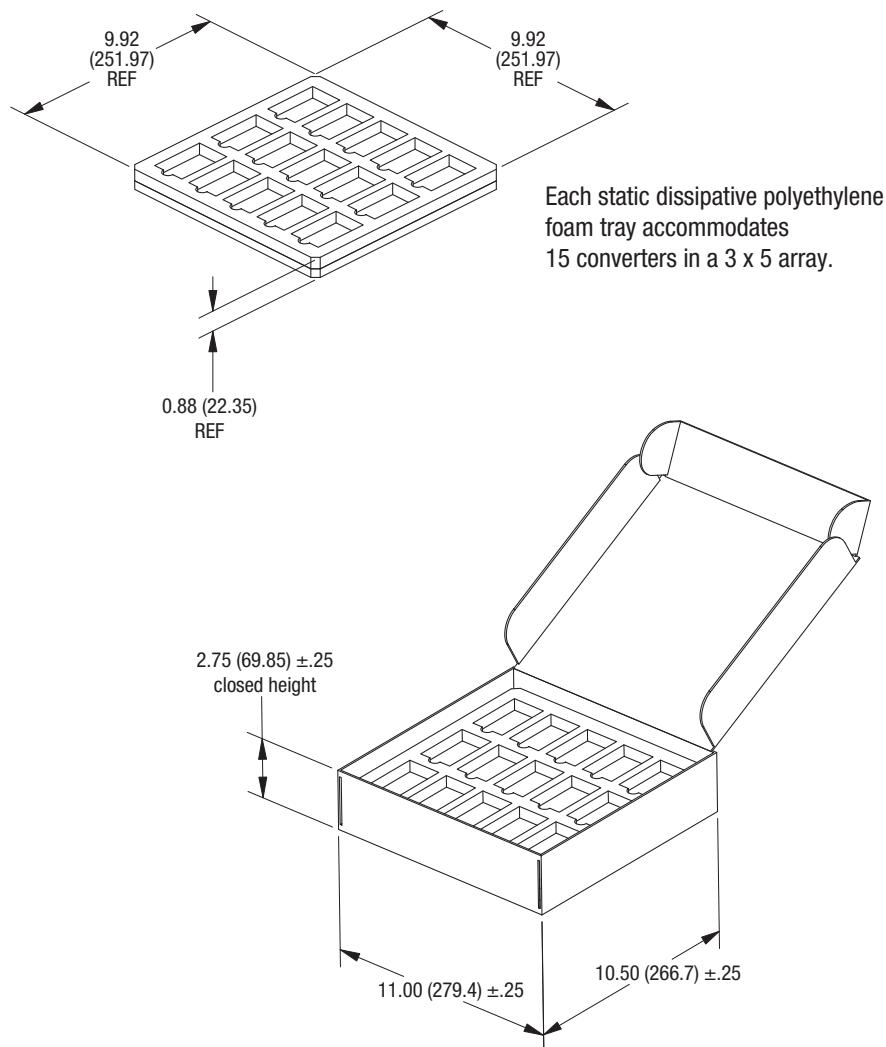
.XX ± 0.02 (0.5)

.XXX ± 0.010 (0.25)

Angles ± 2°

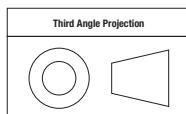
Components are shown for reference only
and may vary between units.

STANDARD PACKAGING



Carton accommodates two (2) trays yielding 30 converters per carton

Dimensions are in inches (mm) shown for ref. only.



Tolerances (unless otherwise specified):
 .XX ± 0.02 (0.5)
 .XXX ± 0.010 (0.25)
 Angles ± 2°

TECHNICAL NOTES

Power Management Overview

The module includes a wide range of readable and configurable power management features that are easy to implement with a minimum of external components. Furthermore, the module includes protection features that continuously protect the load from damage due to unexpected system faults. The SMBALERT pin alerts the host if there is a fault in the module. The following product parameters can continuously be monitored by a host: Vout, Iout, Vin, Temperature, and Power Good. The module is distributed with a default configuration suitable for a wide range operation in terms of Vin, Vout, and load. All power management functions can be reconfigured using the PMBus interface. The product provides a PMBus digital interface that enables the user to configure many aspects of the device operation as well as monitor the input and output parameters. Please contact our FAE for special configurations.

Soft-start Power Up

The default rise time of the ramp up is 20 ms. When starting by applying input voltage the control circuit boot-up time adds an additional 10 ms delay. The soft-start power up of the module can be reconfigured using the PMBus interface.

Over Voltage Protection (OVP)

The module includes over voltage limiting circuitry for protection of the load. The default OVP limit is 20% above the nominal output voltage. If the output voltage surpasses the OVP limit, the module can respond in different ways. The default response from an over voltage fault is to immediately shut down. The device will continuously check for the presence of the fault condition, and when the fault condition no longer exists the device will be re-enabled. The OVP fault level and fault response can be reconfigured using the PMBus interface.

Over Current Protection (OCP, Current limit)

The module includes current limiting circuitry for protection at continuous over load. The default setting for the product is hiccup mode. The current limit could be configured by simply setting the IOUT_OC_FAULT_LIMIT to be greater than the IOUT_OC_WARN_LIMIT. The maximum value that the current limit could be set is 50A.

Power Good

The module provides Power Good (PG) flag in the Status Word register that indicates the output voltage is within a specified tolerance of its target level and no fault condition exists. The Power Good pin default logic is negative and it can be configured by MFR_PGOOD_POLARITY.

PMBus Interface

This module offers a PMBus digital interface that enables the user to configure many characteristics of the device operation as well as to monitor the input and output voltages, output current and device temperature. The module can be used with any standard two-wire I₂C or SMBus host device. In addition, the module is compatible with PMBus version 1.2 and includes an SMBALERT line to help alleviate bandwidth limitations related to continuous fault monitoring. The module supports 100 kHz and 400 kHz bus clock frequency only.

Monitoring via PMBus

A system controller (host device) can monitor a wide variety of parameters through the PMBus interface. The controller can monitor fault conditions by monitoring the SMBALERT pin, which will be asserted when any number of pre-configured fault or warning conditions occur. The system controller can also continuously monitor any number of power conversion parameters including but not limited to the following:

- Input voltage
- Output voltage
- Output current
- Module temperature

Software Tools for Design and Production

For these modules, Murata-PS provides software for configuring and monitoring via the PMBus interface. For more information please contact your local Murata-PS representative.

[Click here for Application Note AN-63, Digital DC-DC Evaluation Board User Guide.](#)

[Click here for Application Note AN-64, Murata Power Brick GUI User Manual.](#)

PMBus Addressing

Figure 1 and the accompanying table display the recommended resistor values for hard-wiring PMBus addresses (1% tolerance resistors recommended): The address is set in the form of two octal (0 to 7) digits, with each pin setting one digit. The resistor values for each digit is shown below.

The SA0 and SA1 pins can be configured with a resistor to GND according to the following equation.

$$\text{PMBus Address} = 8 \times (\text{SA0value}) + (\text{SA1 value})$$

C0 and C1 are 4.7nF capacitors, which are recommended for correct addressing. If the calculated PMBus address is 0d, 11d or 12d, PMBus address 119d is assigned instead. From a system point of view, the user shall also be aware of further limitations of the addresses as stated in the PMBus Specification. It is not recommended to keep the SA0 and SA1 pins left open.

PMBus Commands

The products are designed to be PMBus compliant. The following tables list the implemented PMBus read commands. For more detailed information see "PMBus Power System Management Protocol Specification, Part I – General Requirements, Transport and Electrical Interface" and "PMBus Power System Management Protocol, Part II – Command Language."

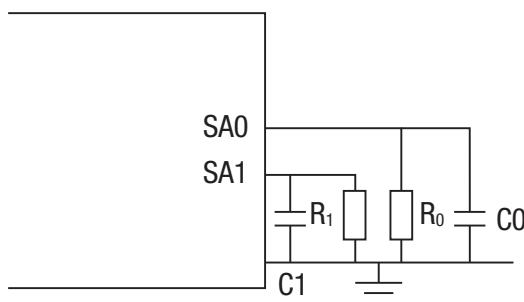


Figure 1. Schematic of Connection of Address Resistors

| Digit (SA0, SA1 index) | Resistor Value [kΩ] |
|------------------------|---------------------|
| 0 | 10 |
| 1 | 22 |
| 2 | 33 |
| 3 | 47 |
| 4 | 68 |
| 5 | 100 |
| 6 | 150 |
| 7 | 220 |

OVERALL

| CMD | Command Name ¹ | SMBus Transaction Type: Writing Data | SMBus Transaction Type: Reading Data | Number Of Data Bytes |
|-----|-------------------------------------|---|---|----------------------|
| 01h | OPERATION ² | Write Byte | Read Byte | 1 |
| 02h | ON_OFF_CONFIG ³ | Write Byte | Read Byte | 1 |
| 03h | CLEAR_FAULTS | Send byte | N/A | 0 |
| 10h | WRITE_PROTECT | Write Byte | Read Byte | 1 |
| 11h | STORE_DEFAULT_ALL ⁴ | Send byte | N/A | 0 |
| 12h | RESTORE_DEFAULT_ALL ⁴ | Send byte | N/A | 0 |
| 15h | STORE_USER_ALL ⁴ | Send byte | N/A | 0 |
| 16h | RESTORE_USER_ALL ⁴ | Send byte | N/A | 0 |
| 19h | CAPABILITY | N/A | Read Byte | 1 |
| 20h | VOUT_MODE | N/A | Read Byte | 1 |
| 21h | VOUT_COMMAND | Write Word | Read Word | 2 |
| 22h | VOUT_TRIM | Write Word | Read Word | 2 |
| 25h | VOUT_MARGIN_HIGH | Write Word | Read Word | 2 |
| 26h | VOUT_MARGIN_LOW | Write Word | Read Word | 2 |
| 28h | VOUT_DROOP | Write Word ¹⁶ | Read Word | 2 |
| 40h | VOUT_OV_FAULT_LIMIT | Write Word | Read Word | 2 |
| 41h | VOUT_OV_FAULT_RESPONSE ⁵ | Write Byte | Read Byte | 1 |
| 42h | VOUT_OV_WARN_LIMIT | Write Word | Read Word | 2 |
| 46h | IOUT_OC_FAULT_LIMIT | Write Word | Read Word | 2 |
| 47h | IOUT_OC_FAULT_RESPONSE ⁶ | Write Byte | Read Byte | 1 |
| 4Ah | IOUT_OC_WARN_LIMIT | Write Word | Read Word | 2 |
| 4Fh | OT_FAULT_LIMIT | Write Word | Read Word | 2 |
| 50h | OT_FAULT_RESPONSE ⁵ | Write Byte | Read Byte | 1 |
| 51h | OT_WARN_LIMIT | Write Word | Read Word | 2 |
| 55h | VIN_OV_FAULT_LIMIT | Write Word | Read Word | 2 |
| 56h | VIN_OV_FAULT_RESPONSE ⁷ | Write Byte | Read Byte | 1 |
| 57h | VIN_OV_WARN_LIMIT | Write Word | Read Word | 2 |
| 58h | VIN_UV_WARN_LIMIT | Write Word | Read Word | 2 |
| 59h | VIN_UV_FAULT_LIMIT | Write Word | Read Word | 2 |
| 5Ah | VIN_UV_FAULT_RESPONSE ⁷ | Write Byte | Read Byte | 1 |
| 5Eh | POWER_GOOD_ON | Write Word | Read Word | 2 |
| 5Fh | POWER_GOOD_OFF | Write Word | Read Word | 2 |

OVERALL (CONT.)

| CMD | Command Name ¹ | SMBus Transaction Type: Writing Data | SMBus Transaction Type: Reading Data | Number Of Data Bytes |
|-----|---------------------------------|---|---|----------------------|
| 60h | TON_DELAY | Write Word ¹⁶ | Read Word | 2 |
| 61h | TON_RISE ¹⁴ | Write Word ¹⁶ | Read Word | 2 |
| 64h | TOFF_DELAY | Write Word ¹⁶ | Read Word | 2 |
| 65h | TOFF_FALL ¹⁴ | Write Word ¹⁶ | Read Word | 2 |
| 78h | STATUS_BYTE | Write Byte | Read Byte | 1 |
| 79h | STATUS_WORD | Write Word | Read Word | 2 |
| 7Ah | STATUS_VOUT | Write Byte | Read Byte | 1 |
| 7Bh | STATUS_IOUT | Write Byte | Read Byte | 1 |
| 7Ch | STATUS_INPUT | Write Byte | Read Byte | 1 |
| 7Dh | STATUS_TEMPERATURE | Write Byte | Read Byte | 1 |
| 7Eh | STATUS_CML | Write Byte | Read Byte | 1 |
| 88h | READ_VIN | N/A | Read Word | 2 |
| 8Bh | READ_VOUT | N/A | Read Word | 2 |
| 8Ch | READ_IOUT | N/A | Read Word | 2 |
| 8Dh | READ_TEMPERATURE_1 ⁸ | N/A | Read Word | 2 |
| 94h | READ_DUTY_CYCLE | N/A | Read Word | 2 |
| 95h | READ_FREQUENCY | N/A | Read Word | 2 |
| 96h | READ_POUT | N/A | Read Word | 2 |
| 98h | PMBus_REVISION | N/A | Read Byte | 1 |
| 99h | MFR_ID | N/A | Block Read | 22 |
| 9Ah | MFR_MODEL ⁹ | N/A | Block Read | <=20 |
| 9Bh | MFR_REVISION ⁹ | N/A | Block Read | <=10 |
| 9Dh | MFR_DATE ⁹ | N/A | Block Read | <=10 |
| 9Eh | MFR_SERIAL ⁹ | N/A | Block Read | <=10 |
| A0h | MFR_VIN_MIN | N/A | Read Word | 2 |
| A1h | MFR_VIN_MAX | N/A | Read Word | 2 |
| A2h | MFR_IIN_MAX | N/A | Read Word | 2 |
| A3h | MFR_PIN_MAX | N/A | Read Word | 2 |
| A4h | MFR_VOUT_MIN | N/A | Read Word | 2 |
| A5h | MFR_VOUT_MAX | N/A | Read Word | 2 |
| A6h | MFR_IOUT_MAX | N/A | Read Word | 2 |
| A7h | MFR_POUT_MAX | N/A | Read Word | 2 |
| A8h | MFR_TAMBIENT_MAX | N/A | Read Word | 2 |
| A9h | MFR_TAMBIENT_MIN | N/A | Read Word | 2 |
| B0h | USER_DATA_00 | Block Write | Block Read | <=20 |
| B1h | USER_DATA_01 | Block Write | Block Read | <=20 |
| C0h | MFR_MAX_TEMP_1 | N/A | Read Word | 2 |
| DBh | MFR_CURRENT_SHARE_CONFIG | N/A | Read Byte | 1 |
| DDh | MFR_PRIMARY_ON_OFF_CONFIG | Write Byte | Read Byte | 1 |
| DEh | MFR_PGOOD_POLARITY | Write Byte | Read Byte | 1 |
| E8h | MFR_VIN_OV_FAULT_HYS | Write Word | Read Word | 2 |
| E9h | MFR_VIN_UV_FAULT_HYS | Write Word | Read Word | 2 |
| EAh | MFR_OT_FAULT_HYS | Write Word | Read Word | 2 |

OVERALL (CONT.)

Notes:

1. a) Unit restores the entire contents of the non-volatile User Store memory when power up
b) PEC is supported
c) Max bus speed: 400kHz
d) SMBALERT# is supported
e) Linear data format used
f) addressing: If the calculated PMBus address is 0d, 11d or 12d, SA0 or SA1 lefts open, default PMBus address 119d is assigned instead.
2. Not supported items:
101001XXb Margin Low(Ignore Fault),
101001XXb On Margin High(Ignore Fault)
3. Restart delay of turned off by OPEATION or CONTROL or primary on/off is 200ms
4. Unit will shutdown 1 second for protection , then recover automatically.
5. Restart delay unit: 500ms, lower limit: 500ms.
Turn off delay unit: 0ms, lower limit: 0ms
if bits 7:6=11b, restart delay is 500ms
6. Restart delay unit and Turn off delay unit are same as note 5
Bits 7:6: 00b,01b,10b are not supported
7. Restart delay unit: 200ms, lower limit: 200ms.
Turn off delay unit:0ms, lower limit: 0ms
if bits 7:6=11b, restart delay is 200ms
8. Temperature of baseplate side
9. Unit's actual information
10. Default value of DROOP CURRENT SHARE ENABLED mode: 0x01
Default value of DROOP CURRENT SHARE DISABLED mode: 0x00
11. Locked to 10mΩ in DROOP CURRENT SHARE mode; configurable and default value is 0mΩ in CURRENT SHARE DISABLED mode
12. Default value of negative logic: 0x04
Default value of positive logic: 0x06
13. Unit can receive any value for VOUT_TRIM command, but Vout is limited to 8.1~13.2V, if calculated Vout exceeds limit, then equal to limit.
14. Value of 0 is acceptable, which is the same as lower limit to unit.
15. Default value of without "B" suffix: 120°C
Default value of with "B" suffix: 125°C
16. Configurable while without "S" suffix locked while with "S" suffix

MURATA-PS DEFINED COMMANDS (01-CFH REFER TO PMBUS 1.2 SPEC)

| DBh: MFR_CURRENT_SHARE_CONFIG | | | | On/Off 2 pin | VOUT_DROOP | TON_DELAY | TOFF_DELAY | TON_RISE | TOFF_FALL |
|--------------------------------|---|-------|--|--------------|------------------|------------------|------------------|------------------|------------------|
| Bits | Purpose | Value | Meaning | | | | | | |
| 7:1 | Droop Current Share Control | 0 | Current share disabled | On/Off 2 | configurable | configurable | configurable | configurable | configurable |
| | | 1 | Droop current share mode enabled | | | | | | |
| DDh: MFR_PRIMARY_ON_OFF_CONFIG | | | | | | | | | |
| Bits | Purpose | Value | Meaning | | | | | | |
| 7:3 | Controls how the unit responds to the CONTROL pin | 0 | Reserved | On/Off 2 | locked to 0x000A | locked to 0x0001 | locked to 0x0000 | locked to 0x0000 | locked to 0x0000 |
| | | 1 | Unit ignores the primary ON/OFF pin | | | | | | |
| 2 | Polarity of primary ON/OFF logic | 0 | Unit requires the primary ON/OFF pin to be asserted to start the unit. | | | | | | |
| | | 1 | Active low (Pull pin low to start the unit) | | | | | | |
| 1 | | 0 | Active high (Pull high or open to start the unit) | | | | | | |
| 0 | | 0 | Reserved | | | | | | |
| DEh: MFR_PGOOD_POLARITY | | | | | | | | | |
| Bits | Purpose | Value | Meaning | | | | | | |
| 7:1 | Power good polarity of pin 12 | 0 | Reserved | On/Off 2 | locked to 0x000A | locked to 0x0001 | locked to 0x0000 | locked to 0x0000 | locked to 0x0000 |
| | | 1 | Negative logic, output low if Vout rises to specific value | | | | | | |
| | | | Positive logic, output high if Vout rises to specific value | | | | | | |

STATUS WORD AND BYTE (GREEN = SUPPORTED)

| STATUS_VOUT | STATUS_WORD | STATUS_INPUT |
|-------------------------------|-------------------------------|----------------------|
| 7 VOUT_OV_FAULT | 7 VOUT | 7 VIN_OV_FAULT |
| 6 VOUT_OV_WARNING | 6 IOUT/POUT | 6 VIN_OV_WARNING |
| 5 VOUT_UV_WARNING | 5 INPUT | 5 VIN_UV_WARNING |
| 4 VOUT_UV_FAULT | 4 MFR_SPECIFIC | 4 VIN_UV_FAULT |
| 3 VOUT_MAX Warning | 3 POWER_GOOD# | 2 IN_OC_FAULT |
| 2 TON_MAX_FAULT | 2 FANS | 1 IN_OC_WARNING |
| 1 TOFF_MAX_WARNING | 1 OTHER | 0 PIN_OP_WARNING |
| 0 VOUT Tracking Error | 0 UNKNOWN | |
| | 7 BUSY | |
| STATUS_IOUT | 6 OFF | |
| 7 IOUT_OC_FAULT | 5 VOUT_OV_FAULT | Manufacturer Defined |
| 6 IOUT_OC_LV_FAULT | 4 IOUT_OC_FAULT | Manufacturer Defined |
| 5 IOUT_OC_WARNING | 3 VIN_UV_FAULT | Manufacturer Defined |
| 4 IOUT_UC_FAULT | 2 TEMPERATURE | Manufacturer Defined |
| 3 Current Share Fault | 1 CML | Manufacturer Defined |
| 2 In Power Limiting Mode | 0 NONE OF THE ABOVE | Manufacturer Defined |
| 1 POUT_OP_FAULT | | Manufacturer Defined |
| 0 POUT_OP_WARNING | | Manufacturer Defined |
| STATUS_TEMPERATURE | STATUS_OTHER | |
| 7 OT_FAULT | 7 Reserved | |
| 6 OT_WARNING | 6 Reserved | |
| 5 UT_WARNING | 5 Input A Fuse/Breaker Fault | |
| 4 UT_FAULT | 4 Input B Fuse/Breaker Fault | |
| 3 Reserved | 3 Input A OR-ing Device Fault | |
| 2 Reserved | 2 Input B OR-ing Device Fault | |
| 1 Reserved | 1 Output OR-ing Device Fault | |
| 0 Reserved | 0 Reserved | |
| STATUS_CML | | |
| 7 Invalid/Unsupported Command | | |
| 6 Invalid/Unsupported Data | | |
| 5 Packet Error Check Failed | | |
| 4 Memory Fault Detected | | |
| 3 Processor Fault Detected | | |
| 2 Reserved | | |
| 1 Other Communication Fault | | |
| 0 Other Memory Or Logic Fault | | |