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## SPECIFICATION

**CUSTOMER :** \_\_\_\_\_

**MODULE NO.:** EA TFT052-41ATS

<p style="text-align: center;"><b>APPROVED BY:</b></p> <p>( FOR CUSTOMER USE ONLY )</p>	<p><b>PCB VERSION:</b> _____</p> <p><b>DATA:</b> _____</p>
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SALES BY	APPROVED BY	CHECKED BY	PREPARED BY
<p><b>ISSUED DATE: 2018/07/12</b></p>			

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# 1. Summary

TFT 5.2" is a TN transmissive type color active matrix TFT liquid crystal display that use amorphous silicon TFT as switching devices. This module is a composed of a TFT\_LCD module, It is usually designed for industrial application and this module follows RoHs.

## **1.1. Accessories**

ZIF connector for display, bottom contact  
ZIF connector for display, top contact  
ZIF connector for touch panel, top contact

**EA WF050-40S**  
**EA WF050-40ST**  
**EA WF050-10T**



## 2. General Specification

Item	Dimension	Unit
Size	5.2	inch
Dot Matrix	480 x RGBx128	dots
Module dimension	140.4 x 49.87 x 4.96	mm
Active area	127.152 x 33.9072	mm
Dot pitch	0.0883 x 0.2649	mm
LCD type	TFT, Normally White, Transmissive	
View Direction	6 o'clock	
Gray Scale Inversion Direction	12 o'clock	
Aspect Ratio	Bar Type	
Backlight Type	LED, Normally White	
Driver IC	ST7252 Or Equal	
Interface	RGB 24bit	
With /Without TP	With CTP	
Surface	Glare	

\*Color tone slight changed by temperature and driving voltage.

### 3. Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-20	—	+70	°C
Storage Temperature	TST	-30	—	+80	°C

Note: Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above

1. Temp.  $\leq 60^{\circ}\text{C}$ , 90% RH MAX. Temp.  $> 60^{\circ}\text{C}$ , Absolute humidity shall be less than 90% RH at  $60^{\circ}\text{C}$

## 4. Electrical Characteristics

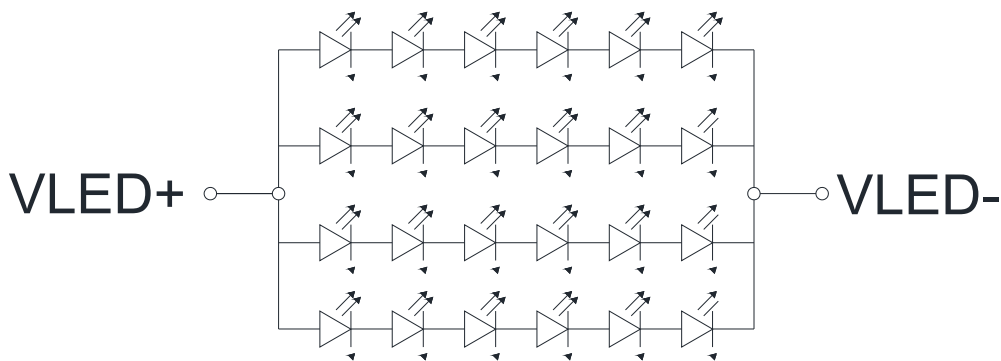
### 4.1. Operating conditions:

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage For LCM	VCC	—	3.0	3.3	3.6	V
Supply Voltage For Touch	VDDT	—	2.8	—	3.3	V
Supply Current For LCM	ICC	-	—	20	—	mA

### 4.2. LED driving conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
LED current		-	80	-	mA	
LED voltage	VLED+	16.8	18.6	21	V	Note 1
LED Life Time		-	50,000	-	Hr	Note 2,3,4

Note 1 : There are 1 Groups LED



Note 2 :  $T_a = 25 \square$

Note 3 : Brightness to be decreased to 50% of the initial value

Note 4 : The single LED lamp case

## 5. DC CHARATERISTICS

Parameter	Symbol	Rating			Unit	Condition
		Min	Typ	Max		
Low level input voltage	$V_{IL}$	0	-	0.3VCC	V	
High level input voltage	$V_{IH}$	0.7VCC	-	VCC	V	

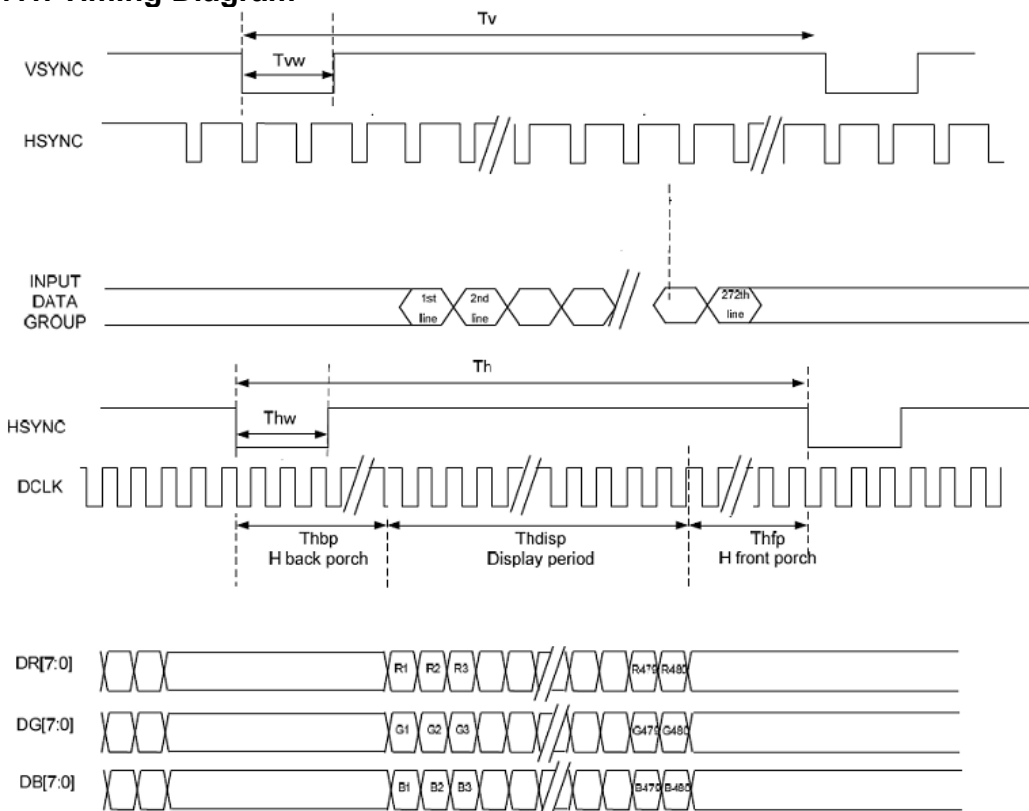


## 6. AC CHARACTERISTICS

Parallel SYNC mode RGB input timing table

Item	Symbol	Min	Typ	Max	Unit	
CLK frequency	Fclk	8	9	12	MHz	
DCLK Period	Tclk	83	111	125	ns	
HSYNC	Period Time	Th	485	531	598	DCLK
	Display Period	Thdisp	-	480	-	DCLK
	Back Porch	Thbp	3	43	43	DCLK
	Front Porch	Thfp	2	8	75	DCLK
	Pulse Width	Thw	2	4	75	DCLK
VSYNC	Period Time	Tv	276	292	321	H
	Display Period	Tvdisp	-	272	-	H
	Back Porch	Tvbp	2	12	12	H
	Front Porch	Tvfp	2	8	37	H
	Pulse Width	Tvw	2	4	37	H

### 7.1. Timing Diagram



## 7. Optical Characteristics

Item	Symbol	Condition.	Min	Typ.	Max.	Unit	Remark	
Response time	Tr+ Tf	$\theta=0^\circ$ 、 $\phi=0^\circ$	-	35	-	.ms	Note 3	
Contrast ratio	CR	At optimized viewing angle	300	500	-	-	Note 4	
Color Chromaticity	White	Wx	$\theta=0^\circ$ 、 $\phi=0$	0.24	0.29	0.34	Note 2,5	
		Wy		0.26	0.31	0.36		
Viewing angle (Gray Scale Inversion Direction)	Hor.	$\Theta_R$	CR $\square$ 10	-	75	-	Deg.	Note 1
		$\Theta_L$		-	75	-		
	Ver.	$\Phi_T$		-	75	-		
		$\Phi_B$		-	75	-		
Brightness	-	-	600	700	-	-	Center of display	

Ta=25±2□, IL=80mA

Note 1: Definition of viewing angle

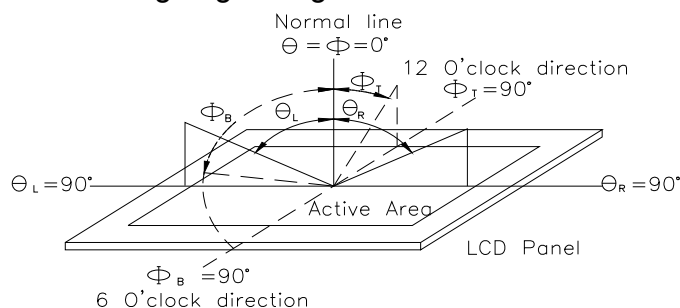


Fig.7.1. Definition of viewing angle

Note 2: Test equipment setup:

After stabilizing and leaving the panel alone at a driven temperature for 10 minutes, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-7orBM-5 luminance meter 1.0° field of view at a distance of 50cm and normal direction.

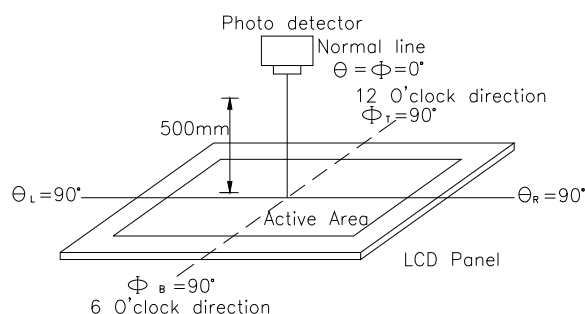
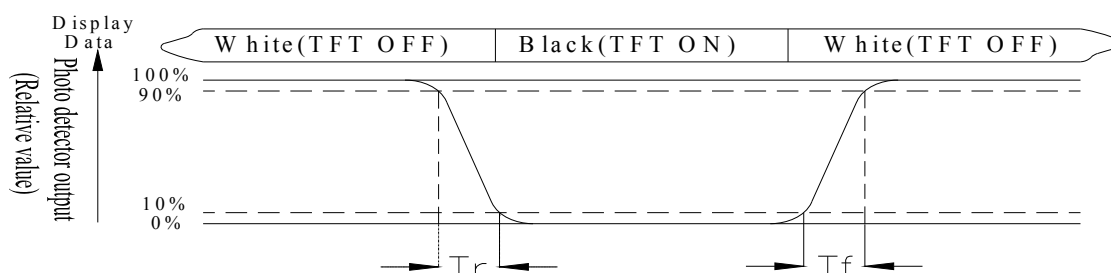


Fig. 7.2. Optical measurement system setup

**Note 3: Definition of Response time:**

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time,  $T_r$ , is the time between photo detector output intensity changed from 90% to 10%. And fall time,  $T_f$ , is the time between photo detector output intensity changed from 10% to 90%



**Note 4: Definition of contrast ratio:**

The contrast ratio is defined as the following expression.

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

Note 5: White  $V_i = V_{i50} \pm 1.5V$

Black  $V_i = V_{i50} \pm 2.0V$

"±" means that the analog input signal swings in phase with VCOM signal.

"±" means that the analog input signal swings out of phase with VCOM signal.

The 100% transmission is defined as the transmission of LCD panel when all the input terminals of module are electrically opened.

**Note 6: Definition of color chromaticity (CIE 1931)**

Color coordinates measured at the center point of LCD

Note 7: Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

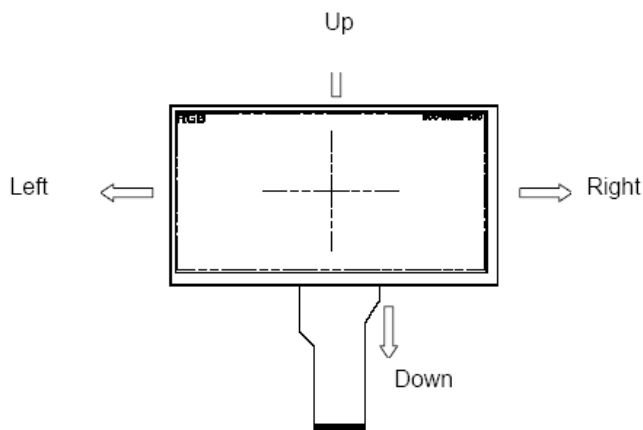
## 8. Interface

Pin	Symbol	Function	Remark
1	VLED-	Power for LED backlight cathode	
2	VLED+	Power for LED backlight anode	
3	GND	Power ground	
4	VCC	Power voltage	
5	R0	Red data (LSB)	
6	R1	Red data	
7	R2	Red data	
8	R3	Red data	
9	R4	Red data	
10	R5	Red data	
11	R6	Red data	
12	R7	Red data (MSB)	
13	G0	Green data (LSB)	
14	G1	Green data	
15	G2	Green data	
16	G3	Green data	
17	G4	Green data	
18	G5	Green data	
19	G6	Green data	
20	G7	Green data (MSB)	
21	B0	Blue data (LSB)	
22	B1	Blue data	
23	B2	Blue data	
24	B3	Blue data	
25	B4	Blue data	
26	B5	Blue data	
27	B6	Blue data	
28	B7	Blue data (MSB)	
29	GND	Power ground	
30	CLK	Pixel clock (DCLK)	
31	LR	Right /Left selection; Default R/L is Pull High	Note1,2
32	HSYNC	Horizontal sync signal; negative polarity	
33	VSYNC	Vertical sync signal; negative polarity	
34	NC	No connection	
35	UD	Up/down selection; Default U/D is Pull High	Note1,2
36	RESET	Reset signal	
37	NC	No connection	
38	NC	No connection	
39	NC	No connection	
40	NC	No connection	

Note 1: Selection of scanning mode, and LR,UD Pull High 10KΩ on FPC

Setting of scan control input		Scanning direction
UD	LR	
L	H	Down to up, left to right
H	L	Up to down, right to left
L	L	Down to up, right to left
H	H	Up to down, left to right

Note 2: Definition of scanning direction. Refer to the figure as below:

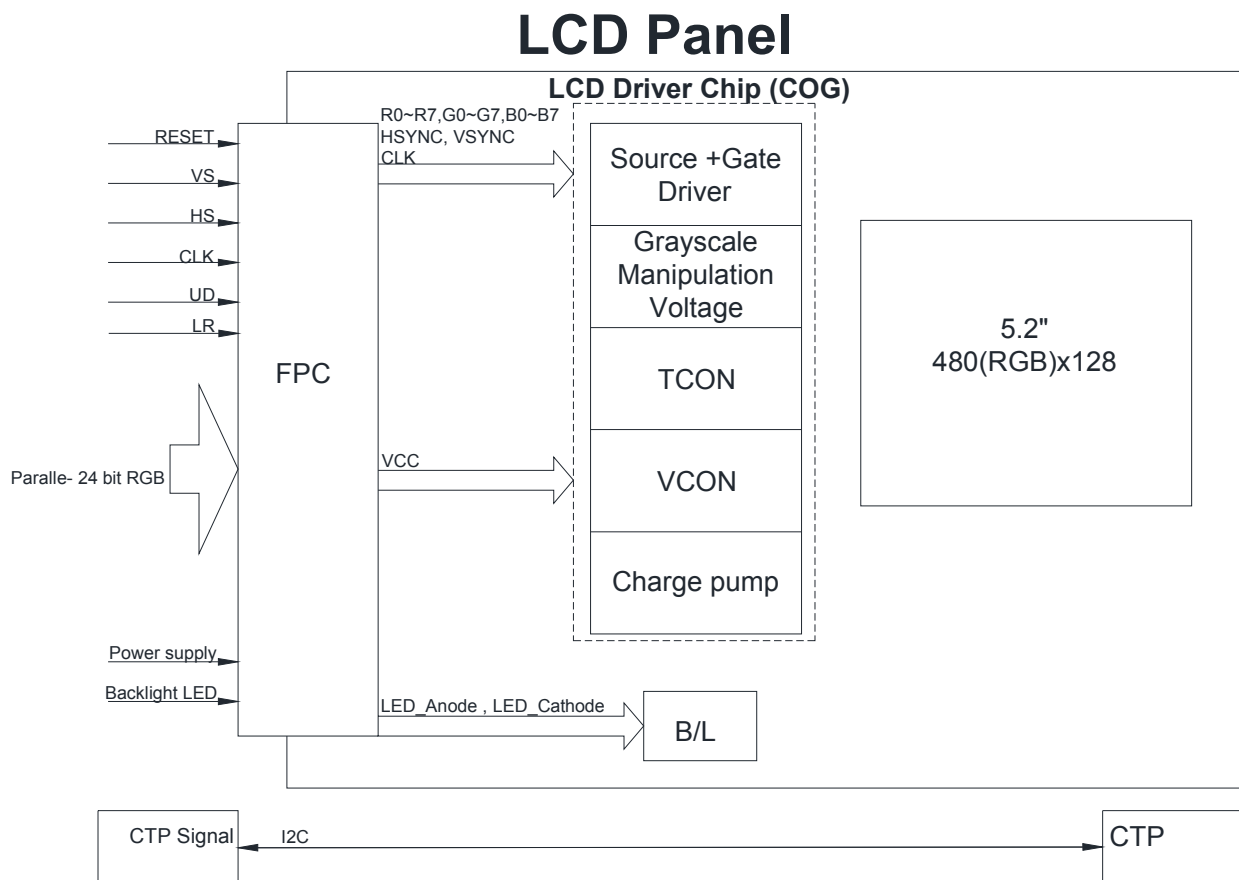


## 8.2. CTP PIN Definition

Pin	Symbol	Function	Remark
1	VSS	Ground for analog circuit	
2	VDDT	Power Supply : +3.3V	
3	SCL	I2C clock signal	
4	NC	No connect	
5	SDA	I2C data signal	
6	NC	No connect	
7	RST	External reset signal, active low	
8	NC	No connect	
9	INT	Interrupt signal, active low, asserted to request Host start a new transaction.	
10	VSS	Ground for analog circuit	

The I<sup>2</sup>C address is set to 0x70 (0x38). Controller is CYTMA568 or compatible and resolution 480x128 dots.

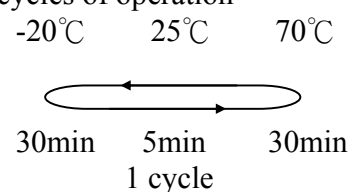
## 9. Block Diagram





# 10. Reliability

Content of Reliability Test (Wide temperature, -20°C~70°C)

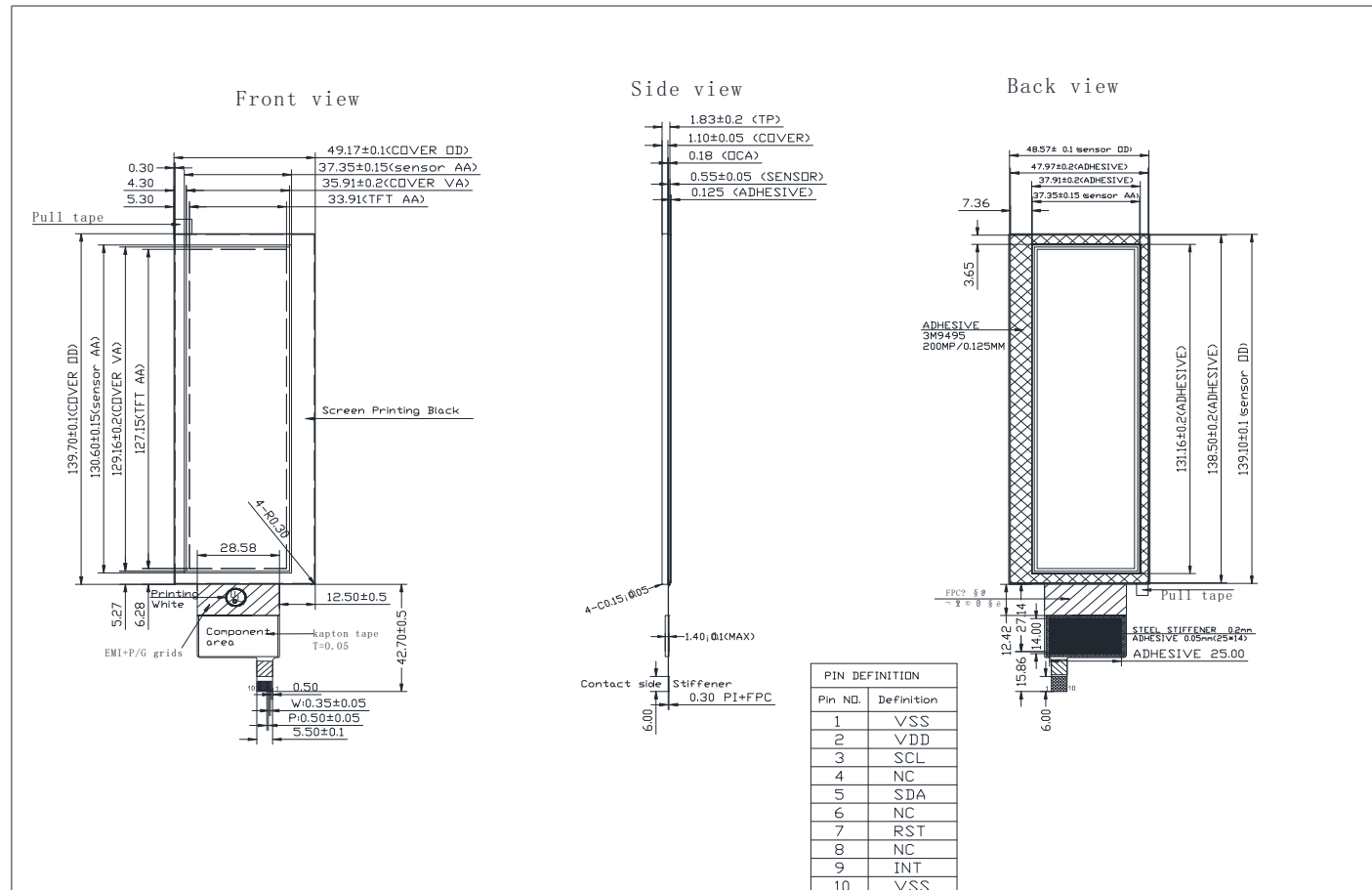
Environmental Test			
Test Item	Content of Test	Test Condition	Note
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80°C 200hrs	2
Low Temperature storage	Endurance test applying the low storage temperature for a long time.	-30°C 200hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70°C 200hrs	—
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-20°C 200hrs	1
High Temperature/ Humidity Operation	The module should be allowed to stand at 60°C, 90%RH max	60°C, 90%RH 96hrs	1,2
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation <div style="text-align: center;">  <p>30min    5min    30min 1 cycle</p> </div>	-20°C/70°C 10 cycles	—
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude : 1.5mm Vibration Frequency : 10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=±600V(contact), ±800v(air), RS=330Ω CS=150pF 10 times	—

Note1: No dew condensation to be observed.

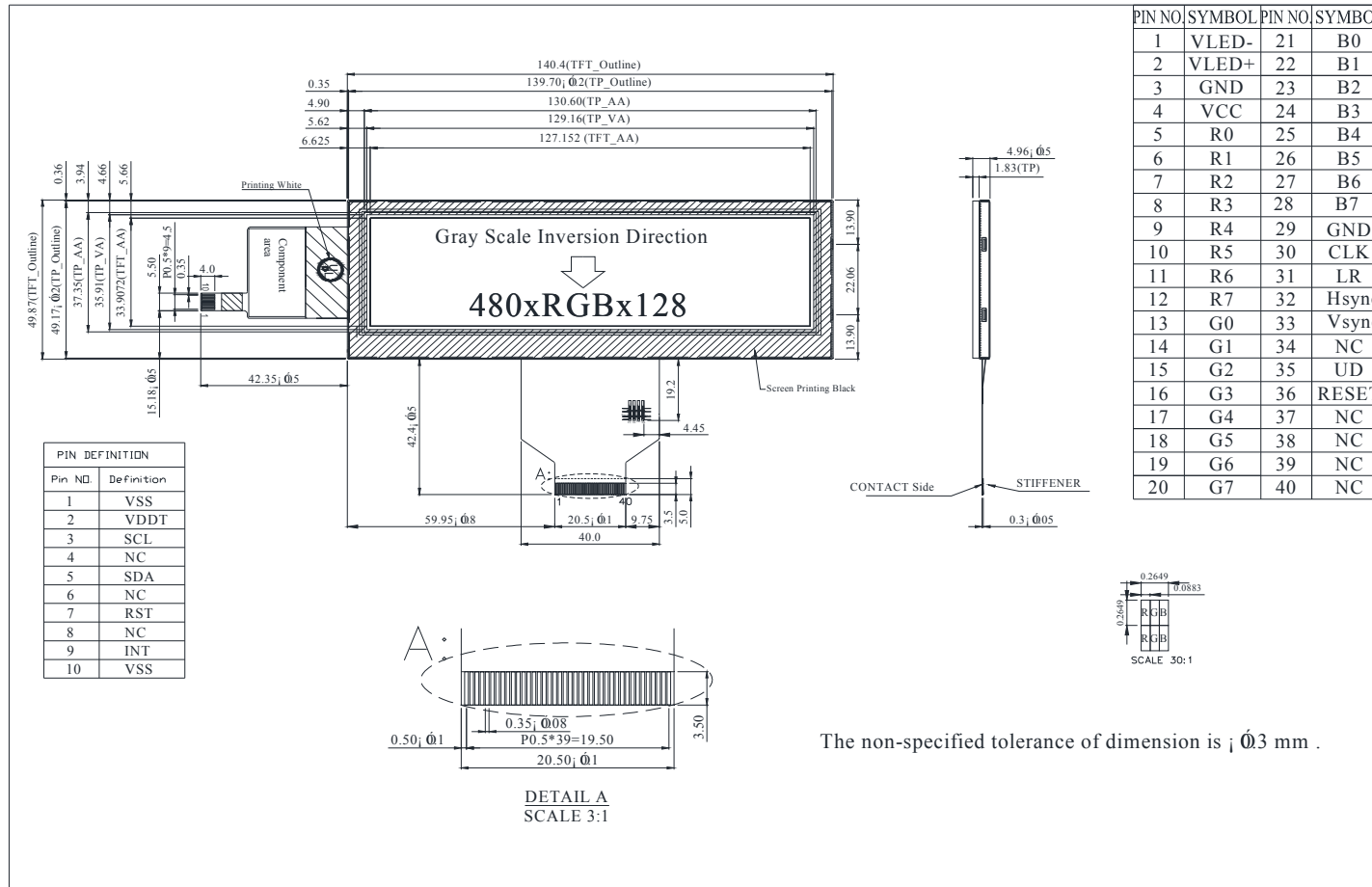
Note2: The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after remove from the test chamber.

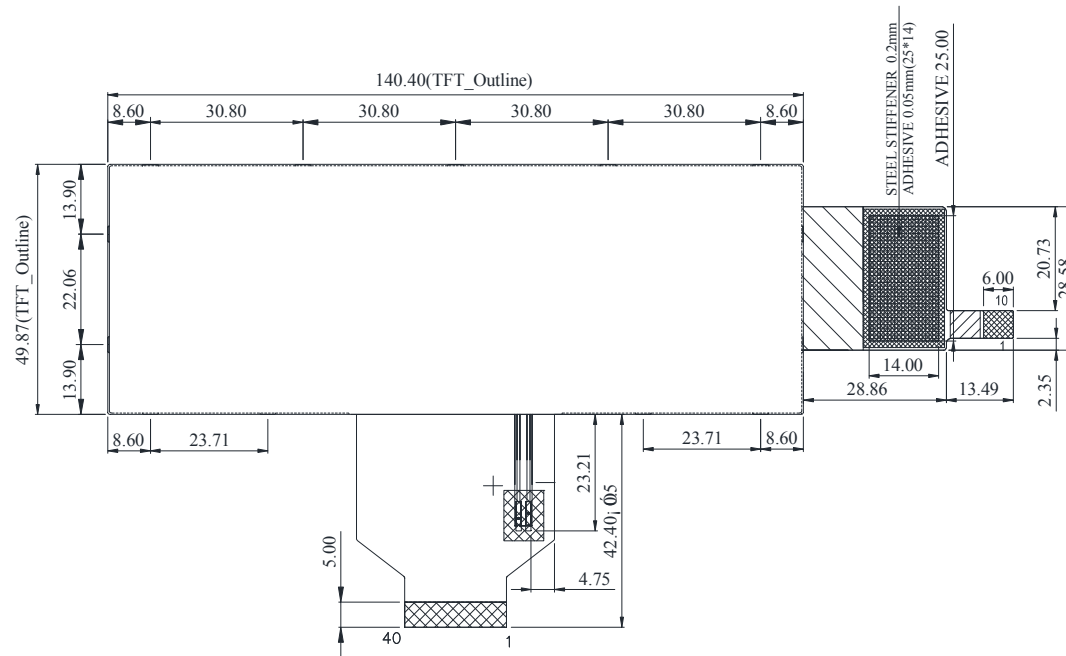
Note3: The packing have to including into the vibration testing.

# 11. Touch Panel Information



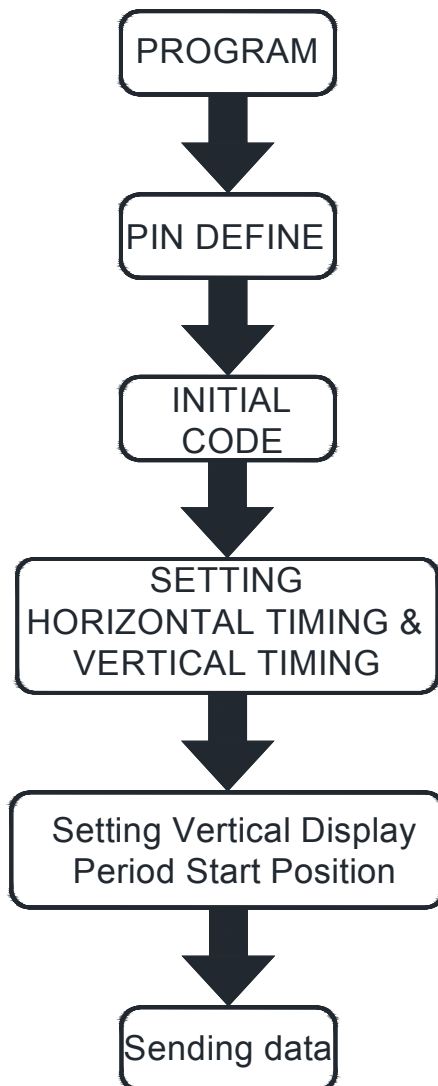
# 12. Contour Drawing





The non-specified tolerance of dimension is  $\pm 0.3$  mm .

## 13.Display start address setting



Ex.

One horizontal line=0x0213

VS period time=0x0124

HS Blanking=0x2b

VS Blanking=0x10

HS Front Porch=0x05

VS Front Porch=0x08

Suggestion :  
Vertical Display Period  
Start Position=0x44

Note :

For different Controller ICs, the value of vertical display period start position need to be adjusted accordingly.