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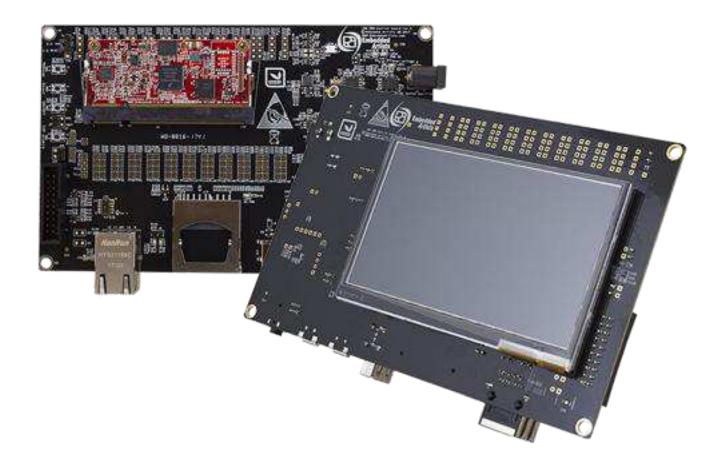




iMX RT1052 Developer's Kit - User's Guide

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iMX RT1052 Developer's Kit User's Guide



Get Up-and-Running Quickly and Start Developing Your Application On Day 1!



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Table of Contents

1	Document Revision History	5	
2	Getting Started	6	
2.1	iMX RT1052 Developer's Kit Content	6	
2.2	Hardware Overview	6	
2.3	Connecting - Get Started	8	
	Tera Term Terminal Emulation Application	9	
2.3.2	PuTTY terminal emulation application	10	
2.4	Alternative Powering	11	
2.5	ESD Precaution	11	
2.6	General Handling Care	12	
2.7	Handling SO-DIMM Boards	12	
2.8	OTP Fuse Programming	13	
2.9	CE Assessment	13	
2.10	Further Information	13	
3	Demo Application	14	
4	iMX OEM Carrier Board Design	15	
4.1	Modifications to iMX OEM Carrier Board	15	
4.2	SP2a: Power Supplies	16	
4.3	SP2b: Current Measurement	18	
4.4	SP2c: VBAT Powering	19	
4.5	SP3-SP4: OEM Board Connector (SODIMM) and Access Pads	20	
4.6	SP5: Push-buttons and LEDs		
4.7	SP6: Debug Interfaces	23	
4.8	SP7: USB Interfaces	25	
4.9	SP8: CAN	26	
4.10	SP9: SD Memory Card Interface	27	
4.11	SP10: LCD Interface	27	
4.12	SP11: Ethernet Interface	28	
4.13	SP12: UART-to-USB Bridge	29	
4.14	SP13: Audio Codec	30	
4.15	Default Jumpers Positions	31	
4.16	Usage of CPU Pins	32	
4.17	Things to Note	35	
4.17.	1 Errata on i.MX RT1052 silicon rev A0	35	
4.17.	2 Three signals in SD interface not available at TPx access pads	35	
5	Troubleshooting	36	
5.1	No SWD/JTAG Connection		
5.2	Powering		

5.3	Contact with OEM Board MCU	36
5.4	Using the Demo Application to Verify Correct Operation	36
6	Different Board Versions	37
7	Disclaimers	38
7.1	Definition of Document Status	39

1 Document Revision History

Revision	Date	Description
PA2	2017-12-19	First released version.
PA3	2018-01-01	Moved program development information to separate document.
PA4	2018-02-05	Added information about SWO trace.
PA5	2018-02-12	Minor corrections on cross-reference.
PA6	2018-04-17	Updated information on i.MX RT1052 silicon revision A0 and A1.

2 Getting Started

This chapter contains information about how to get acquainted with the *iMX RT1052 Developer's Kit.*Please read this chapter first before you start using the board - it will be well spent time!

First of all, thank you for buying Embedded Artists' *iMX RT1052 Developer's Kit* based on NXP's ARM Cortex-M7 i.MX RT1052 microcontroller. **You will get up-and-running quickly!**

- Section 2.1 contains a list of what is included in the *iMX RT1052 Developer's Kit*.
- Section 2.2 together with Figure 10 in chapter 4 gives an overview presentation of the hardware and the interfaces and connectors available.
- Section 2.3 describes how to connect the USB cable, install the UART-to-USB driver and powerup the board the first time.
- Section 2.4 presents an alternative powering solution via a 5V DC power jack.
- Section 2.5 2.9 describes important handling and care issues.
- Section 2.10 contains information about further reading.
- Chapter 3 describes the demo application that can also be used to verify correct operation of the hardware.
- Chapter 4 presents the hardware design of the *iMX OEM Carrier Board*.
- Chapter 5 contains some troubleshooting information.

To get started with program development and program download/flashing, see document *iMX RT1052 Developer's Kit Program Development Guide*.

2.1 iMX RT1052 Developer's Kit Content

- One iMX RT1052 COM board mounted on one iMX COM Carrier board, see Figure 1 below.
- One micro-B to A USB cable
- A product serial number that you should register on Embedded Artists' support page. First
 create a support account and then register the product serial number to this account. On the
 support page for the iMX RT1052 Developer's Kit you will get access to the schematic of the
 iMX OEM Carrier board, datasheets, etc.

For program development you will additionally need a debug probe, like ULINK, J-LINK or LPC-Link2, and a program development environment, like Keil uVision or MCUXpresso. For more information about program development, see document *iMX RT1052 Developer's Kit Program Development Guide*.

2.2 Hardware Overview

Figure 1 below illustrates the top side of the *iMX OEM Carrier board*. Some key components, relevant to get started, have been marked in the picture.



Figure 1 - iMX RT1052 Developer's Kit Top View

- UART-to-USB bridge this gives access to the console UART of the i.MX RT1052 MCU. The USB interface can also power the board.
- 2. USB OTG connector this USB interface can, together with a PC application, be used to program the flash memory of the *iMX RT1052 OEM board*.
- Reset push-button a press will generate a power cycle (in default behavior).
- 4. ON/OFF push-button a 5 second press will shut down the main 3.3V supply.
- ISP Enable push-button pressing this button while the board power up will place the i.MX RT1052 is ISP mode (typically used for programming the iMX RT1052 OEM board flash memory).
- 6. Alternative power supply input this is an alternative power supply input, 5V/1A DC, via a 2.1mm power jack. The center position is the positive terminal.

Figure 10 in chapter 4 gives a slightly more detailed presentation of all interfaces and connectors on the board.

Figure 2 below illustrates the bottom side of the iMX OEM Carrier board. A 4.3 inch LCD with capacitive touch panel is mounted there. When developing graphical applications, this side is typically turned up.



Figure 2 - iMX RT1052 Developer's Kit Bottom View

2.3 Connecting - Get Started

Begin by connecting the micro-B USB connector to position 1) in Figure 1 above. Connect the other end of the USB cable to the PC. The pre-loaded demo application will immediately start, showing some graphics on the LCD (on the bottom side). The demo application is described in more detail in section 3.

Meanwhile the PC will typically install drivers automatically for the UART-to-USB bridge that creates a Virtual COM port, if they are not already installed. If you have problems the drivers can be downloaded from the links below:

http://www.ftdichip.com/Drivers/VCP.htm http://www.ftdichip.com/Support/Documents/InstallGuides.htm

When the driver has been installed, a new COM port will listed under "Ports" in the Device Manager as shown in Figure 3. Please note that the actual port number will most likely be different on your computer.



Figure 3 – Virtual COM port shown in device manager

The next step is to open a terminal application and attached it to the Virtual COM port that has just been created. The baud rate should be **115200**.

Some development environments/IDEs have a built-in terminal application that can be used. Sometimes it is better to have a terminal application with more features. For increased flexibility, we recommend using any of the two alternative terminal applications presented in the following subsections.

2.3.1 Tera Term Terminal Emulation Application

We recommend that you use **Tera Term** which can be downloaded and installed from either of the links below.

https://ttssh2.osdn.jp/index.html.en http://sourceforge.jp/projects/ttssh2/releases/

Launch *Tera Term*. The first time it launches, it will show you the following dialog. Select the serial option. Assuming the USB cable is connected to the *iMX OEM Carrier Board*, there should be a COM port automatically populated in the list.

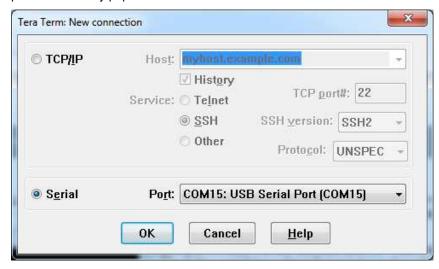


Figure 4 - Tera Term New Connection Window

Configure the serial port settings (using the COM port number identified earlier) to 115200 baud rate, 8 data bits, no parity and 1 stop bit. To do this, go to Setup \rightarrow Serial Port and change the settings.

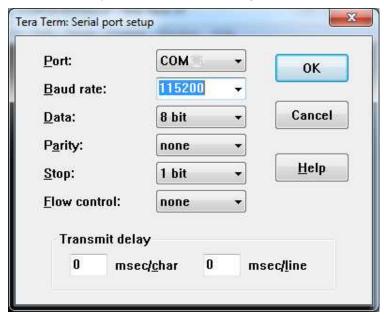


Figure 5 - Tera Term Serial Port Setup

Verify that the connection is open. If connected, *Tera Term* will show something like below in its title bar.



Figure 6 - Tera Term Menu

2.3.2 PuTTY terminal emulation application

Alternatively you can use **PuTTY**. It is another commonly used terminal emulation application. PuTTY can be downloaded and installed from the link below.

http://www.chiark.greenend.org.uk/~sgtatham/putty/download.html

Launch PuTTY by either double clicking on the *.exe file you downloaded or from the Start menu, depending on the type of download you selected.

In the window that launches, select the Serial radio button and enter the COM port number that you determined earlier. Also enter the baud rate, in this case 115200.

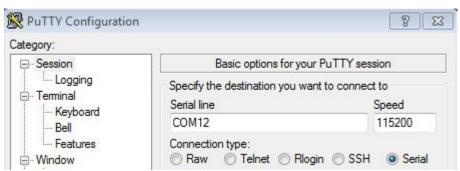


Figure 7 – PuTTY New Session Configuration

Click Open to open the serial connection. Assuming the FTDI cable is connected and you entered the correct COM port, the terminal window will open. If the configuration is not correct, PuTTY will alert you.

2.4 Alternative Powering

The board is normally powered from a PC/laptop via the included USB cable (micro-B to A cable) when connected to position 1) in Figure 1 above. This setup will work in most cases. However note that not all PC/laptops can provide the needed current. A powered USB hub can be used in this case or an external power supply.

A separate power supply is always needed in stand-alone situations or when running USB Host application that require powering external USB devices. If USB Host is not used (or used with very low external current consumption), a 5V DC/1 Amp power supply (5W) is all what is needed. If USB Host is used with high external current consumption a 5V DC/2-2.5 Amp power supply (10-12.5W) is needed. A 2.1mm standard power plug is used to interface the board. Center position is the positive terminal.

It is possible to have both the USB cable and external powering connected to the board at the same time but only one of them will be used at any time. Powering selector, J29, selects either +5V DC powering via J1 or USB powering via J22. See Figure 8 below where to locate J29.

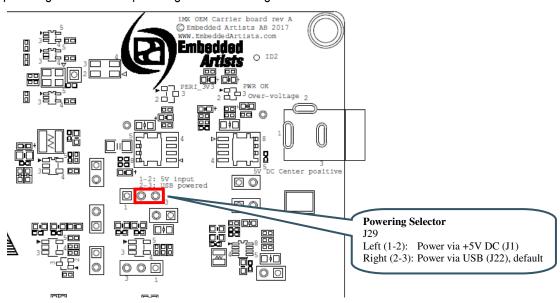


Figure 8 – Location of Powering Selector, J29

2.5 ESD Precaution

Please note that the *iMX RT1052 OEM Board* and *iMX OEM Carrier Board* come without any case/box and all components are exposed for finger touches – and therefore extra attention must be paid to ESD (electrostatic discharge) precaution.

Make it a habit always to first touch the metal surface of one of the USB, SD or Ethernet connectors for a few seconds with both hands before touching any other parts of the boards. That way, you will have the same potential as the board and therefore minimize the risk for ESD.



Never touch directly on the *iMX RT1052 OEM Board* and in general as little as possible on the *iMX OEM Carrier Board*. The push-buttons on the *iMX OEM Carrier Board* have grounded shields to minimize the effect of ESD.

Note that Embedded Artists does not replace boards that have been damaged by ESD.

2.6 General Handling Care

Handle the *iMX RT1052 OEM Board* and *iMX OEM Carrier Board* with care. The boards are not mounted in a protective case/box and are not designed for rough physical handling. Connectors can wear out after excessive use. The *iMX OEM Carrier Board* is designed for prototyping use, and not for integration into an end-product.

For boards with LCD, do not exercise excessive pressure on the LCD glass area. That will damage the display. Also, do not apply pressure on the flex cables connecting the LCD/touch screen. These are relatively sensitive and can be damaged if too much pressure is applied to them.

Note that Embedded Artists does not replace boards where the LCD has been improperly handled.

2.7 Handling SO-DIMM Boards

See picture below for instructions about how to mount/remove the *OEM Board* in the SODIMM connector of the *iMX OEM Carrier Board*.

To install the *OEM Board*, align it to the socket (1). Push the board gently, and with even force between the board edges, fully into the socket (2). Then push the board down in a rotating move (3) until it snaps into place (4). The *OEM Board* shall lie flat and parallel to the base board.

To remove the *OEM Board*, spread the two arms of the SO-DIMM socket apart slightly. The board will pop up (5). Gently rise the board in a rotating move (6) and then extract the board from the socket (7). Apply even force between board edges when removing so that the board is removed parallel to the locking arms.

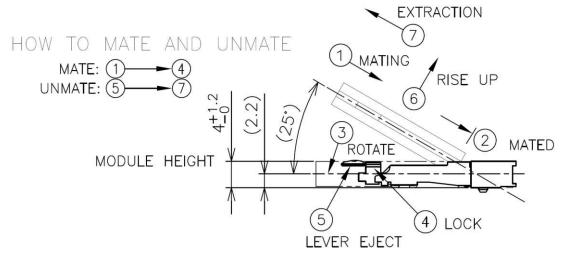


Figure 9 - Instructions how to Mount/Remove the an OEM Board

Do not forget to follow standard ESD precaution routines when mounting/removing the *OEM Board*. Most signals exposed on the 200 edge contact fingers on the SO-DIMM board are unprotected. Maintain the same electrical potential of the *OEM Board* (to be mounted) and the base board. Do not touch the *OEM Board* edge connectors. Handle the *OEM Board* only by the three other edges. Also, do not touch the components on the board.

2.8 OTP Fuse Programming

The i.MX RT1052 MCU has on-chip OTP fuses that can be programmed, see NXP documents *IMXRT1050CEC*, *.MX RT1050 Crossover Processors for Consumer Products - Data Sheet* and *IMXRT1050RM*, *i.MX RT1050 Processor Reference Manual* for details. Once programmed, there is no possibility to reprogram them.

iMX RT1052 OEM Boards are delivered without any OTP fuse programming. It is completely up to the OEM board user to decide if OTP fuses shall be programmed and in that case, which ones.

Note that Embedded Artists does not replace iMX RT1052 OEM Boards because of wrong OTP programming. It's the user's responsibility to be absolutely certain before OTP programming and not to program the fuses by accident.

2.9 CE Assessment

The *iMX RT1052 Developers Kit* (consisting of the *iMX RT1052 OEM Board* and *iMX OEM Carrier Board*) is CE marked. See separate *CE Declaration of Conformity* document.

The *iMX RT1052 Developers Kit* is a class A product. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

EMC emission test has been performed on the *iMX RT1052 Developers Kit*. Standard interfaces like Ethernet, USB, serial have been in use. General expansion connectors where internal signals are made available (for example processor pins) have been left unconnected. Connecting other devices to the product via the general expansion connectors may alter EMC emission. It is the user's responsibility to make sure EMC emission limits are not exceeded when connecting other devices to the general expansion connectors of the *iMX RT1052 Developers Kit*.

Due to the nature of the *iMX RT1052 Developers Kit* – an evaluation board not for integration into an end-product – fast transient immunity tests and conducted radio-frequency immunity tests have not been executed. Externally connected cables are assumed to be less than 3 meters. The general expansion connectors where internal signals are made available do not have any other ESD protection than from the chip themselves. Observe ESD precaution.

Note that the *iMX RT1052 OEM* board is classified as a component and is hence not CE marked separately. It can perform different functions in different integrations and it does not have a direct function. It is therefore not in the scope of the CE Directive. An end product, where an *OEM Board* is integration into, is however very likely to need CE marking.

2.10 Further Information

The following NXP documents are important reference documents and should be consulted for functional details:

- IMXRT1050CEC, .MX RT1050 Crossover Processors for Consumer Products Data Sheet, latest revision
- IMXRT1050IEC, .MX RT1050 Crossover Processors for Industrial Products Data Sheet, latest revision
- IMXRT1050RM, i.MX RT1050 Processor Reference Manual, latest revision
- IMXRT1050CE, Chip Errata for the i.MX RT1050, latest revision
 Note: It is the user's responsibility to make sure all errata published by the manufacturer are taken note of. The manufacturer's advice should be followed.
- AN12094, Power consumption and measurement of i.MXRT1050, latest revision

3 Demo Application

The *iMX RT1052 Developer's Kit* comes pre-loaded with a demo application. If the demo application is missing or over-written by another application, follow the guidelines for how to download a program in the document *iMX RT1052 Developer's Kit Program Development Guide*. A pre-compiled binary file is available on the support site. It can be downloaded with the Mfgtool application.

The demo application demonstrates some features of the *iMX RT1052* Developer's Kit (note that early versions of the Demo application might not support all tests/demos - make sure the latest version is downloaded if the demo application shall be used to verify correct operation of the different interfaces):

- A picture is displayed on the LCD.
- The capacitive touch panel (on the LCD) writes small black dots (one pixel in size) whenever a touch event is detected. Detected coordinates are also printed in the console.
- An audible tone is sent to the headphone interface.
- It is possible to Ping the Ethernet interface. The IP address is hard coded to 192.168.6.231.
 Received ping requests from a pc are printed on the console. After 5 received pings it stops writing received pings.
- USB Host, via the USB Host connector J13. It is possible to insert a Mass-storage Device (MSC), i.e., USB memory stock, that is detected.
- USB Device, via the USB OTG connector J12. When connected to a Windows PC, the USB interface behaves like a mouse and move the cursor in a square patter.
- It is possible to insert a SD memory card that is detected.
- Presses on WAKE and ISP Enable push buttons are detected
- UART and USB-to-UART bridge; a message is printed on the UART. It will look something like this (date and version number will likely be different):

4 iMX OEM Carrier Board Design

This chapter contains information about the peripherals and general design of the *iMX OEM Carrier Board* and how to set the different jumpers on the board. The schematic can be downloaded in pdf format from the support page, and is recommended to have printed out while reading this chapter.

Section naming begins with SPx, which is short for Schematic Page x.

The picture below gives an overview of the iMX OEM Carrier Board design.

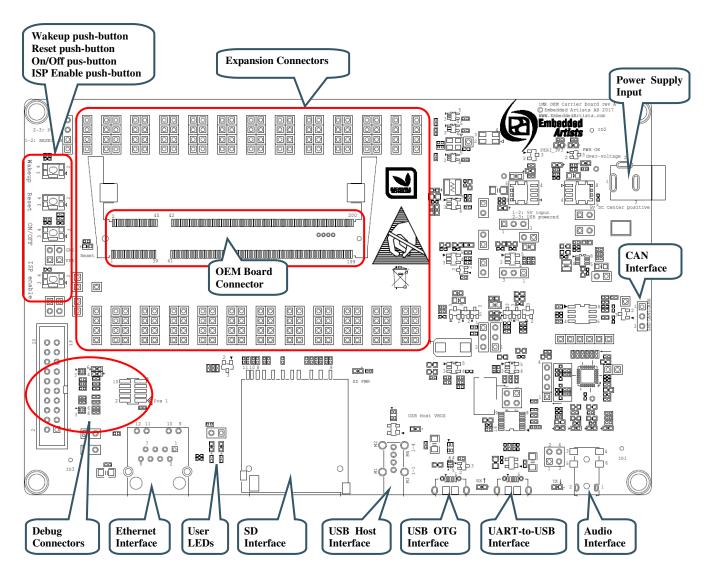


Figure 10 - iMX OEM Carrier Board Overview

4.1 Modifications to iMX OEM Carrier Board

The *iMX OEM Carrier Board* has been designed to be flexible. Most options can be controlled via jumpers but some options might need soldering.

Note that modifications to the board are done at own risk and void all warranties.

4.2 SP2a: Power Supplies

The power supply structure on the *iMX OEM Carrier board* is straight forward. There are two powering sources, selected via J29:

- 1. +5V DC via power jack (J1). 2.1mm inner diameter, 5.5mm outer diameter, center pin positive. This input has reverse polarity and over-voltage protection.
 - This powering source is always needed in stand-alone situations or when running USB Host application that require powering external USB devices. If USB Host is not used (or used with very low external current consumption), a 5V DC/1 Amp power supply (5W) is all what is needed. If USB Host is used with high external current consumption a 5V DC/2-2.5 Amp power supply (10-12.5W) is needed.
- 2. USB powering via the UART-to-USB Bridge connector (J22).
 - The board is normally powered from a PC/laptop via the included USB cable (micro-B to A cable) when connected to J22 (position 1) in Figure 1). This setup will work in most cases. However note that not all PC/laptops can provide the needed current. A powered USB hub or an external power supply can be used in this case.

It is possible to have both the USB cable and external powering connected to the board at the same time but only one of them will be used at any time. Powering selector, J29, selects either of the two powering sources. See Figure 12 below where to locate J29.

The picture below is principal schematic of the powering structure of the *iMX OEM Carrier board*.

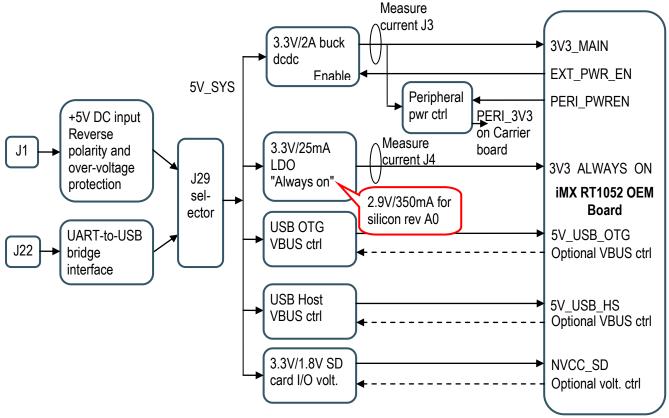


Figure 11 - Principal Powering Schematic

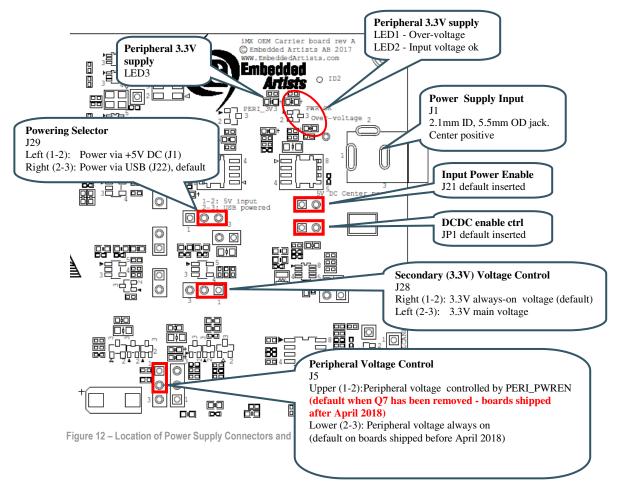
There are two voltage regulators:

- 3.3V/2A that is the main supply voltage for both the iMX RT1052 OEM board and the iMX OEM Carrier board.
 - This is a buck dcdc regulator to minimize power dissipation. The regulator can be switched on/off by control signal EXT_PWR_EN.
- 3.3V/25mA that is the secondary supply voltage that is "always on". It powers the VDD_SNVS_IN supply input on the i.MX RT1052. Current consumption is typically very low on this supply. This is a linear regulator.

Note that due to the erratas on i.MX RT1052 rev A0 silicon, the secondary supply voltage is 2.9V instead of 3.3V and it has much higher current rating (350 mA) than normally needed.

The *iMX OEM Carrier board* supports both 2.9 and 3.3V on VIN_ALWAYSON. When R25 (2.2Mohm) is mounted the voltage is 2.9V and when R25 is removed the voltage is 3.3V.

The picture below illustrates the location of relevant jumpers and LEDs.



4.3 SP2b: Current Measurement

It is possible to measure both the main and secondary supply currents on the *iMX OEM Carrier board*.

- J3 allows measuring the main 3.3V supply by measuring the voltage drop over a 100 milliohm resistor
- J4 allows measuring the secondary 3.3V supply by measuring the voltage drop over a 500 milliohm resistor.

200 milliampere over a 100/500 milliohm resistor will give a 20/100 mV drop. A current sense series resistor is a trade-off between keeping the sense voltage high enough (in all operating conditions) while keeping the voltage drop low (in the highest current consumption). Insert the short circuit jumpers in J3 and J4, respectively, to remove the effect of the current sense resistors voltage drop.

It is possible to replace the series resistors with more suitable values for your specific application. The arrows indicate the current flow direction.

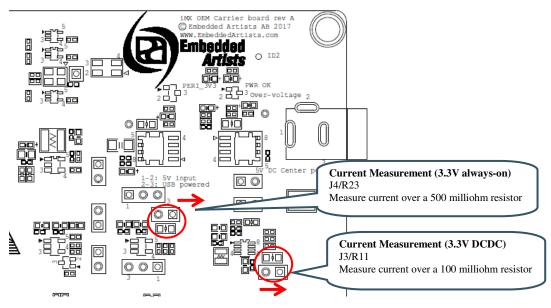


Figure 13 - Location of Current Measurement Connectors J3 and J4

Note that the main 3.3V supply powers both the *iMX RT1052 OEM board* and peripherals on the *iMX OEM Carrier board*, like LCD, SD interface, Audio codec, CAN interface, LEDs, etc. For better discrimination and resolution of current measurement, it is possible to measure currents on the *iMX RT1052 OEM board* also. The following detailed measurements of currents on the *iMX RT1052 OEM board* can be done:

- J1 allows measuring the 3.3V supply to DCDC_IN by measuring the voltage drop over a 20 milliohm resistor.
- J2 allows measuring the VDD_SOC_IN core current by measuring the voltage drop over a 20 milliohm resistor.
- J3 allows measuring the 3.3V supply to VDD_HIGH_IN by measuring the voltage drop over a 20 milliohm resistor.
- J4 allows measuring the VBAT current to VDD_SNVS_IN by measuring the voltage drop over a 1 ohm resistor.
- J5 allows measuring the 3.3V supply to VDD_ADC_3P3 by measuring the voltage drop over a 20 milliohm resistor.

• J6 allows measuring the internal 1.8V supply to the EcoXiP flash by measuring the voltage drop over a 20 milliohm resistor.

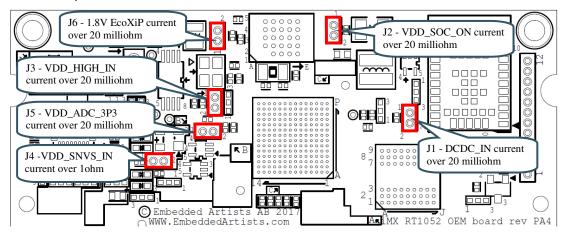


Figure 14 – Location of iMX RT1052 OEM Board Current Measurement Pads

4.4 SP2c: VBAT Powering

The picture below illustrates where the VBAT input connector is (J2) and where the current can be measured over a 1 kohm resistor, J26. The arrow indicates the current flow direction.

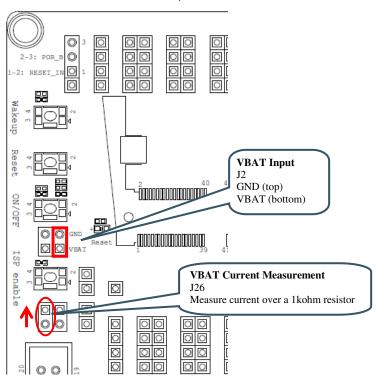


Figure 15 – Location of VBAT Connector and VBAT Current Measurement Pads

4.5 SP3-SP4: OEM Board Connector (SODIMM) and Access Pads

The *iMX RT1052 OEM board* connector is a standard DDR2 SO-DIMM socket with 200 positions and 0.6mm pitch. It has 1.8V keying (which is what DDR2 stands for). The JEDEC standard defining the DDR2 SODIMM boards is called JEDEC MO-224.

All relevant OEM board signals are available to access via the pads that are located in a 100 mil grid pattern around the SODIMM connector, J1. The access pads are located close to the SO-DIMM connectors in order to minimize track lengths.

Note that some *iMX OEM Carrier board* circuits may need to be disconnected before used externally. Carefully investigate the need for this before using a signal for external expansion.

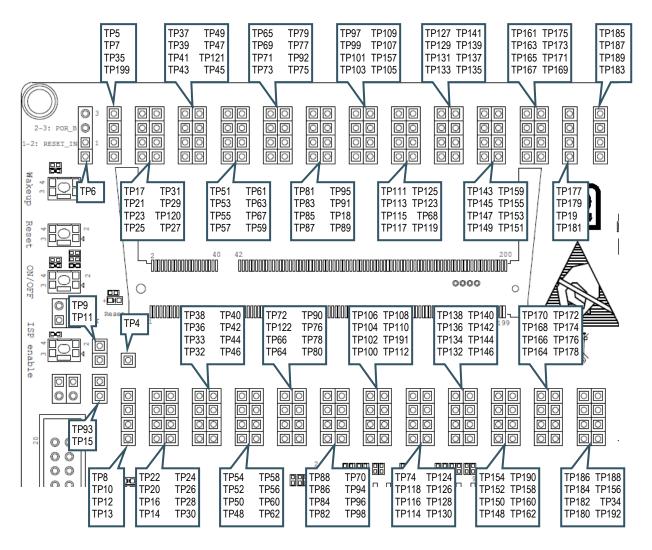


Figure 16 - Location of SODIMM connector, J1

4.6 SP5: Push-buttons and LEDs

There are four push-buttons, as presented in the picture below. The reset push-button, SW3, can be configured to control one of two different signals. via JP8.

- Jumper in 1-2 position on JP8, controls signal RESET_IN, which results in a complete power cycle of main 3.3V power supply. This is the default jumper position.
- Jumper in 1-2 position on JP8, controls signal POR_B, which results in a so called warm reset
 of the i.MX RT1052 MCU.

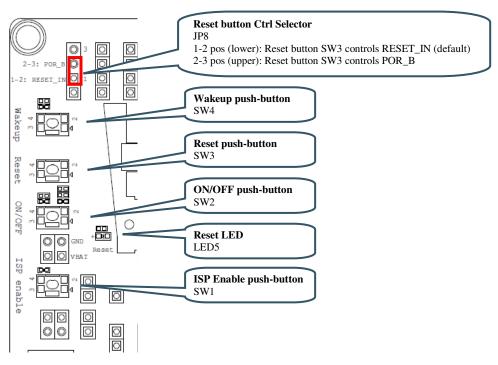


Figure 17 – Location of Push-buttons

There is also jumper to allow a specific signal (GPIO_B1_13) to control the watchdog input signal, WDOG_B. This jumper is inserted by default.

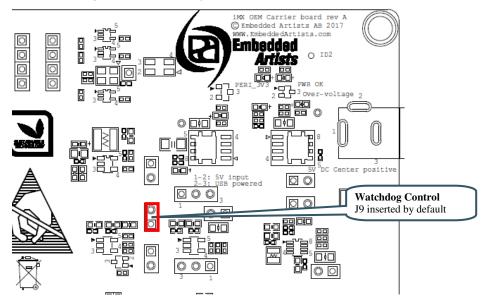


Figure 18 -Location of Watchdog Control Jumper, J9

There are two LEDs that are not connected by default to specific signals. Via jumpers to J8 any signal that is available on the access pads described in section 4.5 can be used to control the LEDs.

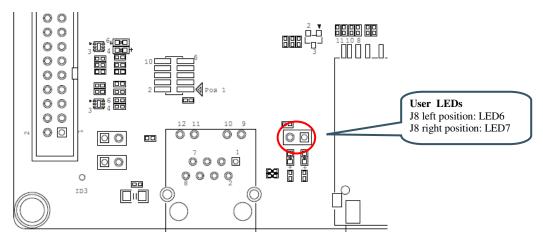


Figure 19 – Location of User LEDs

4.7 SP6: Debug Interfaces

There are two debug interface connectors available:

- J10 this is a Cortex Debug connector. It is a 2x5 pos, 50 mil pitch connector without a shroud. Be careful when inserting the debug probe cable. Position 1 is in specifically marked on the PCB silkscreen. It is located in the lower right corner, see Figure 20 below. The connector supports both the SWD and JTAG interfaces.
- J11 this is an ARM Debug connector. It is a 2x10 pos, 100 mil pitch connector with shroud.

Both connector are defined and supports both the SWD and JTAG type of debug interfaces.

Note that in order to enable the JTAG/SWD interface on the i.MX RT1052, JP5 shall **not** be shorted/inserted.

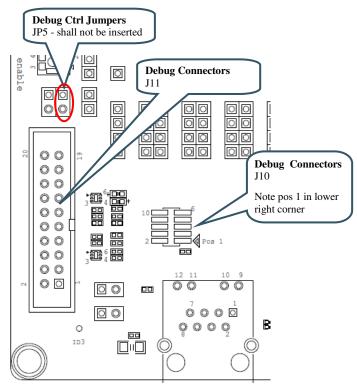


Figure 20 - Debug Interfaces

Note that due to the errata on i.MX RT1052 rev A0 silicon, the debug probe I/O voltage **MUST** follow the i.MX RT1052 I/O voltage.

The debug adapter must not drive any output higher than the Vcc/Vref voltage (and if that voltage is zero, then the debug adapter must not drive any output signal). Vcc/Vref is pin 1 on both J10 and J11.

Make sure the debug probe does not have a fixed output voltage, but rather follow Vcc/Vref. If using LPC-Link2 as debug interface, make sure there is **NO** jumper inserted in JP2.

Note about SWO trace:

Note that the i.MX RT1052 MCU does not connect the SWO trace output signal on JTAG_TDO, which would be the normal (since JTAG_TDO connect to the Cortex debug

connector where SWO in defined to be connected). Instead pin GPIO_B0_13-BOOT_CFG2[1]-LCDIF_D9 carries the SWO output as pin multiplexing alternative 2.

If you need to use SWO trace, the solution is to connect TP15 (JTAG_TDO) and TP83 (GPIO_B0_13-BOOT_CFG2[1]-LCDIF_D9) with a wire. See Figure 16 to locate these two access pads.

Also, the SWO pin must be initialized correctly in file: pin mux.c

Add the define below and add the call to IOMUXC_SetPinMux() and IOMUXC SetPinConfig() in function BOARD InitPins(), as outlined below.

```
#define IOMUXC GPIO B0 13 SWO 0x401F8170U, 0x2U, 0, 0, 0x401F8360U
void BOARD InitPins(void) {
/* GPIO B0_13 is configured as SWO (ALT2)
  Sw Input On Field: Input Path is determined by functionality */
 IOMUXC_SetPinMux(
     IOMUXC GPIO BO 13 SWO,
 /* GPIO B0 13 PAD functional properties :
    Slew Rate Field: Fast Slew Rate
    Drive Strength Field: R0/6
    Speed Field: max(200MHz)
    Open Drain Enable Field: Open Drain Disabled
    Pull / Keep Enable Field: Pull/Keeper Enabled
    Pull / Keep Select Field: Keeper
    Pull Up / Down Config. Field: 100K Ohm Pull Down
    Hyst. Enable Field: Hysteresis Disabled */
 IOMUXC SetPinConfig(
     IOMUXC_GPIO_B0_13_SWO,
     0x10F1u);
}
```

4.8 SP7: USB Interfaces

The iMX OEM Carrier board has two USB interfaces, one USB Host and one USB OTG:

- The USB Host interface has a USB-A connector, J13. By default this interface has VBUS power enable always on. LED8 indicates the presence of +5V VBUS for the interface. This USB interface is connected to i.MX RT1052 USB interface #2.
- The USB OTG interface has a micro-AB connector, J12. By default this interface has VBUS control via the ID-pin. This USB interface is connected to i.MX RT1052 USB interface #1.
 - If no cable is inserted or when a cable with ID pin floating is inserted, VBUS will not be powered on the J12 connector.
 - If a cable with the ID pin grounded is inserted VBUS will be turned on, i.e., powered with 5V. This is the typical situation when connecting a mini-AB to USB-A USB cable, i.e., when wanting the USB OTG interface to behave like a USB Host that can possible also power an external device via VBUS.

See picture below for locating relevant components..

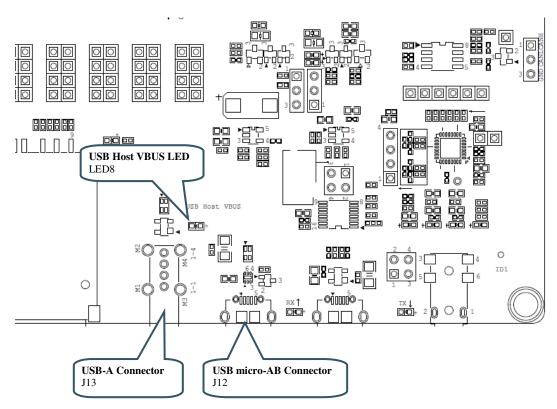


Figure 21 - Location of USB Interfaces