# imall

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#### 1. Global joint venture starts operations as WeEn Semiconductors

Dear customer,

As from November 9th, 2015 NXP Semiconductors N.V. and Beijing JianGuang Asset Management Co. Ltd established Bipolar Power joint venture (JV), **WeEn Semiconductors**, which will be used in future Bipolar Power documents together with new contact details.

In this document where the previous NXP references remain, please use the new links as shown below.

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Thank you for your cooperation and understanding,

WeEn Semiconductors





#### 1. General description

Planar passivated ultra sensitive gate Silicon Controlled Rectifier in a SOT54 (T0-92) plastic package.

#### 2. Features and benefits

- Planar passivated for voltage ruggedness and reliability
- Ultra sensitive gate

#### 3. Applications

- Electronic ballasts
- Safety shut down and protection circuits
- Sensing circuits
- Smoke detectors
- Switched Mode Power Supplies

#### 4. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
V <sub>DRM</sub>	repetitive peak off- state voltage		-	-	400	V
V <sub>RRM</sub>	repetitive peak reverse voltage		-	-	400	V
I <sub>TSM</sub>	non-repetitive peak on- state current	half sine wave; $T_{j(init)} = 25 \text{ °C};$ $t_p = 10 \text{ ms}; \text{Fig. 4}; \text{Fig. 5}$	-	-	8	A
I <sub>T(AV)</sub>	average on-state current	half sine wave; T <sub>lead</sub> ≤ 92 °C; <u>Fig. 1</u>	-	-	0.5	A
I <sub>T(RMS)</sub>	RMS on-state current	half sine wave; T <sub>lead</sub> ≤ 92 °C; <u>Fig. 2;</u> <u>Fig. 3</u>	-	-	0.8	A
Static chara	acteristics	· · · · · ·	I.			
I <sub>GT</sub>	gate trigger current	$V_D = 12 \text{ V}; \text{ I}_T = 10 \text{ mA}; \text{ T}_j = 25 \text{ °C};$ Fig. 7	-	3	12	μA

### 5. Pinning information

Table 2		information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	к	cathode		A H K
2	G	gate		G sym037
3	A	anode		
			TO-92 (SOT54)	

## 6. Ordering information

Table 3. Ordering information					
Type number	Package				
	Name	Description	Version		
EC103D1	TO-92	plastic single-ended leaded (through hole) package; 3 leads	SOT54		

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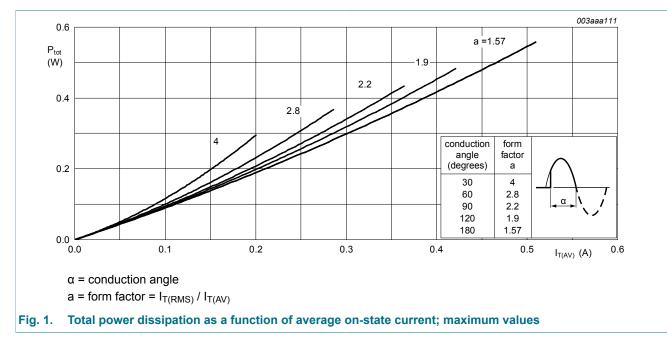
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#### 7. Limiting values

#### Table 4.Limiting values

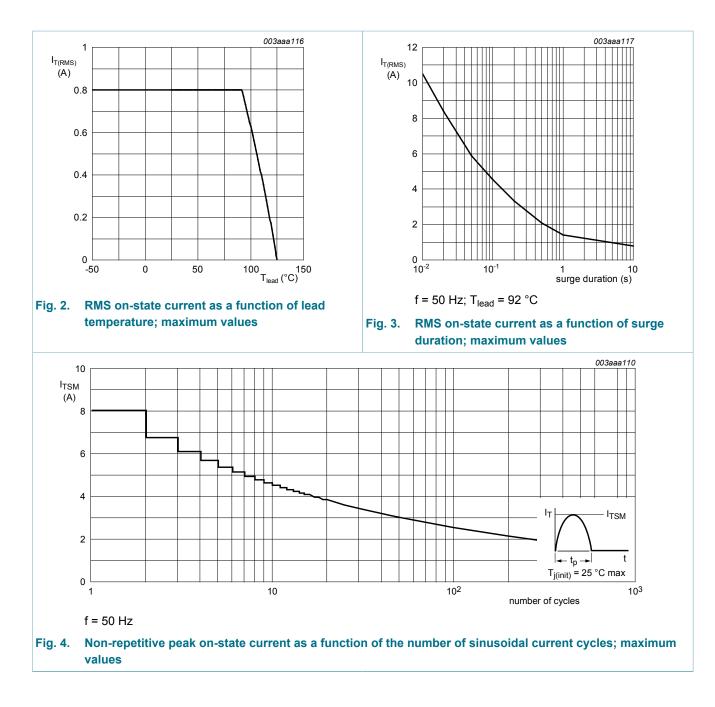
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>DRM</sub>	repetitive peak off-state voltage		-	400	V
V <sub>RRM</sub>	repetitive peak reverse voltage		-	400	V
I <sub>T(AV)</sub>	average on-state current	half sine wave; $T_{lead} \le 92 \text{ °C}$ ; Fig. 1	-	0.5	А
I <sub>T(RMS)</sub>	RMS on-state current	half sine wave; $T_{lead} \le 92$ °C; Fig. 2; Fig. 3	-	0.8	А
I <sub>TSM</sub>	non-repetitive peak on-state current	half sine wave; $T_{j(init)} = 25 \text{ °C};$ $t_p = 10 \text{ ms}; \text{ Fig. 4}; \text{ Fig. 5}$	-	8	A
		half sine wave; $T_{j(init)}$ = 25 °C; $t_p$ = 8.3 ms	-	9	A
l <sup>2</sup> t	I <sup>2</sup> t for fusing	t <sub>p</sub> = 10 ms; SIN	-	0.32	A <sup>2</sup> s
dl <sub>T</sub> /dt	rate of rise of on-state current	$I_T = 2 \text{ A}; I_G = 10 \text{ mA}; \text{ d}I_G/\text{d}t = 100 \text{ mA}/\mu \text{s}$	-	50	A/µs
I <sub>GM</sub>	peak gate current		-	1	А
V <sub>RGM</sub>	peak reverse gate voltage		-	5	V
P <sub>GM</sub>	peak gate power		-	2	W
P <sub>G(AV)</sub>	average gate power	over any 20 ms period	-	0.1	W
T <sub>stg</sub>	storage temperature		-40	150	°C
Tj	junction temperature		-	125	°C



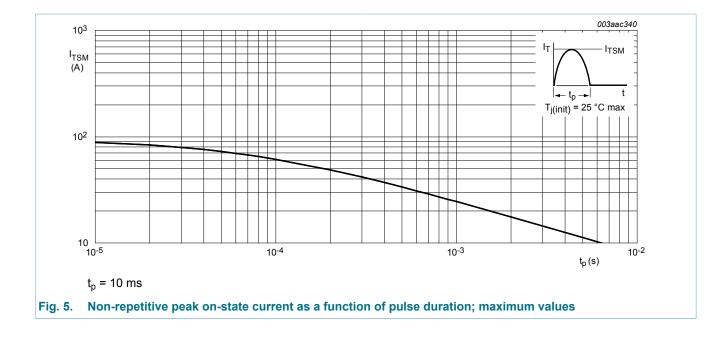
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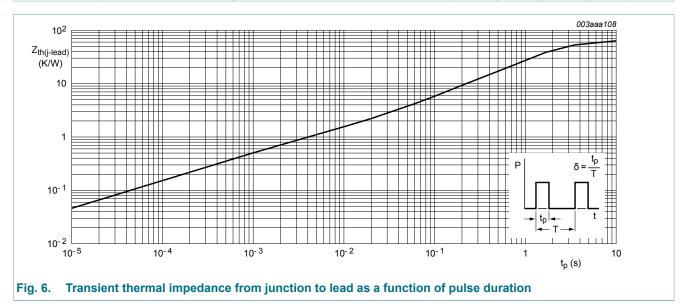


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#### 8. Thermal characteristics

Table 5. Thermal characteristics							
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{\text{th(j-lead)}}$	thermal resistance from junction to lead	Fig. 6		-	-	60	K/W
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	printed circuit board mounted: lead length = 4 mm		-	150	-	K/W



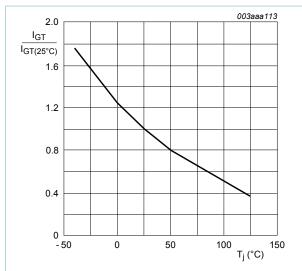
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#### 9. Characteristics

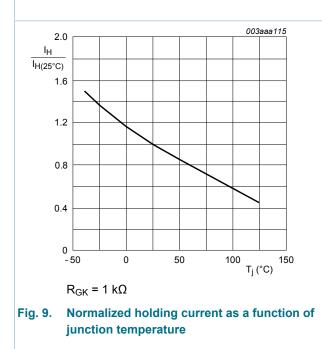
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static char	acteristics	II				
I <sub>GT</sub>	gate trigger current	$V_D = 12 V; I_T = 10 mA; T_j = 25 °C;$ - 3 Fig. 7		3	12	μA
IL	latching current	$V_D$ = 12 V; I <sub>G</sub> = 0.5 mA; R <sub>GK</sub> = 1 kΩ; T <sub>j</sub> = 25 °C; Fig. 8	-	2	6	mA
I <sub>H</sub>	holding current	$V_D$ = 12 V; R <sub>GK</sub> = 1 kΩ; T <sub>j</sub> = 25 °C; Fig. 9	-	2	5	mA
V <sub>T</sub>	on-state voltage	I <sub>T</sub> = 1 A; T <sub>j</sub> = 25 °C; <u>Fig. 10</u>	-	1.2	1.35	V
V <sub>GT</sub>	gate trigger voltage	V <sub>D</sub> = 12 V; I <sub>T</sub> = 10 mA; T <sub>j</sub> = 25 °C; <u>Fig. 11</u>	-	0.5	0.8	V
		V <sub>D</sub> = 400 V; I <sub>T</sub> = 10 mA; T <sub>j</sub> = 125 °C; Fig. 11	0.2	0.3	-	V
ID	off-state current	$V_D$ = 400 V; $T_j$ = 125 °C; $R_{GK}$ = 1 k $\Omega$	-	0.05	0.1	mA
I <sub>R</sub>	reverse current	$V_R$ = 400 V; T <sub>j</sub> = 125 °C; R <sub>GK</sub> = 1 k $\Omega$	-	0.05	0.1	mA
Dynamic c	haracteristics	II				
dV <sub>D</sub> /dt	rate of rise of off-state voltage	$V_{DM} = 268 \text{ V}; \text{ T}_{j} = 125 \text{ °C}; \text{ R}_{GK} = 1 \text{ k}\Omega;$ (V <sub>DM</sub> = 67% of V <sub>DRM</sub> ); exponential waveform; <u>Fig. 12</u>	-	150	-	V/µs
t <sub>gt</sub>	gate-controlled turn-on time	$I_{TM} = 2 \text{ A}; \text{ V}_{\text{D}} = 400 \text{ V}; \text{ I}_{\text{G}} = 10 \text{ mA}; \text{ dI}_{\text{G}} \text{/} \\ \text{dt} = 0.1 \text{ A} \text{/} \mu \text{s}; \text{ T}_{\text{j}} = 25 ^{\circ}\text{C}$	-	2	-	μs
t <sub>q</sub>	commutated turn-off time	$\begin{split} V_{DM} &= 268 \text{ V; } T_{j} = 125 \text{ °C; } I_{TM} = 1.6 \text{ A;} \\ V_{R} &= 35 \text{ V; } (dI_{T}/dt)_{M} = 30 \text{ A/}\mu\text{s; } dV_{D}/\\ dt &= 2 \text{ V/}\mu\text{s; } R_{GK} = 1 \text{ k}\Omega\text{; } (V_{DM} = 67\% \text{ of } \\ V_{DRM}) \end{split}$	-	100	-	μs

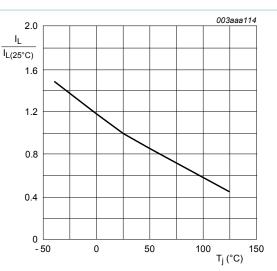
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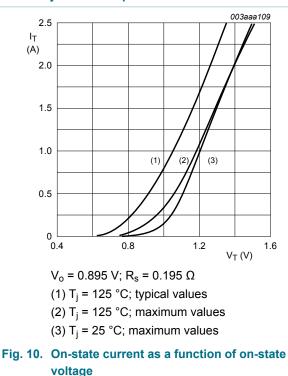






 $R_{GK}$  = 1 k $\Omega$ 





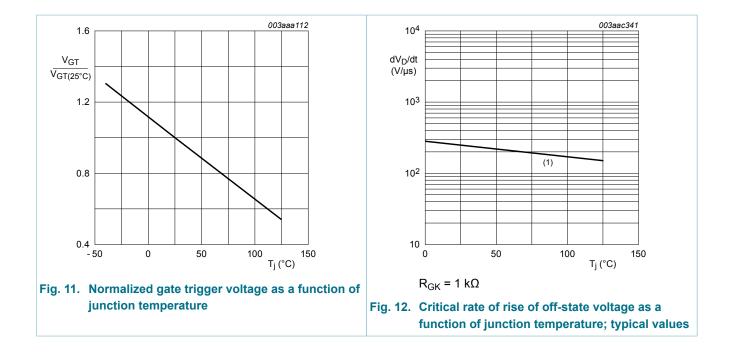
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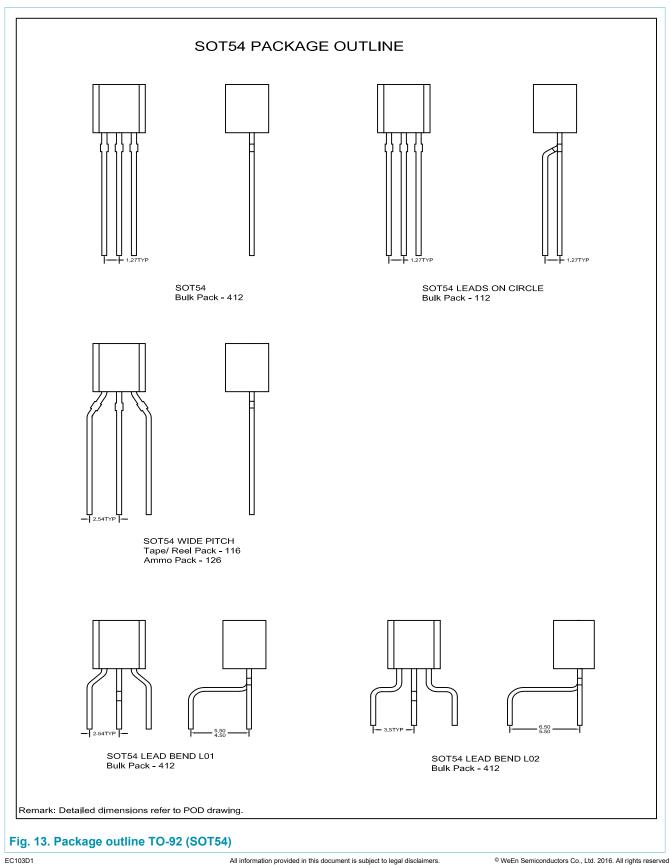
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#### 10. Package outline



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#### 11. Legal information

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Document status [1][2]	Product status [ <u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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