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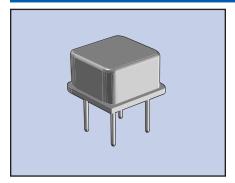






# ECS-2200X SERIES 8 PIN DIP CLOCK OSCILLATOR





The ECS-2200X Series clock oscillator can drive both HCMOS and TTL logic. This oscillator also features tri-state enable/disable capabilities in an 8 pin DIP package.

#### **FEATURES**

- 50pF HCMOS/TTL logic
- Tri-State enable/disable
- Wide frequency range
- Resistance weld package
- 3.3V operation (optional)
- PbFree/RoHS Compliant



#### **PART NUMBERING GUIDE**

PART NUMBER*	FREQUENCY STABILITY
ECS-2200AX	±100 PPM
ECS-2200BX	±50 PPM
ECS-2200CX	± 25 PPM

<sup>\*</sup> Complete part number to include frequency. i.e. ECS-2200AX-100 (100 = 10.000MHz)

#### **OPERATING CONDITIONS/ELECTRICAL CHARACTERISTICS**

PARAMETERS	FREQUENCY RANGE	CONDITIONS	MINIMUM	TYPICAL	MAXIMUM	UNITS
FREQUENCY RANGE (fo)	1.000 ~ 150.000		1.000		150.000	MHz
OPERATING TEMP. RANGE (T <sub>OPR</sub> )	1.000 ~ 150.000		0		+70	°C
STORAGE TEMP. RANGE (T <sub>STG</sub> )	1.000 ~ 150.000		-55		+125	°C
FREQUENCY STABILITY	1.000 ~ 150.000	All conditions*	-100		+100	PPM
INPUT CURRENT (IDD)	1.000 ~ 25.000			17	25	mA
	25.000 ~ 50.000			33	46	mA
	50.000 ~ 80.000			45	77	mA
	80.000 ~ 150.000			67	82	mA
OLITPLIT OVAMATTRY	1.000 ~ 80.000	50% V <sub>DD</sub> level	45	50 ±3	55	%
OUTPUT SYMMETRY	80.000 ~ 150.000	50% V <sub>DD</sub> level	40	50 ±3	60	%
RISE TIME (Tr)	1.000 ~ 150.000	10% ~ 90% V <sub>DD</sub> level			5	nS
FALL TIME (TF)	1.000 ~ 150.000	90% ~ 10% V <sub>DD</sub> level			5	nS
OUTPUT VOLTAGE (Vol) (Voh)	1.000 ~ 150.000	IoL = 16 mA			0.5	V
	1.000 ~ 150.000	Iон = -16 mA	4.5			V
OUTPUT CURRENT (IOL)	1.000 ~ 150.000	Vol = 0.5 V			16	mA
	1.000 ~ 150.000	VoH = 4.5 V			-16	mA
OUTPUT LOAD	1.000 ~ 150.000	TTL			10	TTL
	1.000 ~ 80.000	HCMOS			50	pF
	80.000 ~ 150.000	HCMOS			30	pF
START-UP TIME (Ts)	1.000 ~ 150.000	0.0V TO 5.0V			10	mS
SUPPLY VOLTAGE (VDC)		+5.0 ±0.25				VDC

<sup>\*</sup> Inclusive of 25°C tolerance, operating temperature range, input voltage change, load change, aging, shock and vibration.

### PACKAGE DIMENSIONS (mm)

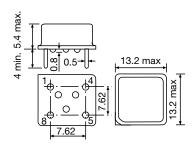


Figure 1) ECS-2200X Series — Side, Bottom and Top views

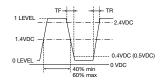


Figure 2) TTL Output Wave Form

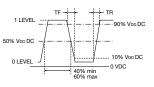


Figure 3) HCMOS Output Wave Form

PIN CONNECTIONS				
#1	TRI-STATE			
#4	CASE GROUND			
#5	OUTPUT			
#8	+5V DC			

ENABLE / DISABLE FUNCTION**				
INH (PIN 1)	OUTPUT (PIN 5)			
OPEN**	ACTIVE			
1 LEVEL VIH ≥ 2.2 V				
(VIH ≥ 2.0 V				
ABOVE 70MHz)	ACTIVE			
'0' LEVEL VIL ≤ 0.8 V	HIGH Z			

<sup>\*\*</sup> An internal pullup resistor from pin 1 to pin 8 allows active output if pin 1 is left open.