

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



## Contact us

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ED-21744-000

SHT I.I

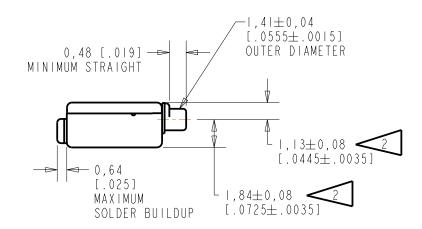
#### NOTE:

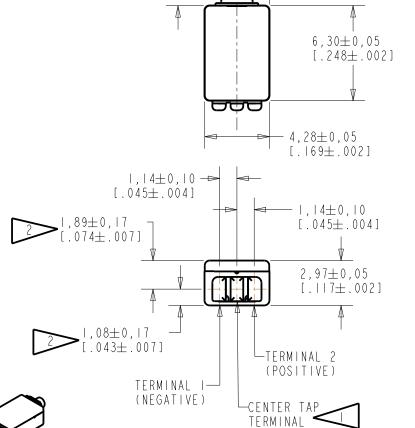
A POSITIVE GOING VOLTAGE AT TERMINAL 2, RELATIVE TO THE CENTER TAP TERMINAL, OR A NEGATIVE GOING VOLTAGE AT TERMINAL I, RELATIVE TO THE CENTER TAP TERMINAL, CAUSES A DECREASE IN PRESSURE AT THE SOUND OUTLET.

➤ LOCATED FROM TWO SURFACES FOR CUSTOMER CONVENIENCE. ONLY APPLICABLE FROM ONE SURFACE, NOT TO BE USED TOGETHER. HORIZONTAL LOCATION FOR TERMINAL CENTERED TO ±0,17 [.007].

RELEASE LEVEL

REVISION





 $2.16 \pm 0.08$ 

 $[.085\pm.003]$ 

 $1,60\pm0,05-$ 

 $[.063 \pm .002]$ 

SCALE 2:1 NOMINAL WEIGHT

DIMENSIONS IN MILLIMETERS [INCHES]

B A	C10106694 C10103542	- 7 - 07   2 - 30 - 05	Active	В	
SCALE:		DR. BY	DATE		
DO NOT SCALE DRAWING					12-30-05 DATE
TITLE:	REC	EIVER	ED-21744-000	GJP APP. BY	1 - 5 - 0 6 Date
	OUTLI	NE DRAWING	SHT I.I	GJP	1 - 5 - 06

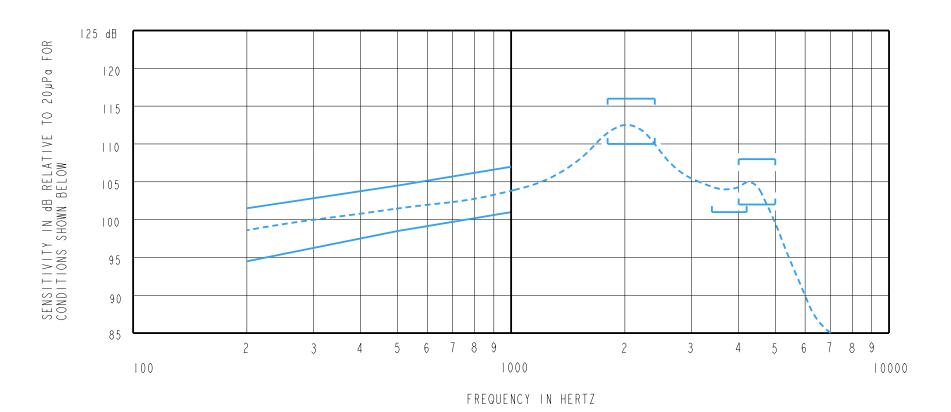
Implementation Date

C.O. #

Revision

# KNOWLES ELECTRONICS ITASCA, ILLINOIS U.S.A.

TYPE II DAMPING SHEET 2.1



### NOTES:

I. MEASUREMENTS MADE USING 10m (.394") X 1mm (.039") ID TUBE CONNECTED TO A SIMULATED ANSI S3.7-1973 TYPE HA-3 COUPLER. (IEC 126).

2.	SENSITIVITY	
FREQUENCY	MIN.	MAX.
200 500	94.5 98.5	101.5
1000	101.0	107.0
1800-2400 3400-4200	110.0	116.0
4000-5000	102.0	108.0

- 3. RESPONSE, IMPEDANCE, AND DISTORTION MEASUREMENTS MADE USING THE ELECTRICAL TEST CONDITIONS SHOWN BELOW.
- 4. ELECTRICAL SOURCE IMPEDANCE MUST BE GREATER THAN 20 TIMES IKHZ IMPEDANCE FOR TEST CONDITIONS SHOWN BELOW.
- 5. INDIVIDUAL SPECIFICATIONS.

	PORT	IMPEDANCE OHMS ±15%		DCR @20°C	DISTORTION		ELECTRICAL TEST CONDITIONS	
LOCAT	OCATION	I K H z	500Hz	OHMS ±10%	MAX. %	FREQ Hz	AC mA RMS	DC mA
	12C	3050	1700	825	5	800	0.35	0.00

Revision	C.O. #	Implementation Date	KELEASE LEVEL		REVISION
B A	C10106694 C10103542	- 7 - 07   2 - 30 - 05	Active		В
WHEN TEST LIMITS ARE USED TO ESTABLISH INCOMING INSPECTION ACCEPTANCE/REJECTION CRITERIA, CORRELATION OF TEST EQUIPMENT WITH KNOWLES IS ALSO REQUIRED FOR FILMINATION OF FOULPMENT AND TEST METHOD VARIATION					DATE   2 - 30 - 05
ELEMANTOR OF EGOTIMENT THE TEOT METHOD VANITATION					DATE
TITLE:	RF	CFIVFR	ED-21744-000	GJP	I - 5 - 0 6
	11 _	O L I V L IV		APP. BY	DATE

SHT 2.1

PERFORMANCE SPECIFICATION

## **KNOWLES ELECTRONICS** ITASCA, ILLINOIS U.S.A.