

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

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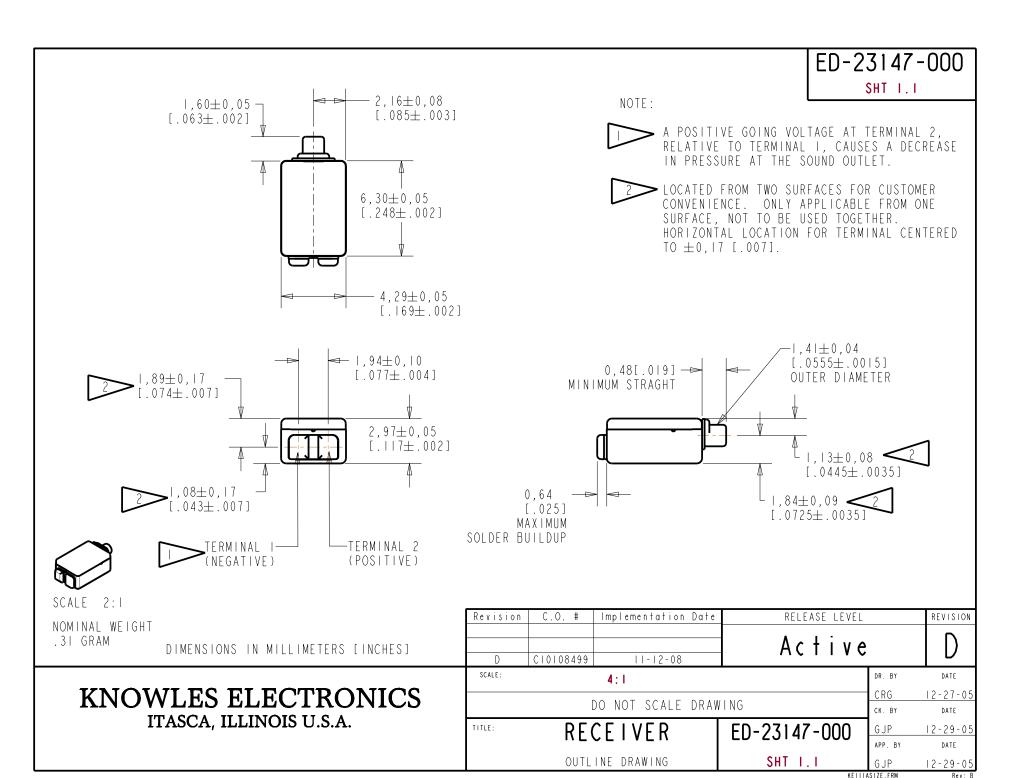
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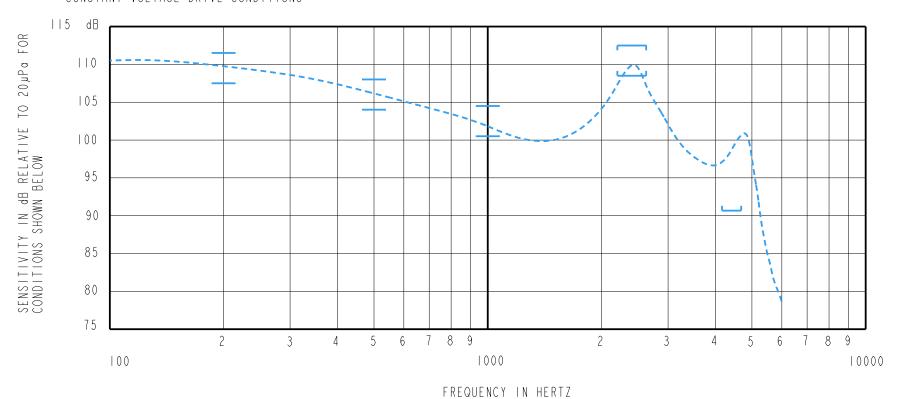


NO DAMPING

ED-23147-000

SHEET 2.1





ACOUSTICAL

SENSITIVITY

DEVICE WILL PRODUCE THE SPL LISTED BELOW UNDER TEST CONDITIONS DESCRIBED IN TABLE 3. NOMINAL SENSITIVITY AT IKHZ IS dB RELATIVE TO 20μPα. ALL OTHER VALUES IN dB RELATIVE TO THE SENSITIVITY AT IKHz.

FREQUENCY (Hz)	MINIMUM	NOMINAL	MAXIMUM
200	+5.0	+7.0	+9.0
500	+1.5	+3.5	+5.5
1000	-2.0	102.5	+2.0
2000 - 3000	+6.0	+8.0	+ 0 . 0
4400	-12.0		

PORT LOCATION: 12C

TABLE I

TOTAL HARMONIC DISTORTION

DEVICE WILL NOT EXCEED TOTAL HARMONIC DISTORTION LEVELS LISTED BELOW.

FREQUENCY (Hz)	AC DRIVE (V rms)	DC BIAS (mA)	LIMIT (%)
500	0.413	0	10
830	0.146	0	5
1250	0.146	0	5

TABLE 2

TEST CONDITIONS

NOMINAL SOURCE VOLTAGE	0.146 V rms, OmA DC BIAS
SOURCE IMPEDANCE	< I Ohm
TUBING	
COUPLER CAVITY	2 CM ³ , SIMULATED ANSI S3.7 TYPE HA-3 (IEC 126)

TABLE 3

ELECTRICAL

DC RESISTANCE	25 OHMS ± 10%
IMPEDANCE @ 500 Hz	48 OHMS ± 15%
IMPEDANCE @ kHz	87 OHMS ± 15%

TABLE 4

ISOLATION: CASE WILL BE ELECTRICALLY ISOLATED FROM THE COIL CIRCUIT.

Revision	C.O. #	Implementation Date	RELEASE LEVEL		REVISION
D	C10108499	11-12-08	Active		D
WHEN TEST LIMITS ARE USED TO ESTABLISH INCOMING INSPECTION ACCEPTANCE/REJECTION CRITERIA, CORRELATION OF TEST EQUIPMENT WITH KNOWLES IS ALSO REQUIRED FOR				DR. BY	DATE 12-27-05
ELIMINATION OF EQUIPMENT AND TEST METHOD VARIATION			CK. BY	DATE	
TITLE:	RE	CEIVER	ED-23147-000	GJP APP. BY	12-29-05 DATE
	PERFORMAI	NCE SPECIFICATION	SHT 2.1	GJP	12-29-05

KNOWLES ELECTRONICS ITASCA, ILLINOIS U.S.A.