

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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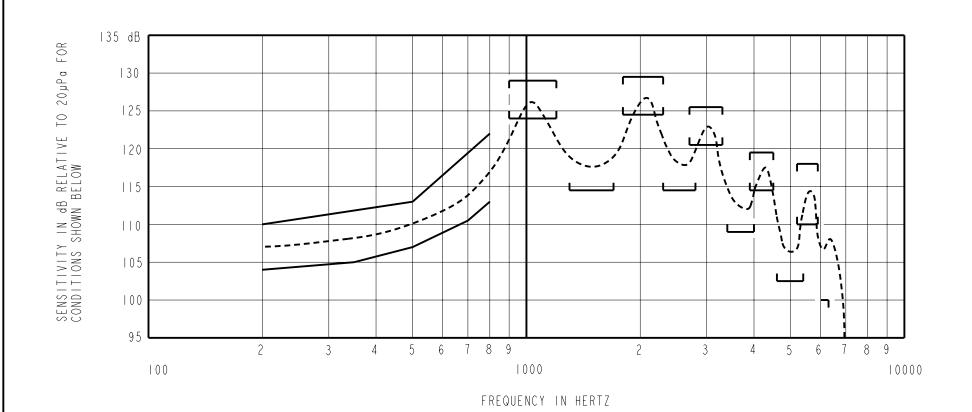




EF-21935-000 SHT I.I NOTES: -1.60 ± 0.05 $[.063\pm.002]$ 2,82±0,05 → ➤ A POSITIVE GOING VOLTAGE AT TERMINAL 2, $[.110\pm.002]$ RELATIVE TO TERMINAL I, CAUSES A DECREASE IN PRESSURE AT THE SOUND OUTLET. ➤ LOCATED FROM TWO SURFACES FOR CUSTOMER 7.87 ± 0.05 CONVENIENCE. ONLY APPLICABLE FROM ONE $[.310 \pm .002]$ SURFACE. NOT TO BE USED TOGETHER. HORIZONTAL LOCATION FOR TERMINAL CENTERED TO ± 0 , |7 [.007]. $5.59 \pm 0.05 [.220\pm.002]$ **→** 2,46±0,10 $[.097 \pm .004]$ $-1,41\pm0,04$ $[.0555\pm.0015]$ -TERMINAL 2 TERMINAL I-OUTER DIAMETER (POSITIVE) (NEGATIVE) 2,08±0,08 [.082±.003] $-4,01\pm0,05$ $[.158\pm.002]$ $1,99\pm0,08$.0785±.0030] $3,21\pm0,08$,93±0,08 **◄** $[.1265\pm.0030]$ $2,02\pm0,08$ $[.076\pm.003]$ [.0795 + .0030]0.64 0,80±0,08 < [.025] $[.0315 \pm .0030]$ CENTERTAP-MAXIMUM TERMINAL SOLDER BUILDUP Revision C.O. # Implementation Date RELEASE LEVEL REVISION NOMINAL WEIGHT .66 GRAM DIMENSIONS IN MILLIMETERS [INCHES]

KNOWLES ELECTRONICS ITASCA, ILLINOIS U.S.A.

			Release	d	ΙΑΙ	
А	C10103490	11-28-05	1.0.0400	<u> </u>		
SCALE:			DR. BY	DATE		
	[MMM CK. BY	11-28-05 DATE			
TITLE:	REG	CEIVER	EF-21935-000	GJP APP. BY	12-1-05 DATE	
	OUTL	INE DRAWING	SHT I.I	GJP ASIZE.ERM	12-1-05 Rev: B	



NOTES:

I. MEASUREMENTS MADE USING 8mm (.315") X Imm (.039") ID + 28mm (I.10") X I.5mm (.059") ID EAR HOOK SIMULATOR INTO 25mm (.984") OF 2mm (.079") ID TUBE + 18mm (.709") OF 3mm (.118") ID TUBE + 2cm³ CAVITY (AS IEC126). (T-2607 AND B & K DB0138).

2. <u>SENSITIVITY</u>

FREQUENCY	MIN.	MAX.
200	104.0	110.0
350	105.0	
500	107.0	113.0
700	110.5	
800	113.0	122.0
900-1200	124.0	129.0
1300-1700	114.5	
1800-2300	124.5	129.5
2300-2800	114.5	
2700-3300	120.5	125.5
3400-4000	109.0	
3900-4500	114.5	119.5
4600-5400	102.3	118.0
5200-5900 <6300	100.0	110.0
\U300	100.0	

- 3. RESPONSE, IMPEDANCE, AND DISTORTION MEASUREMENTS MADE USING THE ELECTRICAL TEST CONDITIONS SHOWN BELOW.
- 4. ELECTRICAL SOURCE IMPEDANCE MUST BE GREATER THAN 20 TIMES IKHZ IMPEDANCE FOR TEST CONDITIONS SHOWN BELOW.
- 5. INDIVIDUAL SPECIFICATIONS.

	PORT LOCATION	IMPEDANCE OHMS ±15%		DCR @20°C	DISTORTION		ELECTRICAL TEST CONDITIONS	
		l K H z	500Hz	OHMS ±10%	MAX. %	FREQ Hz	AC mA RMS	DC mA
	128	327	128	48.5	10	500	1.56	0.00

Kevision	C.O. #	Implementation Date	KELEASE LEVEL		KEA1210M
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			l Released		Δ
А	C10103490	11-28-05			<i>/</i> \
WHEN TEST I	IMITS ARE IIS	SED TO ESTABLISH INCOMING	INSPECTION ACCEPTANCE/REJECTION	DR. BY	DATE

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ELIMINATI	MMM ck. By	11-28-05 Date		
TITLE:	RECEIVER	EF-21935-000	GJP APP. BY	12-1-05 DATE
	DEDECOMANCE SDECIEICATION	C TUS	0.10	10 1 05