

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



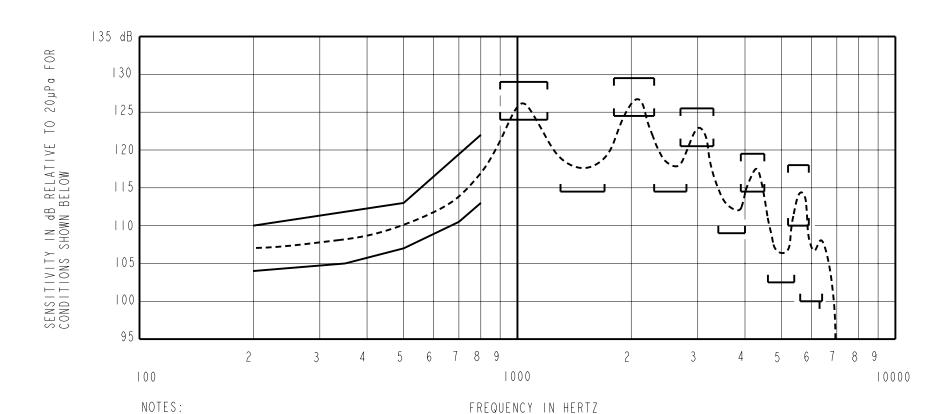




EF-27715-000 SHT I.I NOTES: 1.60 ± 0.05 $[.063 \pm .002]$ 1,83±0,08 → ➤A POSITIVE GOING VOLTAGE AT TERMINAL 2, $[.072\pm.003]$ RELATIVE TO TERMINAL I, CAUSES A DECREASE IN PRESSURE AT THE SOUND OUTLET. 7.87 ± 0.05 LOCATED FROM TWO SURFACES FOR CUSTOMER $[.310 \pm .002]$ CONVENIENCE. ONLY APPLICABLE FROM ONE SURFACE, NOT TO BE USED TOGETHER. HORIZONTAL LOCATION FOR TERMINAL CENTERED TO ± 0.17 [.007]. 5.59 ± 0.05 $[.220\pm.002]$ $-2,46\pm0,10$ $[.097\pm.004]$ $| , 4 | \pm 0, 04$ -TERMINAL 2 TERMINAL I— (NEGATIVE) $[.0555\pm.0015]$ (POSITIVE) OUTER DIAMETER 4.01 ± 0.05 $[.158 \pm .002]$ 2,08±0,08 **<** $1,99\pm0,08$ $[.082 \pm .003]$ 0.0785 ± 0.00301 $3,21\pm0,08$ 2,02±0,08 $1,93\pm0,08$ $[.1265 \pm .0030]$ $[.0795 \pm .0030]$ $[.076\pm.003]$ - 0,64 [.025] CENTERTAP └ 0,80±0,08 **<** MAXIMUM SOLDER TERMINAL $[.03|5\pm.0030]$ BUILDUP Revision C.O. # Implementation Date RELEASE LEVEL REVISION NOMINAL WEIGHT \mathbf{D} DIMENSIONS IN MILLIMETERS [INCHES] .66 GRAM

KNOWLES ELECTRONICS ITASCA, ILLINOIS U.S.A.

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A	C10103759	2 - 3 - 06	l		
SCALE:	:	2:1		DR. BY	DATE
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	NEC	_		APP. BY	DATE
	OUTLIN	E DRAWING	SHT I.I	GJP	2 - 6 - 06
	•	•		EIIIASIZE.FRM	Rev: B



I. MEASUREMENTS MADE USING 8mm (.315") X Imm (.039") ID + 28mm (I.10") X I.5mm (.059") ID EAR HOOK SIMULATOR INTO 25mm (.984") OF 2mm (.079") ID TUBE + 18mm (.709") OF 3MM (.118") ID TUBE + 2cm³ CAVITY (AS IEC126). (T-2607 AND B & K DB0138).

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ΤY

FREQUENCY	MIN.	MAX.
200	104.0	110.0
350	105.0	
500	107.0	113.0
700	110.5	
800	113.0	122.0
900-1200	124.0	129.0
1300-1700	114.5	
1800-2300	124.5	129.5
2300-2800	114.5	
2700-3300	120.5	125.5
3400-4000	109.0	
3900-4500	114.5	119.5
4600-5400	102.5	
5200-5900	110.0	118.0
5600-6400	100.0	
<6300	100.0	

- 3. RESPONSE, IMPEDANCE, AND DISTORTION MEASUREMENTS MADE USING THE ELECTRICAL TEST CONDITIONS SHOWN BELOW.
- 4. ELECTRICAL SOURCE IMPEDANCE MUST BE GREATER THAN 20 TIMES IKHZ IMPEDANCE FOR TEST CONDITIONS SHOWN BELOW.
- 5. INDIVIDUAL SPECIFICATIONS.

PORT	IMPE[OHMS	DANCE ±∣5%	DCR @20°C	DISTO	RTION	ELECTRIC CONDI	
LOCATION	IKHz	500Hz	OHMS ±10%	MAX. %	FREQ Hz	AC mA RMS	DC mA
118	327	128	48.5	10	500	1.56	0.00

Revision	C.O. #	Implementation vare	KELEASE LEVEL	KEA1210M
			Released	ΙΔ
А	C10103759	2-3-06		/ \

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WHEN TEST LIMITS ARE USED TO ESTABLISH INCOMING INSPECTION ACCEPTANCE/REJECTION CRITERIA, CORRELATION OF TEST EQUIPMENT WITH KNOWLES IS ALSO REQUIRED FOR ELIMINATION OF EQUIPMENT AND TEST METHOD VARIATION				
RECEIVER	EF-27715-000			
PERFORMANCE SPECIFICATION	SHT 2.1			

DR. BY	DATE
MMM	2 - 3 - 06
CK. BY	DATE
GJP	2 - 6 - 06
APP. BY	DATE
C ID	2 6 06