Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

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EFC8811R

Power MOSFET
for 1-Cell Lithium-ion Battery Protection
12V, 3.2mΩ, 27A, Dual N-Channel

ON Semiconductor®

www.onsemi.com

This Power MOSFET features a low on-state resistance. This device is suitable for applications such as power switches of portable machines. Best suited for 1-cell lithium-ion battery applications.

V_{SSS}	R_{SS}(on) Max	I_S Max
12V	3.2mΩ@ 4.5V	27A
	3.2mΩ@ 4.0V	
	3.2mΩ@ 3.8V	
	4.4mΩ@ 3.1V	
	6.3mΩ@ 2.5V	

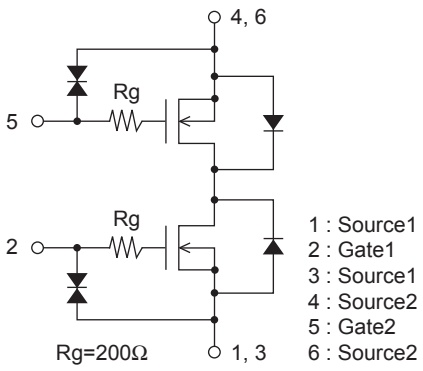
Features

- 2.5V drive
- 2kV ESD HBM
- Common-Drain Type
- ESD Diode-Protected Gate
- Pb-Free, Halogen Free and RoHS compliance

Applications

- 1-Cell Lithium-ion Battery Charging and Discharging Switch

ELECTRICAL CONNECTION
N-Channel



SPECIFICATIONS

ABSOLUTE MAXIMUM RATINGS at Ta = 25°C (Note 1)

Parameter	Symbol	Value	Unit
Source to Source Voltage	V_{SSS}	12	V
Gate to Source Voltage	V_{GSS}	±8	V
Source Current (DC)	I_S	27	A
Source Current (Pulse) PW≤100μs, duty cycle≤1%	I_{SP}	100	A
Total Dissipation Surface mounted on ceramic substrate (5000mm^2 × 0.8mm)	P_T	2.5	W
Junction Temperature	Tj	150	°C
Storage Temperature	Tstg	–55 to +150	°C

Note 1 : Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Value	Unit
Junction to Ambient Surface mounted on ceramic substrate (5000mm^2 × 0.8mm)	$R_{\theta JA}$	50	°C/W

CSP6, 1.77x3.54 /
EFCP3517-6DGH-020

MARKING

ORDERING INFORMATION

See detailed ordering and shipping information on page 6 of this data sheet.

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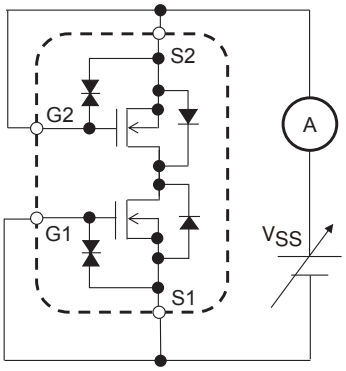
ELECTRICAL CHARACTERISTICS at Ta = 25°C (Note 2)

Parameter	Symbol	Conditions		Value			Unit
				min	typ	max	
Source to Source Breakdown Voltage	$V_{(BR)SSS}$	I_S=1mA, V_{GS}=0V	Test Circuit 1	12			V
Zero-Gate Voltage Source Current	I_{SSS}	V_{SS}=10V, V_{GS}=0V	Test Circuit 1			1	μA
Gate to Source Leakage Current	I_{GSS}	V_{GS}=±8V, V_{SS}=0V	Test Circuit 2			±1	μA
Gate Threshold Voltage	V_{GS}(th)	V_{SS}=6V, I_S=1mA	Test Circuit 3	0.5		1.3	V
Forward Transconductance	g_{FS}	V_{SS}=6V, I_S=3A	Test Circuit 4		19		S
Static Source to Source On-State Resistance	R_{SS}(on)1	I_S=5A, V_{GS}=4.5V	Test Circuit 5	1.8	2.3	3.2	mΩ
	R_{SS}(on)2	I_S=5A, V_{GS}=4.0V	Test Circuit 5	1.9	2.4	3.2	mΩ
	R_{SS}(on)3	I_S=5A, V_{GS}=3.8V	Test Circuit 5	2.0	2.6	3.2	mΩ
	R_{SS}(on)4	I_S=5A, V_{GS}=3.1V	Test Circuit 5	2.1	3.3	4.4	mΩ
	R_{SS}(on)5	I_S=5A, V_{GS}=2.5V	Test Circuit 5	2.7	4.0	6.3	mΩ
Turn-ON Delay Time	t_d(on)	V_{SS}=6V, V_{GS}=4.5V, I_S=3A	Test Circuit 6		80		ns
Rise Time	t_r				570		ns
Turn-OFF Delay Time	t_d(off)				38,000		ns
Fall Time	t_f				17,700		ns
Total Gate Charge	Qg	V_{SS}=6V, V_{GS}=4.5V, I_S=27A	Test Circuit 7		100		nC
Forward Source to Source Voltage	$V_{F(S-S)}$	I_S=3A, V_{GS}=0V	Test Circuit 8		0.75	1.2	V

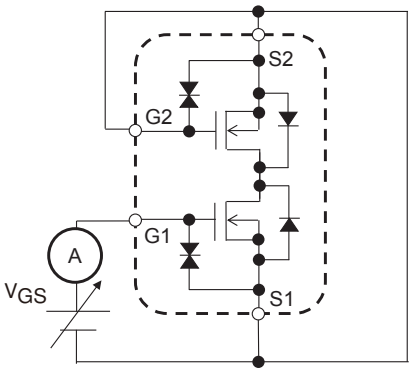
Note 2 : Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

Test circuits are example of measuring FET1 side

Test Circuit 1
V_{SSS} / I_{SSS}

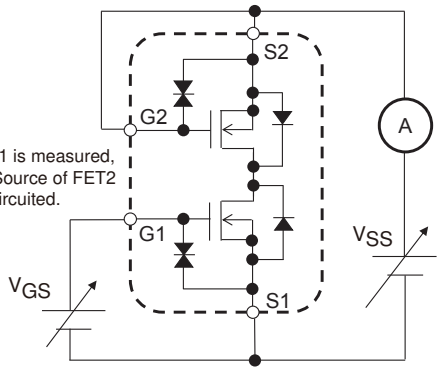


Test Circuit 2
I_{GSS}



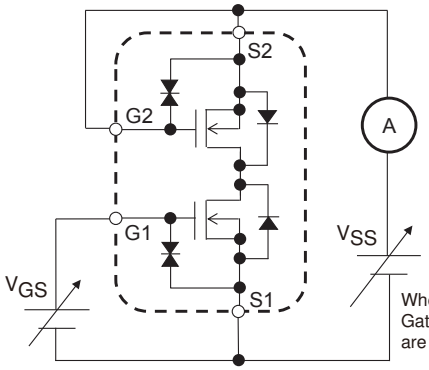
When FET1 is measured, Gate and Source of FET2 are short-circuited.

Test Circuit 3
$V_{GS(th)}$



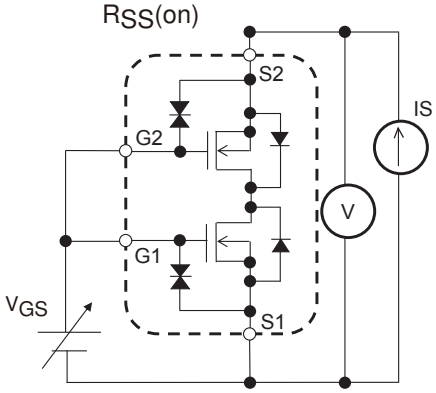
When FET1 is measured, Gate and Source of FET2 are short-circuited.

Test Circuit 4
g_{FS}

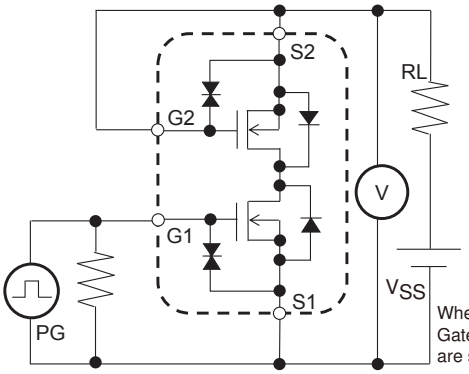


When FET1 is measured, Gate and Source of FET2 are short-circuited.

Test Circuit 5
$R_{SS(on)}$

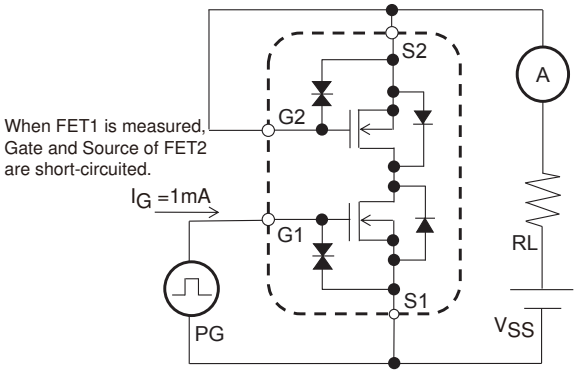


Test Circuit 6
$t_{d(on)}$, t_r, $t_{d(off)}$, t_f



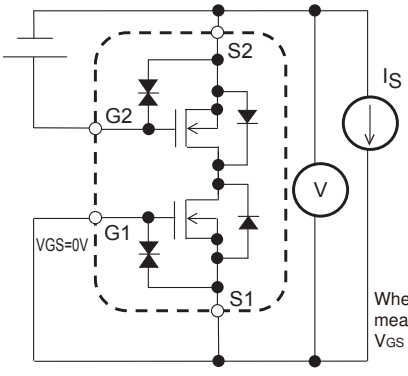
When FET1 is measured, Gate and Source of FET2 are short-circuited.

Test Circuit 7
Qg



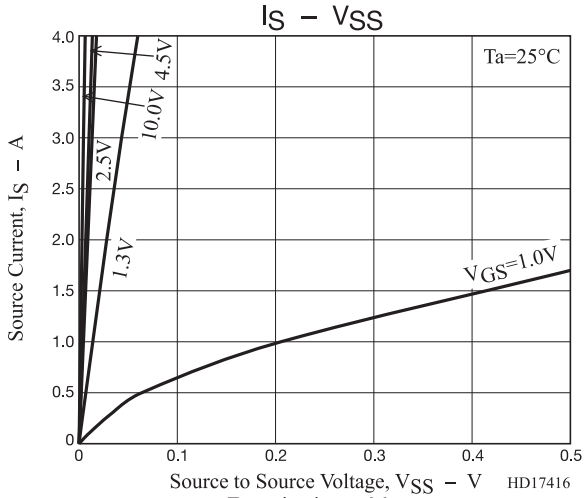
When FET1 is measured, Gate and Source of FET2 are short-circuited.

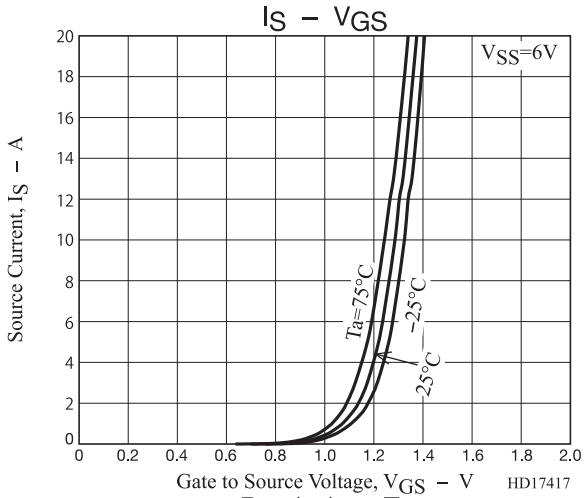
Test Circuit 8
$V_{F(S-S)}$

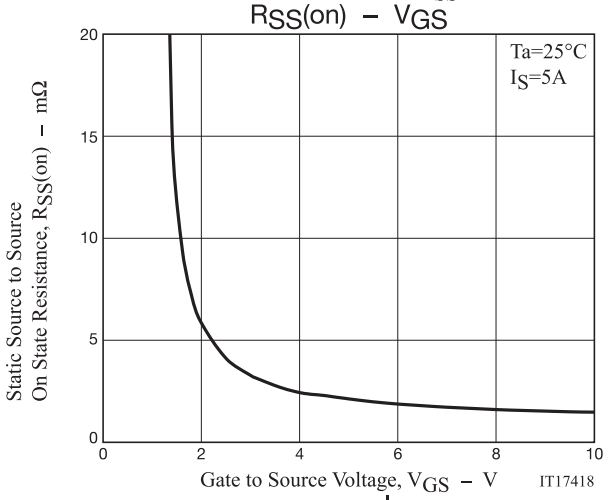


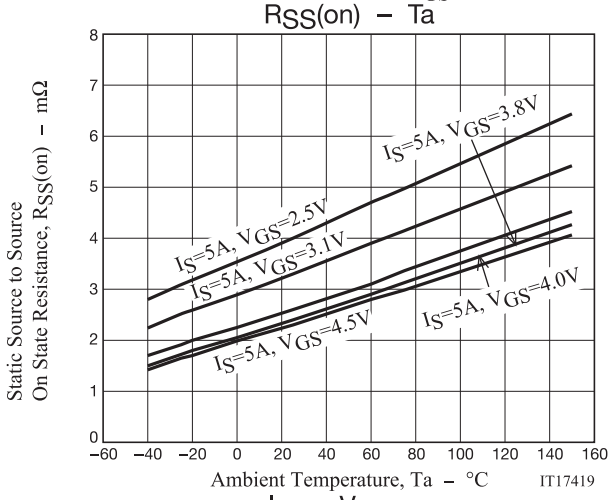
When FET1 is measured,+4.5V is added to V_{GS} of FET2.

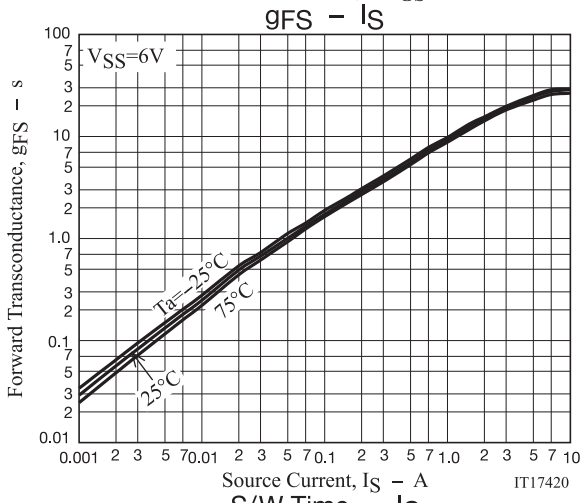
When FET2 is measured, the position of FET1 and FET2 is switched.

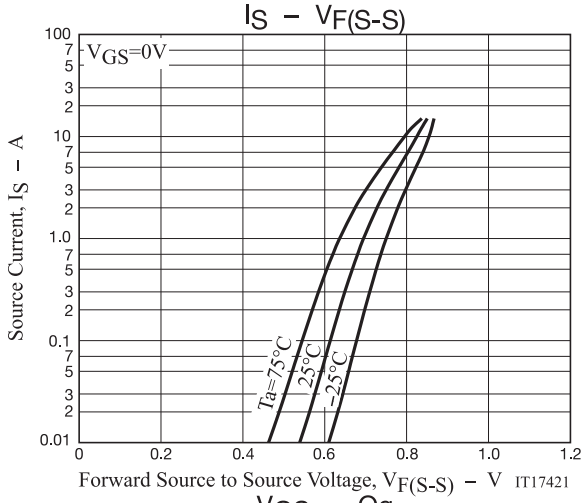

IS – VSS
Ta=25°C
4.5V
10.0V
2.5V
1.3V
VGS=1.0V
Source Current, IS – A
Source to Source Voltage, VSS – V
HD17416

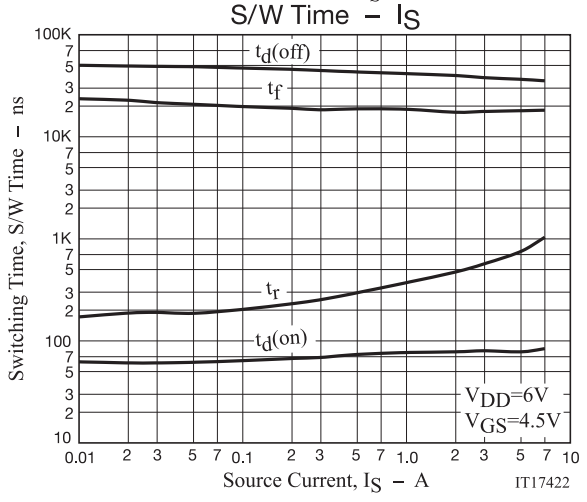

IS – VGS
VSS=6V
Ta=75°C
–25°C
25°C
Source Current, IS – A
Gate to Source Voltage, VGS – V
HD17417

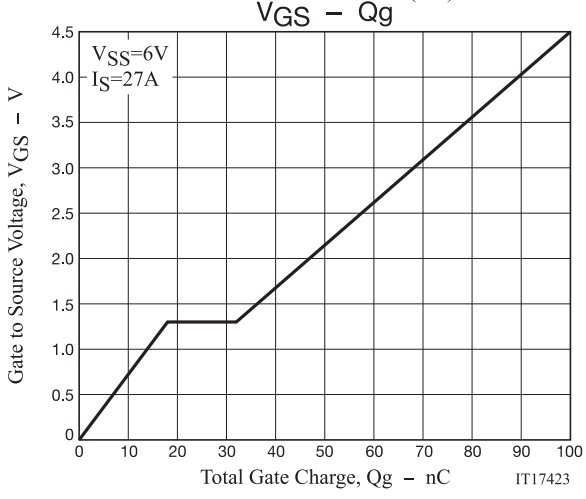

RSS(on) – VGS
Ta=25°C
IS=5A
Static Source to Source
On State Resistance, RSS(on) – mΩ
Gate to Source Voltage, VGS – V
IT17418

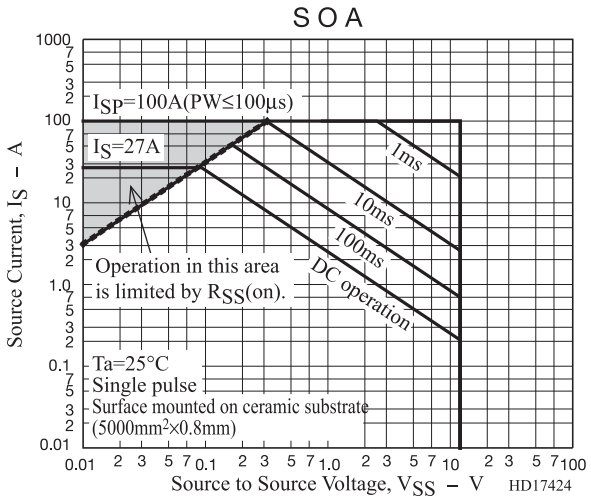

RSS(on) – Ta
IS=5A, VGS=3.8V
IS=5A, VGS=2.5V
IS=5A, VGS=3.1V
IS=5A, VGS=4.0V
IS=5A, VGS=4.5V
Static Source to Source
On State Resistance, RSS(on) – mΩ
Ambient Temperature, Ta – °C
IT17419

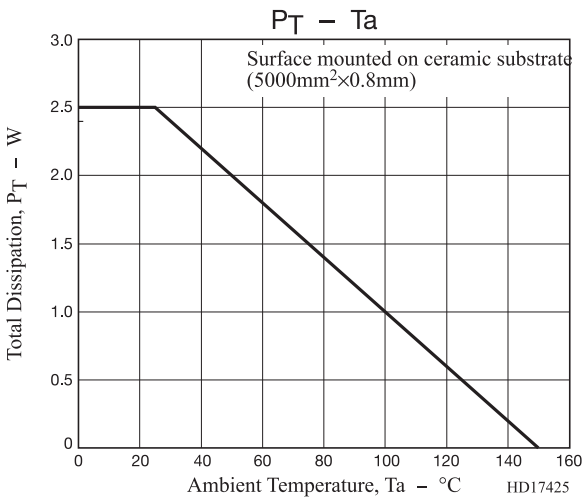

gFS – IS
VSS=6V
Ta=–25°C
75°C
25°C
Forward Transconductance, gFS – s
Source Current, IS – A
IT17420

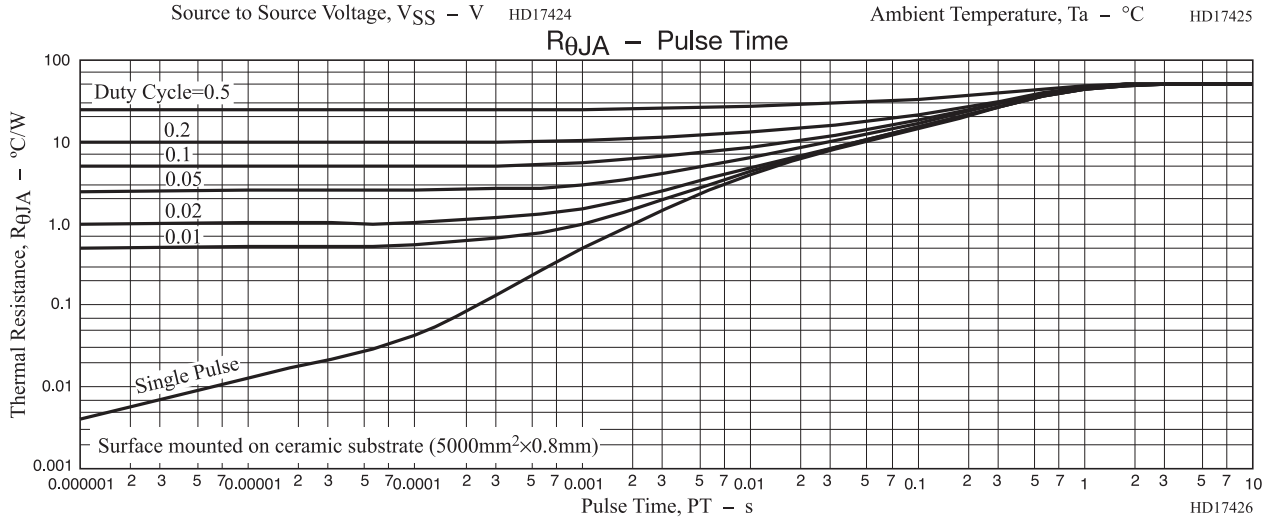

IS – VF(S-S)
VGS=0V
Ta=75°C
25°C
–25°C
Source Current, IS – A
Forward Source to Source Voltage, VF(S-S) – V
IT17421


S/W Time – IS
td(off)
tf
tr
td(on)
VDD=6V
VGS=4.5V
Switching Time, S/W Time – ns
Source Current, IS – A
IT17422


VGS – Qg
VSS=6V
IS=27A
Gate to Source Voltage, VGS – V
Total Gate Charge, Qg – nC
IT17423

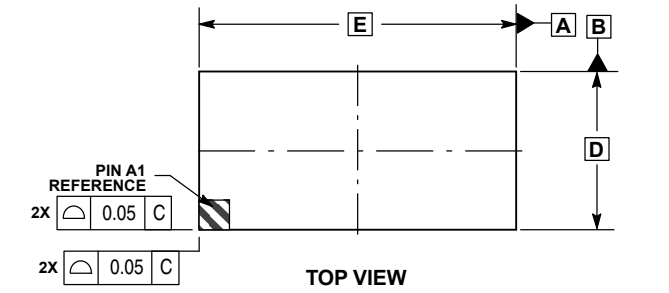

S O A
ISP=100A(PW≤100μs)
IS=27A
1ms
10ms
100ms
DC operation
Operation in this area
is limited by RSS(on).
Ta=25°C
Single pulse
Surface mounted on ceramic substrate
(5000mm2×0.8mm)
Source Current, IS – A
Source to Source Voltage, VSS – V
HD17424


PT – Ta
Surface mounted on ceramic substrate
(5000mm2×0.8mm)
Total Dissipation, PT – W
Ambient Temperature, Ta – °C
HD17425

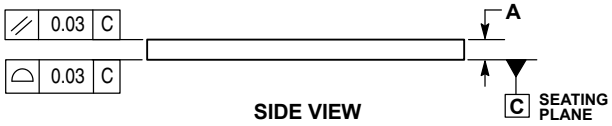

RθJA – Pulse Time
Duty Cycle=0.5
0.2
0.1
0.05
0.02
0.01
Single Pulse
Surface mounted on ceramic substrate (5000mm2×0.8mm)
Thermal Resistance, RθJA – °C/W
Pulse Time, PT – s
HD17426

PACKAGE DIMENSIONS
unit : mm

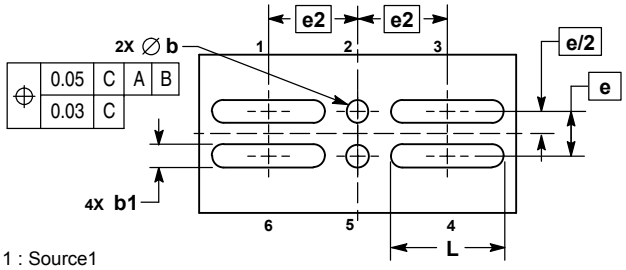
CSP6, 1.77x3.54 / EFCP3517-6DGH-020
CASE 568AL
ISSUE O



TOP VIEW



SIDE VIEW



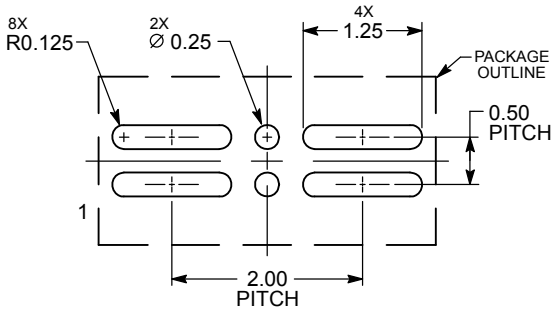
BOTTOM VIEW

1 : Source1
2 : Gate1
3 : Source1
4 : Source2
5 : Gate2
6 : Source2

NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.

DIM	MILLIMETERS MIN	MAX
A	---	0.22
b	0.22	0.28
b1	0.22	0.28
D	1.77 BSC	
E	3.54 BSC	
e	0.50 BSC	
e2	1.00 BSC	
L	1.22	1.28

RECOMMENDED SOLDERING FOOTPRINT*



DIMENSIONS: MILLIMETERS

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ORDERING INFORMATION

Device	Marking	Package	Shipping (Qty / Packing)
EFC8811R-TF	ML	CSP6, 1.77x3.54 / EFCP3517-6DGH-020 (Pb-Free / Halogen Free)	5,000 / Tape & Reel

† For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. http://www.onsemi.com/pub_link/Collateral/BRD8011-D.PDF

Note on usage : Since the EFC8811R is a MOSFET product, please avoid using this device in the vicinity of highly charged objects. Please contact sales for use except the designated application.

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