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### LOW-JITTER SAW OSCILLATOR (SPSO)

**OUTPUT: LV-PECL, LVDS, HCSL** 

## EG - 2121 / 2102CA

•Frequency range
•Supply voltage

•Supply voltage

: 53.125 MHz to 700 MHz

2.5 V ⋅⋅⋅ EG-2121CA

3.3 V ⋅⋅⋅ EG-2102CA

•Output

•Function

: LV-PECL or LVDS or HCSL

•Function

: Output enable (OE)

•External dimensions

: 7.0 × 5.0 × 1.2 mm

•Very low jitter and low phase noise by SAW unit.



#### Specifications (characteristics)

▶ Differential LV-PECL Output

ltem	Symbol	EG-2121CA	EG-2102CA	Condition	s / Pomarks
item	Syllibol	LV-PECL		Conditions / Remarks	
Output frequency range	fo	53.125 MHz to 500 MHz	100 MHz to 700 MHz	Please contact us about av	ailable frequencies.
Supply voltage	Vcc	2.5 V ±0.125 V	3.3 V ±0.3 V		
Storage temperature	T_stg	-40 °C to +100 °C		Storage as single product.	
Operating temperature	T_use	P:0 °C to +70 °C ,R:-5 °C to	+85 °C ,S:-20 °C to +70 °C		
Frequency tolerance	f_tol	G: $\pm 50 \times 10^{-6}$	,H: ±100 × 10 <sup>-6</sup>		
Current consumption	Icc	80 mA Max.	100 mA Max.	OE=Vcc, L_ECL=50 Ω	
Disable current	I_dis	20 mA Max.	32 mA Max	OE=GND	
Symmetry	SYM	P:40 % to 60 % (fo > 350 MHz) P:45 % to 55 % (fo ≤ 350 MHz)	P:45 % to 55 %	at outputs crossing point	
		D:48 % to 52 % (fo ≤ 175 MHz)	D:48 % to 52 % (fo ≤ 350 MHz)		
	Voh	1.55 V Typ. 2.35 V Typ.			
Output voltage		Vcc-1.025 V to Vcc-0.88 V		DC characteristics	
Calput Tollago	Vol	0.8 V Typ.	1.6 V Typ. o Vcc-1.62 V	-	
Output load condition (ECL)	L ECL	50 Ω		Terminated to Vcc -2.0 V	
Input voltage	VIH VIL	70 % Vcc Min. 30 % Vcc Max.		OE terminal	
Rise time / Fall time	tr / tf	400 ps Max.		Between 20% and 80% of (	(Voh-Vol)
Start-up time	t_str	10 ms Max.		Time at minimum supply vo	oltage to be 0 s
•		0.8 ps Max.		fo < 100 MHz	Officet frequency 12 kl late
Phase Jitter	tpJ	0.5 ps Max.		100 MHz ≤ fo < 200 MHz 20 MHz	Offset frequency: 12 kHz to
		0.3 ps Max.		200 MHz ≤ fo	ZO IVII IZ
Frequency aging	f_aging	$\pm 10 \times 10^{-6}$ / year Max.		+25 °C, First year, Vcc=2.5	V,3.3 V

►LVDS Output

ltam	O. mala al	EG-2121CA	EG-2102CA	Conditions / Remarks	
Item	Symbol	LV	DS		
Output frequency range	fo	53.125 MHz to 700 MHz		Please contact us about available frequencies.	
Supply voltage	Vcc	2.5 V ±0.125 V			
Storage temperature	T_stg			Storage as single product.	
Operating temperature	T use	P:0 °C to +70 °C ,R:-5 °C to	+85 °C ,S:-20 °C to +70 °C		
Frequency tolerance	f_tol	G: $\pm 50 \times 10^{-6}$	,H: ±100 × 10 <sup>-6</sup>		
Current consumption	Icc	30 mA Max	45 mA Max.	OE=Vcc, L_LVDS= 100 Ω	
Disable current	I_dis	20 mA Max	30 mA Max.	OE=GND	
Symmetry	SYM	L:40 % to 60 % (fo > 350 MHz) L:45 % to 55 % (fo ≤ 350 MHz) V:48 % to 52 % (fo ≤ 175 MHz)	L:40 % to 60 % (fo > 350 MHz) L:45 % to 55 % (fo ≤ 350 MHz) V:48 % to 52 % (fo ≤ 175 MHz)	at outputs crossing point	
	Vop	350 mV Typ. 247 mV to 454 mV		Vod1, Vod2	
O to to the co	dVod	50 mV Max.		dVod =   Vod1-Vod2	DC characteristics
Output voltage	Vos	1.25 V Typ. 1.125 V to 1.375 V		Vos1, Vos2	
	dVos	150 mV Max.		dVos =   Vos1-Vos2	
Output load condition (LVDS)	L_LVDS	100 Ω		Connected between OUT	to OUT
Input voltage	VIH VIL	70 % Vcc Min. 30 % Vcc Max.		OE terminal	
Rise time / Fall time	tr / tf	400 ps Max		Between 20 % and 80 % Peak voltage	of Differential Output Peak to
Start-up time	t_str	10 ms Max.		Time at minimum supply v	oltage to be 0 s
Phase Jitter	tpJ	0.8 ps Max.		fo < 100 MHz	Offset frequency: 12 kHz to
		0.5 ps Max.		$100~MHz \leq f_0 < 200~MHz$	0 < 200 MHz 20 MHz
		0.3 ps Max.		200 MHz ≤ fo	
Frequency aging	f_aging	$\pm$ 10 $\times$ 10 <sup>-6</sup> / year Max.		+25 °C, First year, Vcc=2.	5 V,3.3 V



#### HCSL Output

Itam	O. was be as l	EG-2121CA	EG-2102CA	Canditions	/ Domorles
Item	Symbol	HCSL		Conditions / Remarks	
Output frequency range	fo	100 MHz to 350 MHz		Please contact us about available frequencies.	
Supply voltage	Vcc	2.5 V ±0.125 V	3.3 V ±0.3 V		
Storage temperature	T_stg	-40 °C to	+125 °C	Storage as single product.	
Operating temperature	T_use	P:0 °C to +70 °C ,R:-5 °C to	+85 °C ,S:-20 °C to +70 °C		
Frequency tolerance	f_tol	G: ± 50 × 10 <sup>-6</sup> ,H: ±100 × 10 <sup>-6</sup>			
Current consumption	Icc	80 mA Max. 85 mA Max.		OE=Vcc,L_HCSL=50 Ω	
Disable current	I_dis	20 mA Max.	35 mA Max	OE=GND	
Symmetry	SYM	45 % to 55 %		at outputs crossing point	
Output Voltage	Vон	0.75 V Typ.		DC characteristics	
· •	Vol	-0.3 V Typ.			
Output load condition (HCSL)	L_HCSL	50 Ω		Terminated to GND	
Input voltage	VIH	70 % Vcc Min.		OE terminal	
	VIL	30 % Vcc Max.			
Rise time / Fall time	tr / tf			Between 0.175 V and 0.525	5 V of output
Start-up time	t_str	10 ms Max.		Time at minimum supply vo	Itage to be 0 s
Phase Jitter	tpJ	0.8 ps Max.		fo < 100 MHz	Offset frequency: 12 kHz to
		0.5 ps Max.		$100 \text{ MHz} \le f_0 < 200 \text{ MHz}$	-20 MHz
		0.3 ps Max.		200 MHz ≤ f <sub>0</sub>	20 1411 12
Frequency aging *2	f_aging	$\pm$ 10 $\times$ 10 <sup>-6</sup> / year Max.		+25 °C, First year, Vcc=2.5	V,3.3 V

Product Name (Standard form) EG-2121 CA 250.000000MHz P G P A

1 2 (3)

4567

①Model ②Package type ③Frequency

⑦Frequency aging (A\*1: Frequency tolerance include aging, N\*2: Frequency tolerance exclude aging)

This includes initial frequency tolerance, temperature variation, supply voltage variation, reflow drift, and aging(+25 °C,10 years). This includes initial frequency tolerance, temperature variation, supply voltage variation, and reflow drift(except aging).

(⑤⑥⑦: GRA, GSA are not available)

(§ ©: As for LV-PECL and LVDS output, for 53.125 MHz  $\leq$  fo < 100 MHz only HP is available)

4	Output	Symmetry		
Symbol	Output	EG-2121CA	EG-2102CA	
Р	LV-PECL	40 % to 60 %(fo > 350 MHz) 45 % to 55 %(fo ≤ 350 MHz)	45 % to 55 %	
D	LV-PECL	48 % to 52 %(fo≤ 175 MHz)	48 % to 52 %(f <sub>0</sub> ≤ 350 MHz)	
L	LVDS	40 % to 60 %(fo > 350 MHz) 45 % to 55 %(fo ≤ 350 MHz)		
V	LVDS	48 % to 52 %(fo ≤ 175 MHz)		
Н	HCSL	45 % to 55 %		

⑤Frequency tolerance			
G	±50 × 10 <sup>-6</sup>		
Н	±100 × 10 <sup>-6</sup>		

Р	0 to +70°C			
R	-5 to +85°C			
S	-20 to +70°C			

#### Table 2 Jitter

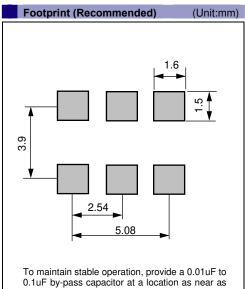
Item	Symbol	Specifications	Remarks
	tDJ	0.2 ps Typ.	Deterministic Jitter
	tru	3 ps Typ.	Random Jitter
Jitter *	trms	3 ps Typ.	σ (RMS of total distribution)
	t <sub>p-p</sub>	25 ps Typ.	Peak to Peak
	tacc	4 ps Typ.	Accumulated Jitter(σ) n=2 to 50000 cycles

#3 is connected to the cover.

: Differential LV-PECL, LVDS output

: HCSL output

#### External dimensions (Unit:mm) E EG-2121 5.0±0.2 250.000H **OGPA 282A** #2 2.54 #2 #3 7.0±0.2 5.08 Pin map 5.08 Pin Connection OE GND OUT OE pin = HIGH : Specified frequency output. OE pin = LOW : Output is high impedance OUT Vcc



possible to the power source terminal of the crystal product (between Vcc - GND).

<sup>\*</sup> Tested using a DTS-2075 Digital timing system made by WAVECREST with jitter analysis software VISI6. \* Based on SIA-3100C signal integrity analyzer made from WAVECREST.

# PROMOTION OF ENVIRONMENTAL MANAGEMENT SYSTEM CONFORMING TO INTERNATIONAL STANDARDS

At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard.

All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

ISO 14000 is an international standard for environmental management that was established by the International Standards Organization in 1996 against the background of growing concern regarding global warming, destruction of the ozone layer, and global deforestation.

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In order provide high quality and reliable products and services than meet customer needs,

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ISO/TS16949 is the international standard that added the sector-specific supplemental requirements for automotive industry based on ISO9001.

Explanation of the mark that are using it for the catalog



►Pb free.



- ► Complies with EU RoHS directive.
  - \*About the products without the Pb-free mark.

    Contains Pb in products exempted by EU RoHS directive.

    (Contains Pb in sealing glass, high melting temperature type solder or other.)



▶ Designed for automotive applications such as Car Multimedia, Body Electronics, Remote Keyless Entry etc.



 $\blacktriangleright$  Designed for automotive applications related to driving safety (Engine Control Unit, Air Bag, ESC etc ).

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