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Power management (dual transistors)

EMF21 / UMF21N

2SA2018 and DTC114E are housed independently in a EMT6 or UMT6 package.

Application

Power management circuit

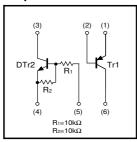
● Features

- 1) Power switching circuit in a single package.
- 2) Mounting cost and area can be cut in half.

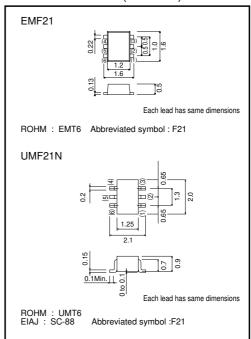
●Structure

Silicon epitaxial planar transistor

Equivalent circuits



●External dimensions (Units : mm)



Package, marking, and packaging specifications

| Туре | EMF21 | UMF21N |
|-----------------------------|-------|--------|
| Package | EMT6 | UMT6 |
| Marking | F21 | F21 |
| Code | T2R | TR |
| Basic ordering unit(pieces) | 8000 | 3000 |

● Absolute maximum ratings (Ta=25°C)

Tr1

| Parameter | Symbol | Limits | Unit |
|------------------------------|--------|-----------------|-------|
| Collector-base voltage | Vсво | -15 | V |
| Collector-emitter voltage | Vceo | -12 | V |
| Emitter-base voltage | VEBO | -6 | V |
| Collector current | Ic | -500 | mA |
| Collector current | Іср | -1.0 | A *1 |
| Power dissipation | Pc | 150(TOTAL) | mW *2 |
| Junction temperature | Tj | 150 | °C |
| Range of storage temperature | Tstg | −55~+150 | °C |

DTr2

| Parameter | Symbol | Limits | Unit |
|------------------------------|--------|------------------|-------|
| Supply voltage | Vcc | 50 | V |
| Input voltage | Vin | −10~+40 | V |
| Collector current | Ic | 100 | mA *1 |
| Output current | lo | 50 | mA |
| Power dissipation | Pc | 150(TOTAL) | mW *2 |
| Junction temperature | Tj | 150 | °C |
| Range of storage temperature | Tstg | <i>–</i> 55∼+150 | °C |

^{*1} Characteristics of built-in transistor.

● Electrical characteristics (Ta=25°C)

Tr1

| Parameter | Symbol | Min. | Тур. | Max. | Unit | Conditions |
|--------------------------------------|----------------------|------|------|------|------|--|
| Collector-emitter breakdown voltage | BVceo | -12 | _ | _ | V | Ic=-1mA |
| Collector-base breakdown voltage | ВУсво | -15 | _ | _ | V | Ic=-10μA |
| Emitter-base breakdown voltage | BVEBO | -6 | _ | _ | V | I _E =-10μA |
| Collector cut-off current | Ісво | _ | _ | -100 | nA | V _{CB} =-15V |
| Emitter cut-off current | ІЕВО | _ | _ | -100 | nA | V _{EB} =-6V |
| Collector-emitter saturation voltage | V _{CE(sat)} | _ | -100 | -250 | mV | Ic=-200mA, I _B =-10mA |
| DC current gain | hfe | 270 | _ | 680 | - | Vce=-2V, Ic=-10mA |
| Transition frequency | f⊤ | _ | 260 | _ | MHz | Vce=-2V, Ie=10mA, f=100MHz |
| Collector output capacitance | Cob | _ | 6.5 | _ | рF | V _{CB} =-10V, I _E =0mA, f=1MHz |

DTr2

| Parameter | Symbol | Min. | Тур. | Max. | Unit | Conditions |
|----------------------|--------------------|------|------|------|------|------------------------------|
| lancit valta e a | VI(off) | - | - | 0.5 | ,, | Vcc=5V, Io=100μA |
| Input voltage | V _{I(on)} | 3 | _ | _ | V | Vo=0.3V, Io=10mA |
| Output voltage | V _{O(on)} | - | 0.1 | 0.3 | ٧ | lo/l⊫10mA/0.5mA |
| Input current | lı | - | _ | 0.88 | mA | V=5V |
| Output current | IO(off) | - | - | 0.5 | μΑ | Vcc=50V, Vi=0V |
| DC current gain | Gı | 30 | _ | _ | - | Vo=5V, Io=5mA |
| Input resistance | R ₁ | 7 | 10 | 13 | kΩ | _ |
| Resistance ratio | R2/R1 | 0.8 | 1 | 1.2 | - | - |
| Transition frequency | f⊤ | - | 250 | - | MHz | Vce=10V, Ie=-5mA, f=100MHz * |

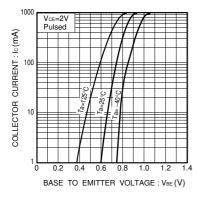
^{*} Transition frequency of the device

^{*1} Single pulse Pw=1ms
*2 120mW per element must not be exceeded.
Each terminal mounted on a recommended land.

^{*2} Each terminal mounted on a recommended land.

Electrical characteristic curves

Tr1



1000 VGE=2V Pulsed
Ta=25°C
Ta=25°C
Ta=-40°C
Ta=-40°C
Ta=-40°C
COLLECTOR CURRENT: Ic (mA)

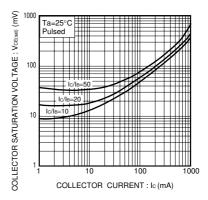
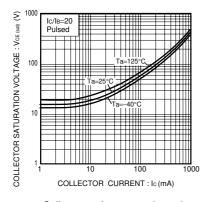
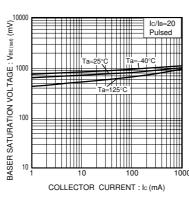


Fig.1 Grounded emitter propagation characteristics

Fig.2 DC current gain vs. collector current

Fig.3 Collector-emitter saturation voltage vs. collector current (I)





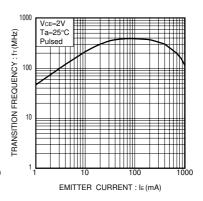


Fig.4 Collector-emitter saturation voltage vs. collector current (II)

Fig.5 Base-emitter saturation voltage vs. collector current

Fig.6 Gain bandwidth product vs. emitter current

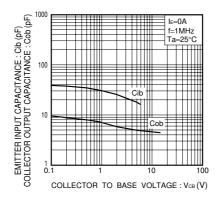


Fig.7 Collector output capacitance vs. collector-base voltage Emitter input capacitance vs. emitter-base voltage

Fig.1 Input voltage vs. output current (ON characteristics)

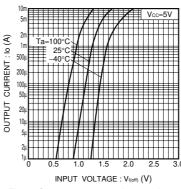


Fig.2 Output current vs. input voltage (OFF characteristics)

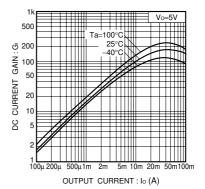


Fig.3 DC current gain vs. output current

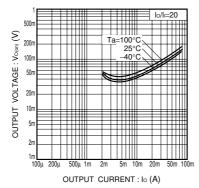


Fig.4 Output voltage vs. output current

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