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EMIF02-SPK02F2

2-line IPAD[™], EMI filter and ESD protection

Datasheet - production data

Features

- Packaged in lead-free Flip Chip
- Very low resistance: 0.35 Ω
- High attenuation: -45 dB at 900 MHz
- Very low PCB space consumption: 0.89 mm x 1.26 mm
- Very thin package: 0.65 mm
- High efficiency in ESD suppression IEC6 1000-4-2 level 4
- High reliability offered by monolithic integration
- High reduction of parasitic elements through integration and wafer level packaging

Complies with the following standards

- IEC 61000-4-2 level 4:
 - ±15 kV (air discharge)
 - ±8 kV (contact discharge)

Application

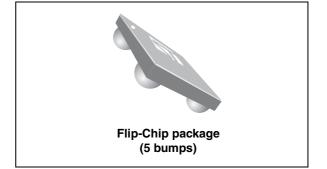
Mobile phones

Description

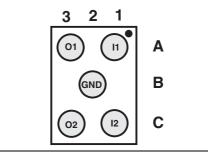
The EMIF02-SPK02F2 chip is a highly integrated device designed to suppress EMI/RFI noise for interface line filtering.

The EMIF02-SPK02F2 flip-chip packaging means the package size is equal to the die size. That's why the EMIF02-SPK02F2 is a very small device.

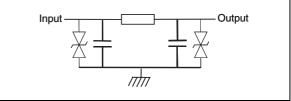
Additionally, this filter includes ESD protection circuitry, which prevents damage to the protected device when subjected to ESD surges up 30 kV.











TM: IPAD is a trademark of STMicroelectronics

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This is information on a product in full production.

1 Characteristics

Table 1.Absolute maximum ratings ($T_{amb} = 25 \ ^{\circ}C$)

Symbol	Parameter	Value	Unit	
	ESD discharge IEC 61000-4-2			
V _{PP}	Air discharge	30	kV	
	Contact discharge	30		
I _{SPK}	Maximum rms current per channel	350	mA	
Тj	Junction temperature range	-30 to 125	°C	
T _{stg}	Storage temperature range -55 to + 150			

Figure 3. Electrical characteristics - definitions

V _{BR} V _{CL} I _{RM}	=	Breakdown voltage Clamping voltage	Slope: 1/R _d
1	=	Clamping voltage	
1		Clamping vollage	
BIVI	=	Leakage current @ V _{BM}	
V _{RM}	=	Stand-off voltage	
I _F	=	Forward current	V _{CL} V _{BR} V _{RM} I _{RM}
I _{PP}	=	Peak pulse current	I_{RM} V_{RM} V_{BR} V_{CL} V
I _B	=	Breakdown current	
V _F	=	Forward voltage drop	
R _d	=	Dynamic resistance	Slope: 1/Rd
αT	=	Voltage temperature	кIpp

	amb	/			
Symbol	Test conditions	Min	Тур	Мах	Unit
V _{BR}	I _R = 1 mA	6			V
I _{RM}	V _{RM} = 3 V			400	nA
R _{I/O}			0.35	0.8	Ω
C _{LINE}	V _R = 0 V DC, 1 MHz	185	250	315	pF
F _c	Cut-off frequency: $Z_{SOURCE} = Z_{LOAD} = 50 \ \Omega$		20		MHz



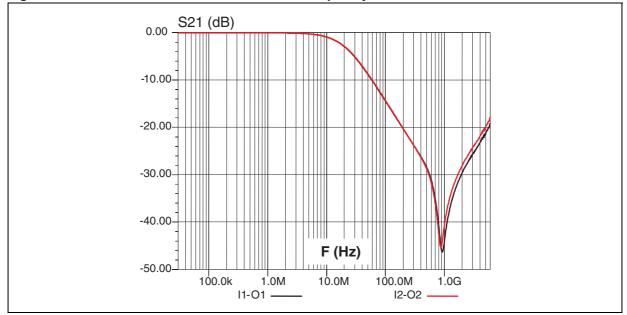


Figure 4. Attenuation measurements versus frequency

Figure 5. Crosstalk measurements versus frequency

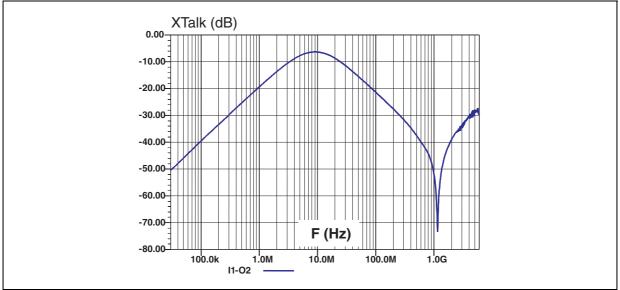
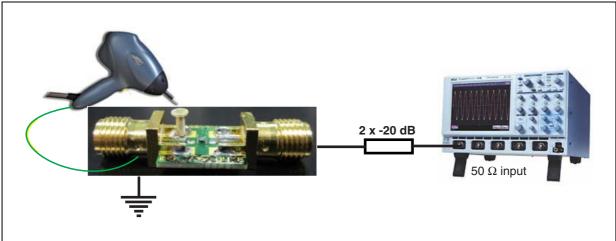
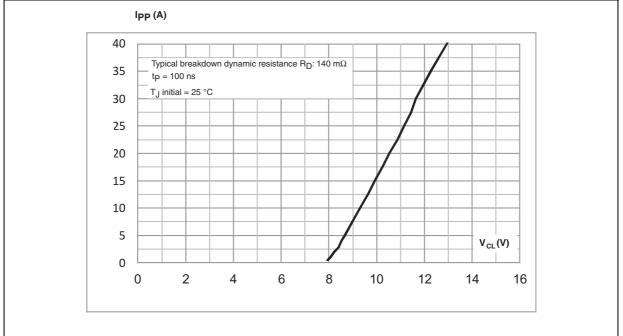




Figure 6. ESD test conditions







Note: For further information on the dynamic characteristic see the STMicroelectronics' application note AN4022, "TVS short pulse R_d measurement and correlation with TVS clamping voltage during ESD".



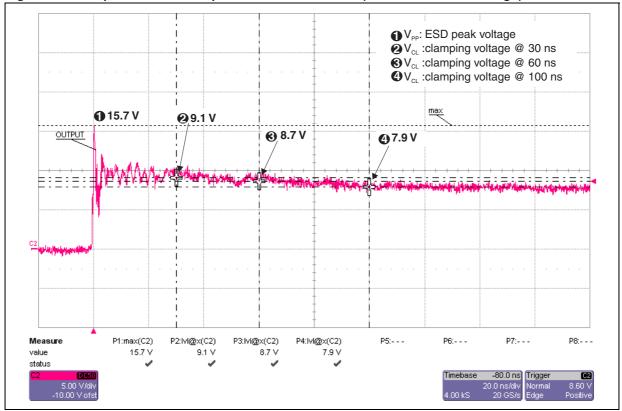
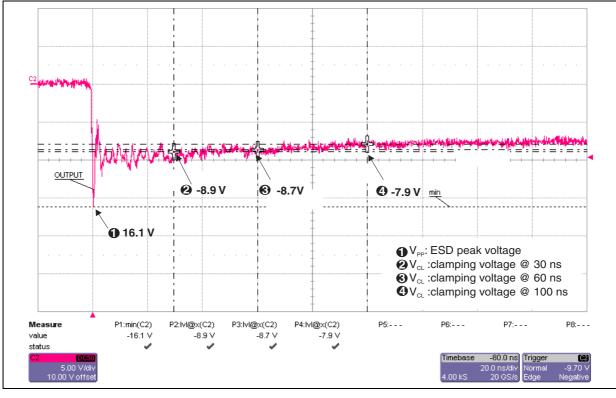


Figure 8. Output filter ESD response to IEC 61000-4-2 (+8 kV contact discharge) I1 to O1







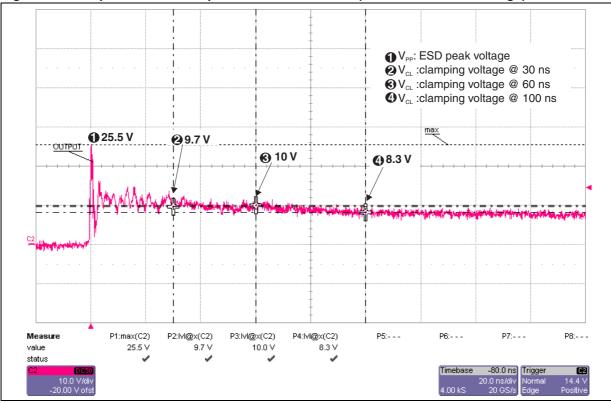
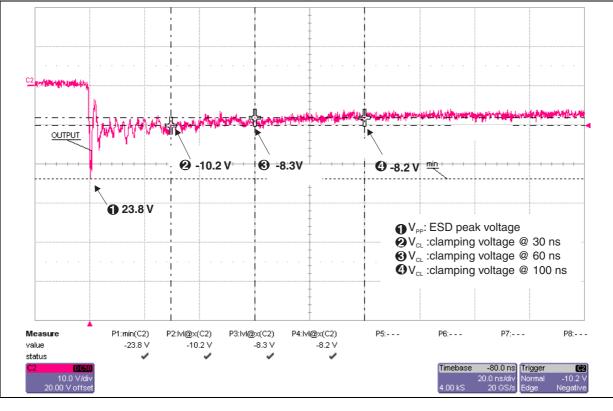


Figure 10. Output filter ESD response to IEC 61000-4-2 (+15 kV contact discharge) I1 to O1

Figure 11. Output filter ESD response to IEC 61000-4-2 (-15 kV contact discharge) I1 to O1



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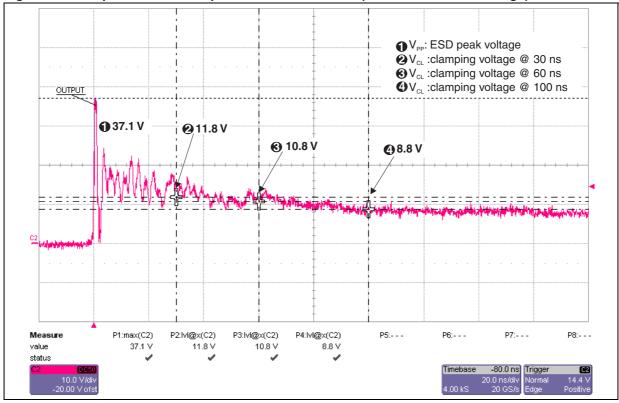
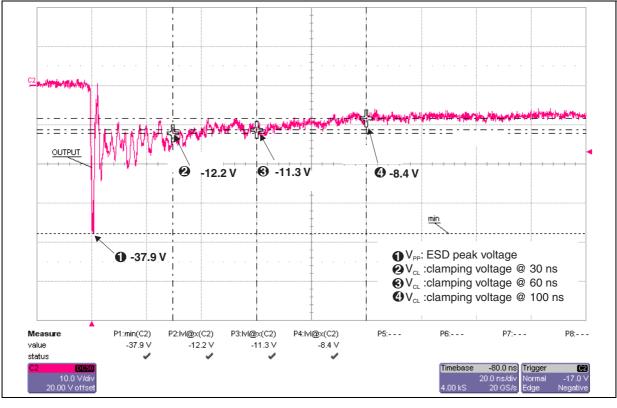


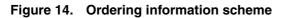
Figure 12. Output filter ESD response to IEC 61000-4-2 (+30 kV contact discharge) I1 to O1

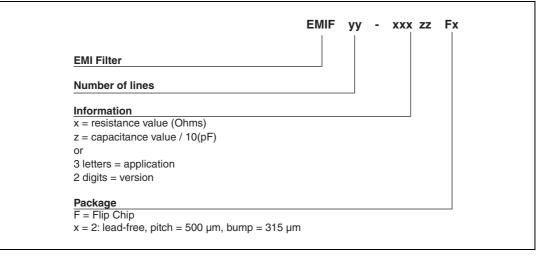
Figure 13. Output filter ESD response to IEC 61000-4-2 (-30 kV contact discharge) I1 to O1





2 Ordering information scheme





3 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: <u>www.st.com</u>. ECOPACK[®] is an ST trademark.



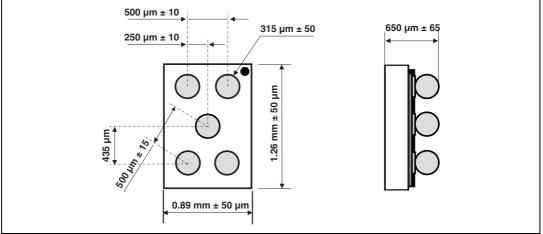
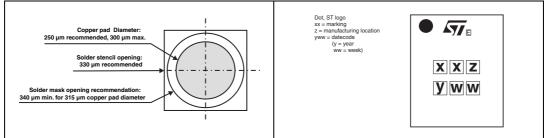




Figure 17. Marking





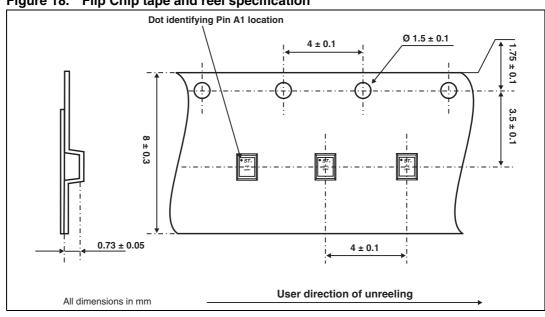
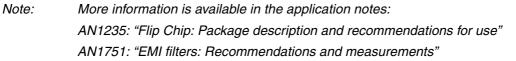


Figure 18. Flip Chip tape and reel specification





4 Ordering information

Table 3. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
EMIF02-SPK02F2	JD	Flip Chip	1.8 mg	5000	Tape and reel 7"

5 Revision history

Table 4.Document revision history

Date	Revision	Changes
17-Sep-2008	1	Initial release.
12-Sep-2011	2	Updated Figure 15 and Figure 16.
3-Apr-2012	3	Updated cover page features and description.Inserted <i>Figure 6</i> to <i>Figure 13</i> .



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