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## 2A, 200V - 600V Surface Mount Super Fast Rectifier

#### **FEATURES**

- · Glass passivated junction chip
- · Ideal for automated placement
- Super fast recovery time for high efficiency
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21

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- · High frequency rectification
- Freewheeling application
- Switching mode converters and inverters in computer, automotive and telecommunication.

#### **MECHANICAL DATA**

- Case: DO-214AA (SMB)
- Molding compound meets UL 94 V-0 flammability rating
- Moisture sensitivity level: level 1, per J-STD-020
- Part no. with suffix "H" means AEC-Q101 qualified
- Packing code with suffix "G" means green compound (halogen-free)
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: As marked
- Weight: 0.11 g (approximately)

KEY PARAMETERS						
PARAMETER	VALUE	UNIT				
I <sub>F(AV)</sub>	2	Α				
$V_{RRM}$	200 - 600	V				
I <sub>FSM</sub>	50	Α				
T <sub>J MAX</sub>	150	°C				
Package	DO-214AA (SMB					
Configuration	Single	dice				







DO-214AA (SMB)

ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C unless otherwise noted)						
PARAMETER	SYMBOL	ES2LD	ES2LG	ES2LJ	UNIT	
Marking code on the device		ES2LD	ES2LG	ES2LJ		
Repetitive peak reverse voltage	$V_{RRM}$	200	400	600	V	
Reverse voltage, total rms value	$V_{RMS}$	140	280	420	V	
Forward current	I <sub>F(AV)</sub>		2		Α	
Surge peak forward current, 8.3 ms single half sinewave superimposed on rated load per diode	I <sub>FSM</sub>		50		Α	
Junction temperature	TJ		- 55 to +150		°C	
Storage temperature	T <sub>STG</sub>		- 55 to +150		°C	

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THERMAL PERFORMANCE							
PARAMETER	SYMBOL	LIMIT	UNIT				
Junction to Lead Thermal Resistance	$R_{\Theta JL}$	35	°C/W				
Junction to Ambient Thermal Resistance	$R_{\Theta JA}$	80	°C/W				
Junction to Case Thermal Resistance	R <sub>eJC</sub>	25	°C/W				

Thermal Performance Note: Units mounted on recommended PCB (16mm x 16mm Cu pad test board)

ELECTRICAL SPECIFICATIONS (T <sub>A</sub> = 25°C unless otherwise noted)						
PARAMETER		CONDITIONS	SYMBOL	TYP	MAX	UNIT
	ES2LD			-	0.94	V
Forward voltage (1)	ES2LG	I <sub>F</sub> = 1A, T <sub>J</sub> = 25°C	$V_{F}$	-	1.30	V
	ES2LJ			-	1.70	V
		T <sub>J</sub> = 25°C		-	10	μΑ
Reverse current @ rated V <sub>R</sub>	per aloae \	T <sub>J</sub> = 125°C	$I_R$	-	350	μΑ
	ES2LD			25	-	pF
Junction capacitance	ES2LG	1 MHz, V <sub>R</sub> =4V C <sub>J</sub>	20	-	pF	
ES2LJ				20	-	pF
Reverse recovery time		I <sub>F</sub> =0.5A , I <sub>R</sub> =1.0A I <sub>RR</sub> =0.25A	t <sub>rr</sub>	-	35	nS

#### Notes:

- 1. Pulse test with PW=0.3 ms
- 2. Pulse test with PW=30 ms

ORDERING INFORMATION							
PART NO.	PART NO. SUFFIX	PACKING CODE	PACKING CODE SUFFIX	PACKAGE	PACKING		
		R5		SMB	850 / 7" Plastic reel		
ES2Lx	Н	R4	G	SMB	3,000 / 13" Paper reel		
(Note1, 2)		M4		SMB	3,000 / 13" Plastic reel		

#### Notes:

- 1. "x" defines voltage from 200V (ES2LD) to 600V (ES2LJ)
- 2. Whole series with green compound (halogen-free)

EXAMPLE					
EXAMPLE P/N	PART NO.	PART NO. SUFFIX	PACKING CODE	PACKING CODE SUFFIX	DESCRIPTION
ES2LDHR5G	ES2LD	Н	R5	G	AEC-Q101 qualified Green compound



#### **CHARACTERISTICS CURVES**

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$ 

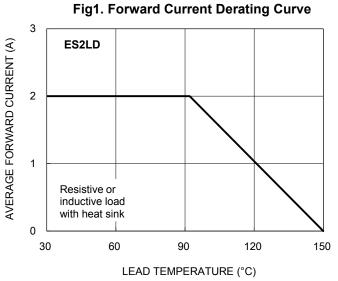
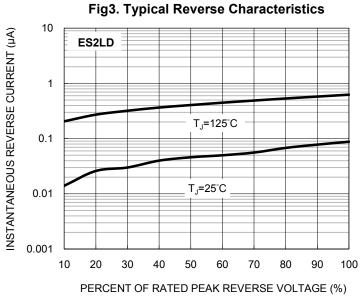
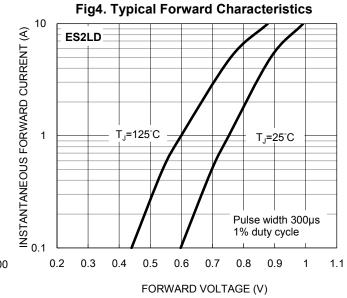


Fig2. Typical Junction Capacitance 800 ES2LD 600 CAPACITANCE (pF) 400 200 f=1.0MHz Vsig=50mVp-p 0 20 40 60 80 100 REVERSE VOLTAGE (V)

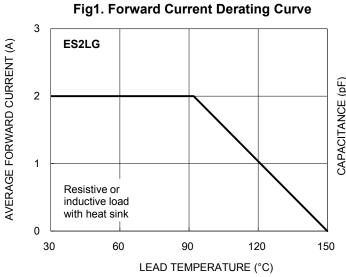


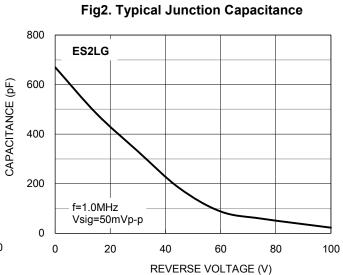


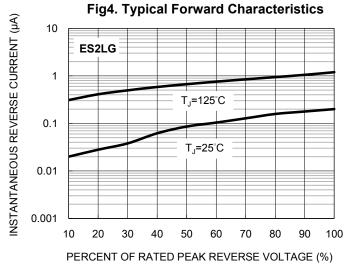


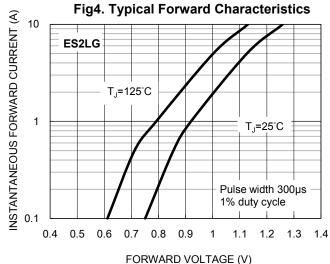
#### **CHARACTERISTICS CURVES**

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$ 











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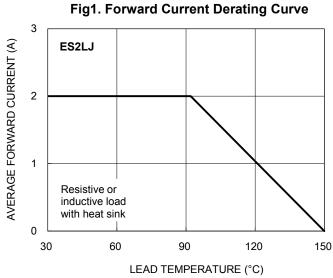
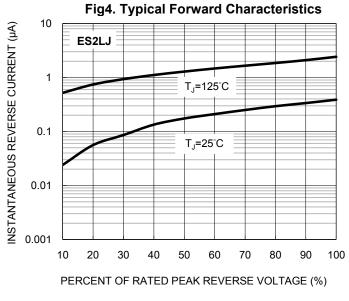
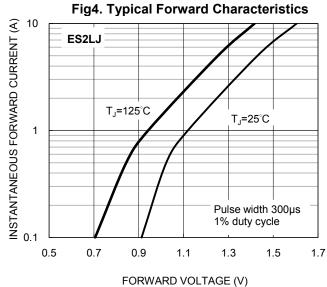


Fig2. Typical Junction Capacitance 800 ES2LJ CAPACITANCE (pF) 600 400 200 f=1.0MHz Vsig=50mVp-p 0 80 20 40 60 100 REVERSE VOLTAGE (V)

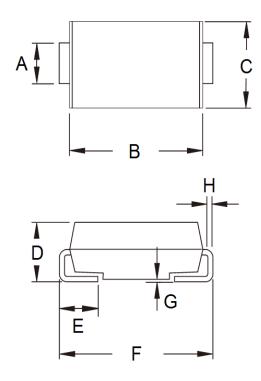






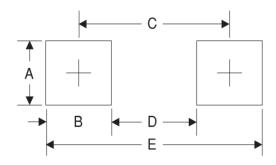
### **PACKAGE OUTLINE DIMENSIONS**

DO-214AA (SMB)



DIM.	Unit	(mm)	Unit (inch)	
DIIVI.	Min	Max	Min	Max
Α	1.95	2.20	0.077	0.087
В	4.05	4.60	0.159	0.181
С	3.30	3.95	0.130	0.156
D	1.95	2.65	0.077	0.104
Е	0.75	1.60	0.030	0.063
F	5.10	5.60	0.201	0.220
G	0.05	0.20	0.002	0.008
Н	0.15	0.31	0.006	0.012

### **SUGGESTED PAD LAYOUT**



Symbol	Unit (mm)	Unit (inch)
А	2.3	0.091
В	2.5	0.098
С	4.3	0.169
D	1.8	0.071
E	6.8	0.268

#### **MARKING DIAGRAM**



P/N = Marking Code G = Green Compound YW = Date Code F = Factory Code



Taiwan Semiconductor

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