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Silicon TVS diodes

ESD / transient protection of CAN/LIN

bus networks power supply lines according to:

IEC61000-4-2 (ESD): ±30kV (air / contact)

IEC61000-4-4 (EFT): 80 A (5/50 ns)

IEC61000-4-5 (surge): 5 A (8/20µs)

ISO7637-2: Pulse 1 (max. 50 V),

Pulse 2 (max. 125 V), Pulse 3a, b (max.800 V)

Max. working voltage: 24 V

• Low capacitance: 24 pF typ.

• Low clamping voltage: < 41 V

• Extremely low reverse current: < 1 nA typ.

Pb-free (RoHS compliant) package

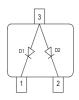


Applications

- Low and High-Speed CAN
- Fault Tolerant CAN
- Industrial control networks
- 12/24 V DC power supply lines



ESD24VS2U



Туре	Package	Configuration	Marking
ESD24VS2U	SOT23	2 lines, uni-directional*	EUs

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^{* 1} line, bi-directional between pins 1 and 2, if pin 3 is not connested



Maximum Ratings at $T_A = 25$ °C, unless otherwise specified

Parameter	Symbol	Value	Unit
ESD contact discharge ¹⁾	V _{ESD}	30	kV
Peak pulse current $(t_p = 8 / 20 \mu s)^2)$	I _{pp}	5	А
Peak pulse power $(t_p = 8 / 20 \mu s)^2$	P_{pk}	230	W
Operating temperature range	T_{op}	-55150	°C
Storage temperature	$T_{ m stg}$	-65150	

Electrical Characteristics at $T_A = 25$ °C, unless otherwise specified

Parameter	Symbol	Values		Unit	
		min.	typ.	max.	
Characteristics	·	•	•	•	
Reverse working voltage	V_{RWM}	-	-	24	V
Breakdown voltage	$V_{(BR)}$	26	-	32	
$I_{(BR)} = 1 \text{ mA}$					
Reverse current	I _R	-	<1	10	nA
V _R = 24 V					
Clamping voltage	V_{CL}				V
$I_{PP} = 1 \text{ A}, t_p = 8 / 20 \ \mu\text{s})^{2}$		-	30	34	
$I_{PP} = 5 \text{ A}, t_p = 8 / 20 \mu\text{s})^{2}$		-	36	41	
Line capacitance ³⁾	C _T				pF
$V_{R} = 0 \text{ V}, f = 1 \text{ MHz}, \text{ (pins 1 to 2, pin 3 n.c.)}$		_	24	28	
$V_{R} = 0 \text{ V}, f = 1 \text{ MHz}, \text{ (pins 1 or 2 to 3)}$		-	48	52	

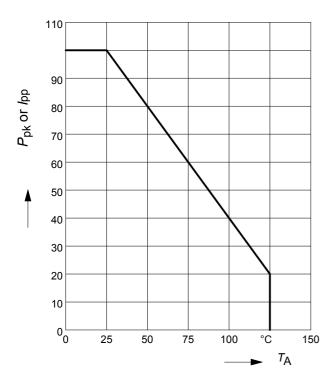
 $^{^{1}}V_{\mbox{\footnotesize{ESD}}}$ according to IEC61000-4-2. Device stressed with 10 positive / negative ESD pulses.

 $^{^2\}emph{I}_{\rm pp}$ according to IEC61000-4-5. Non-repetitive current pulse.

³Total capacitance line to ground (per linie)

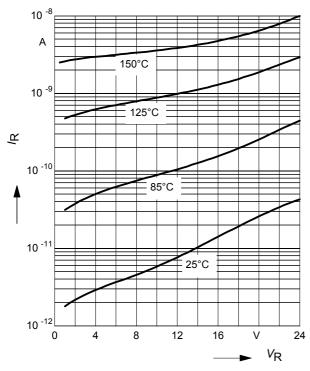


Power derating curve $P_{pk} = f(T_A)$



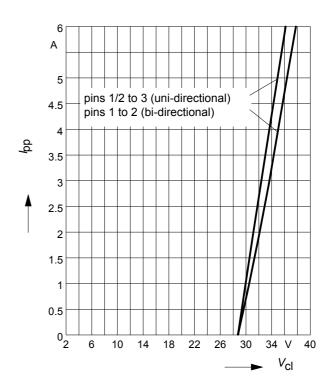
Reverse current $I_R = f(V_R)$

 T_A = Parameter, pins 1 / 2 to 3 (uni-directional)



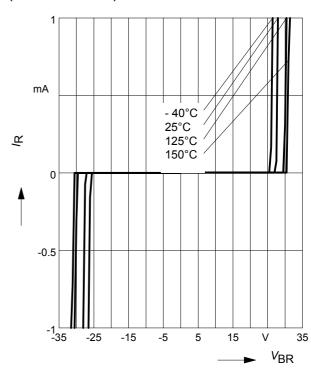
Clamping voltage, $V_{cl} = f(I_{pp})$

 $t_{\rm p}$ = 8 / 20 $\mu {\rm s}$



Breakdown voltage $V_{BR} = f(I_R)$

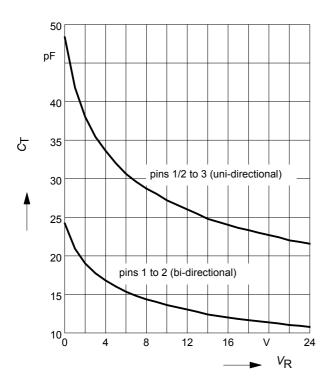
 T_A = Parameter, pins 1 to 2 (bi-directional)





Line capacitance $C_T = f(V_R)$

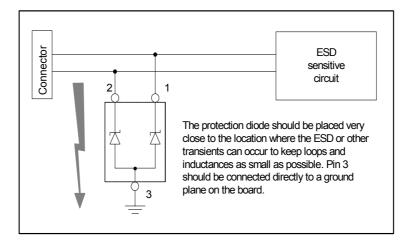
f = 1MHz





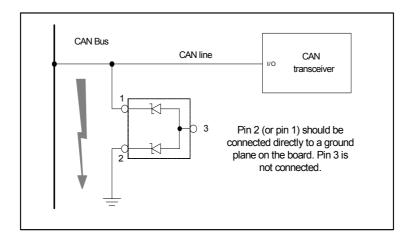
Application example ESD24VS2U (uni-directional)

12V / 24V DC power supply line protection



Application example ESD24VS2U (bi-directional)

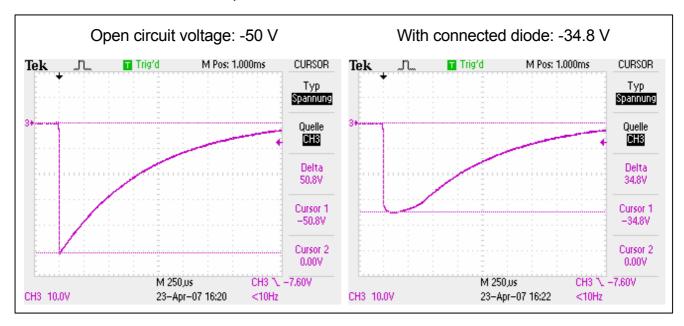
Single Wire CAN and LIN bus protection





Clamping voltage according to ISO 7637-2: Pulse 1

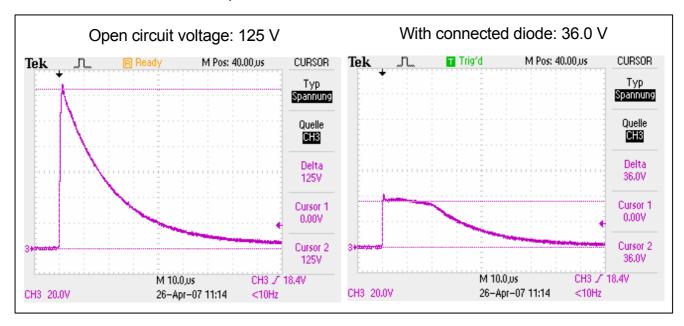
Ri = 10 Ohm, td = 2 ms, 5000 pulses





Clamping voltage according to ISO 7637-2: Pulse 2a

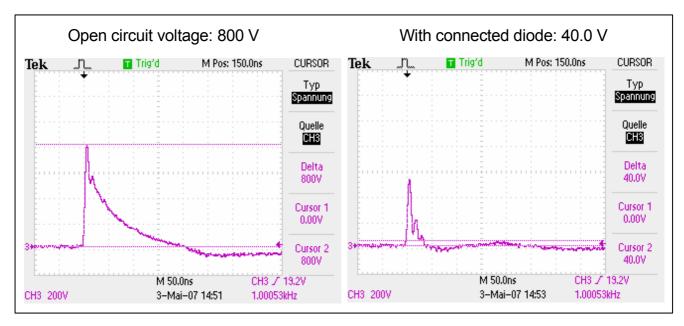
Ri = 10 Ohm, td = 2 us, 4000 pulses, 60 min





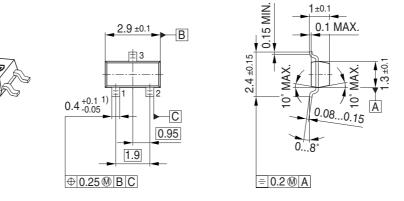
Clamping voltage according to ISO 7637-2: Pulse 3

Ri = 50 Ohm, td = 100 ns, 10 min



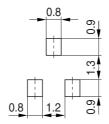


Package Outline

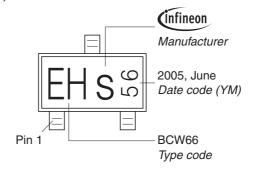


1) Lead width can be 0.6 max. in dambar area

Foot Print

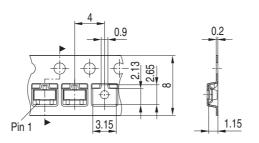


Marking Layout (Example)



Standard Packing

Reel ø180 mm = 3.000 Pieces/Reel Reel ø330 mm = 10.000 Pieces/Reel



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