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With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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### **Low Capacitance TVS Diode**

 ESD / transient protection of high-speed data lines up to:

IEC61000-4-2 (ESD): ±30 kV (air / contact)

IEC61000-4-4 (EFT): 4 kV / 80 A (5/50 ns)

IEC61000-4-5 (surge): 6 A (8/20 μs)

• Reverse working voltage: 5.3 V max.

• Very low reverse current: < 1 nA typ.

• Low capacitance: < 2 pF.

 Very low clamping voltage: 10 V typ. at positive transients, 2.5 V typ. at negative transients

• Very low series inductance down to 0.4 nH typ.

Pb-free (RoHS compliant) package

### **Applications**

- Mobile communication
- FM antenna protection
- USB 2.0, 10/100/1000 Ethernet, Firewire, DVI,
- Consumer products (STB, MP3, DVD, DSC...)
- LCD displays, camera
- Notebooks and desktop computers, peripherals



### ESD5V3L1U-02LRH



Туре	Package	Configuration	Marking
ESD5V3L1U-02LRH	TSLP-2-17	1 line, uni-directional	E8





**Maximum Ratings** at  $T_A = 25$ °C, unless otherwise specified

Parameter	Symbol	Value	Unit
ESD (air / contact) discharge <sup>1)</sup>	V <sub>ESD</sub>	30	kV
Peak pulse current $(t_p = 8 / 20 \mu s)^2$	I <sub>pp</sub>	6	Α
Operating temperature range	$T_{op}$	-55125	°C
Storage temperature	$T_{\rm stg}$	-65150	

**Electrical Characteristics** at  $T_{\Delta} = 25^{\circ}$ C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
Characteristics					
Reverse working voltage, from pin 1 to 2	$V_{RWM}$	-	-	5.3	V
Breakdown voltage	$V_{(BR)}$	6	-	-	
$I_{(BR)} = 1 \text{ mA}$ , from pin 1 to 2					
Reverse current	$I_{R}$	-	< 1	100	nA
$V_{R}$ = 5.3 V, from pin 1 to 2					
Clamping voltage	$V_{\mathrm{CL}}$				V
$I_{PP}$ = 1 A, $t_{p}$ = 8/20 µs <sup>2)</sup> , from pin 1 to 2		-	9	-	
$I_{PP} = 3 \text{ A}, t_p = 8/20 \ \mu\text{s}^{2}$ , from pin 1 to 2		-	10	-	
Forward clamping voltage	$V_{FC}$				
$I_{PP}$ = 1 A, $t_{p}$ = 8/20 µs <sup>2)</sup> , from pin 2 to 1		-	1.5	-	
$I_{PP} = 3 \text{ A}, t_p = 8/20 \ \mu\text{s}^{2)}$ , from pin 2 to 1		-	2.5	-	
Line capacitance <sup>3)</sup>	C <sub>T</sub>				pF
$V_{R} = 0 \text{ V}, f = 1 \text{ MHz}$		-	1	2	
$V_{R} = 5 \text{ V}, f = 1 \text{ MHz}$		-	1	2	
Series inductance	L <sub>S</sub>	-	0.4	-	nH

 $<sup>^{1}</sup>V_{\text{ESD}}$  according to IEC61000-4-2

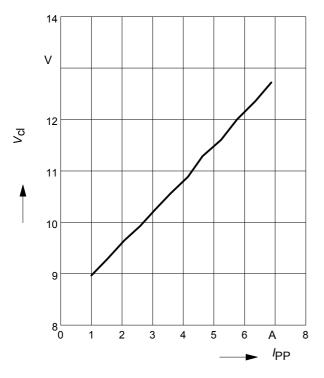
 $<sup>^2</sup>I_{\rm pp}$  according to IEC61000-4-5

<sup>&</sup>lt;sup>3</sup>Total capacitance line to ground



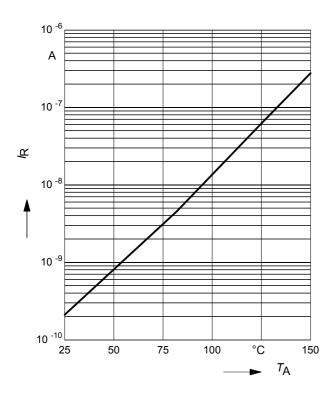
Clamping voltage,  $V_{cl} = f(I_{pp})$ 

 $t_{\rm p}$  = 8 / 20 µs, pin 1 to 2



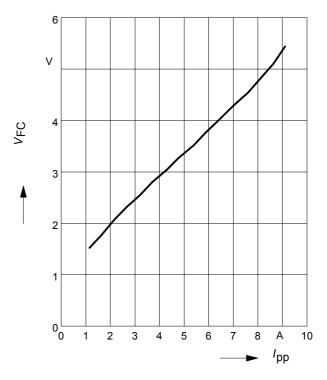
Reverse current  $I_R = f(T_A)$ 

 $V_{R}$  = 5.3V, from pin 1 to 2



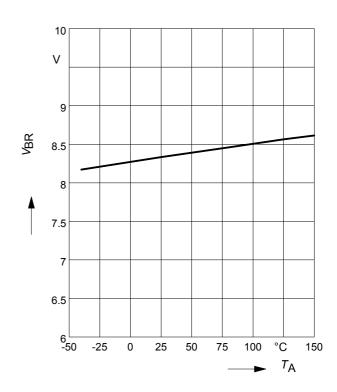
Forward clamping voltage  $V_{FC} = f(I_{PP})$ 

 $t_{\rm p}$  = 8 / 20 µs, pin 2 to 1



Breakdown voltage  $V_{br} = f(T_A)$ 

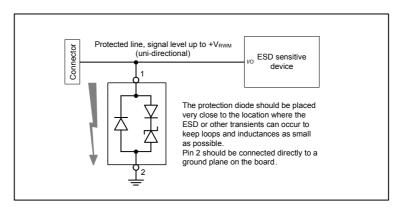
 $I_{\mathsf{F}}$  = 1mA, from pin 1 to pin 2





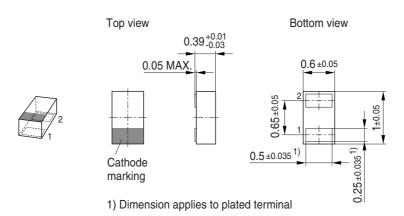
# Application example ESD5V3L1U-02LRH

1-channel, uni-directional



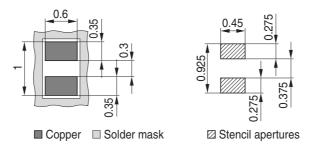


# Package Outline

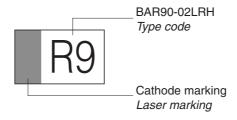


### Foot Print

For board assembly information please refer to Infineon website "Packages"

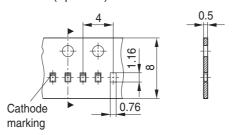


# Marking Layout (Example)



# Standard Packing

Reel ø180 mm = 15.000 Pieces/Reel Reel ø330 mm = 50.000 Pieces/Reel (optional)



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#### Edition 2009-11-16

Published by Infineon Technologies AG 81726 Munich, Germany

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