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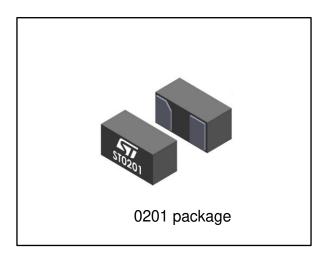


ESDZV5-1BF4



Ultra low clamping single line bidirectional ESD protection

Datasheet - production data



Features

- Ultra low clamping voltage: 7 V (IEC 61000-4-2 contact discharge 8 kV at 30 ns/ 16 A TLP)
- Bidirectional device
- Low leakage current
- 0201 package
- ECOPACK®2 compliant component
- Exceeds the following standard:
 - IEC 61000-4-2 level 4 = ±30 kV (air discharge) and ±18 kV (contact discharge)

Applications

Where transient over voltage protection in ESD sensitive equipment is required, such as:

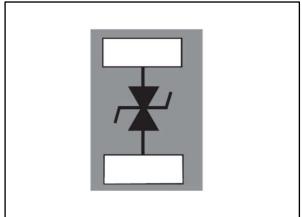
- Smartphones, mobile phones and accessories
- Tablets and notebooks
- Portable multimedia devices and accessories
- Wearable, home automation, healthcare
- Highly integrated systems

Description

The ESDZV5-1BF4 is a bidirectional single line TVS diode designed to protect the data line or other I/O ports against ESD transients.

The device is ideal for applications where both reduced line capacitance and board space saving are required.

Figure 1: Functional diagram



Characteristics ESDZV5-1BF4

1 Characteristics

Table 1: Absolute ratings (T_{amb} = 25 °C)

Symbol	Paran	Value	Unit	
V _{PP}	Peak pulse voltage Contact discharge Air discharge		18 30	kV
P _{PP}	Peak pulse power dissipation (8	70	W	
I _{PP}	Peak pulse current (8/20 μs)	7	Α	
Tj	Operating junction temperature	-55 to +150	°C	
T _{stg}	Storage temperature range	-65 to +150	°C	
TL	Maximum lead temperature for s	260	°C	

Figure 2: Electrical characteristics (definitions)

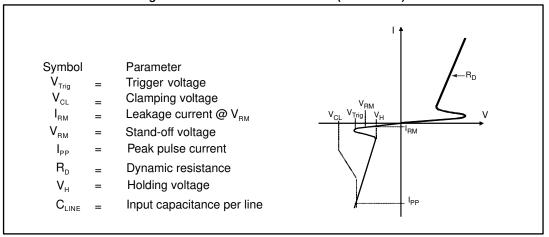


Table 2: Electrical characteristics (T_{amb} = 25 °C)

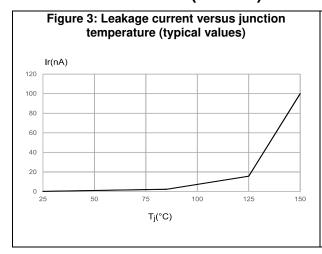
Symbol	Test condition		Тур.	Max.	Unit
V_{Trig}	Higher voltage than V_{Trig} guarantees the protection turn-on	5.8		10	V
V _H	Lower voltage than $V_{\mbox{\scriptsize H}}$ guarantees the protection turn-off	4	4.6		
I _{RM}	$V_{RM}^{(1)} = 5.5 \text{ V}$			100	nA
V _{CL}	8 kV contact discharge after 30 ns, IEC 61000-4-2		7		V
V _{CL}	$8/20 \mu s$ waveform, $I_{PP} = 7 A$			10	V
CLINE	$F = 1 \text{ MHz}, V_{\text{LINE}} = 0 \text{ V}, V_{\text{OSC}} = 30 \text{ mV}$		6	7.5	pF
R_D	Pulse duration 100 ns		0.18		Ω

Notes:

⁽¹⁾Application note: when used to protect a line connected to a DC source, the DC voltage must be lower than the minimum V_H to enable the diode to return to its non-conducting state after the transient.

ESDZV5-1BF4 Characteristics

1.1 Characteristics (curves)



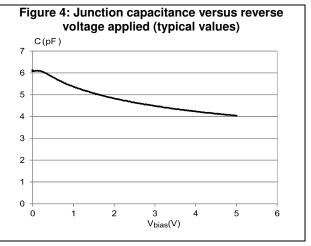


Figure 5: ESD response to IEC 61000-4-2 (+8 kV contact discharge)

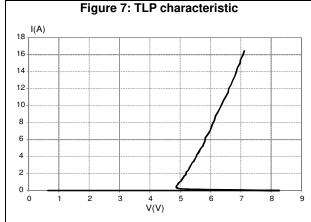
5 V/div

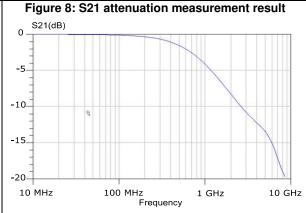
Peak clamping voltage at 30 ns Clamping voltage at 60 ns Clamping voltage at 100 ns

Figure 6: ESD response to IEC 61000-4-2 (-8 kV contact discharge)

5 V/div

Peak clamping voltage
Clamping voltage at 60 ns
Clamping voltage at 100 ns
Clamping voltage at 100 ns
Clamping voltage at 100 ns





2 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: **www.st.com**. ECOPACK® is an ST trademark.

2.1 0201 package information

Top Side D₁ \$fE E1 Bottom

Figure 9: 0201 package outline



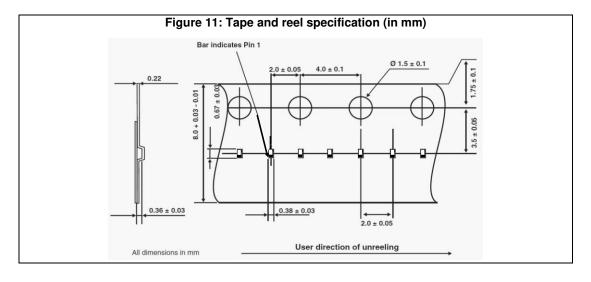
The marking codes can be rotated by 90 ° or 180° to differentiate assembly location. In no case should this product marking be used to orient the component for its placement on a PCB. Only pin 1 mark is to be used for this purpose.

Table 3: 0201 package mechanical data

rabio or olor package modification data						
	Dimensions					
Ref.	Millimeters					
	Min.	Тур.	Max.			
Α	0.270	0.300	0.330			
b	0.1675	0.1875	0.2075			
D	0.560	0.580	0.600			
D1		0.3375				
Е	0.260	0.280	0.300			
E1	0.205	0.225	0.245			
fD	0.0175	0.0275	0.0375			
fE	0.0175	0.0275	0.0375			

Figure 10: Marking

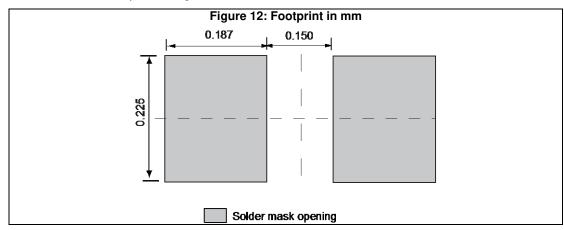
A



3 Recommendation on PCB assembly

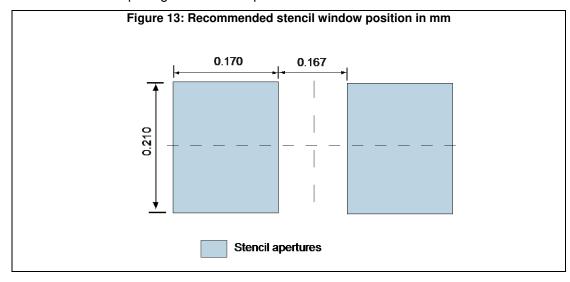
3.1 Footprint

- 1. Footprint in mm
 - a. SMD footprint design is recommended.



3.2 Stencil opening design

- 1. Reference design
 - a. Stencil opening thickness: 75 µm / 3 mils



3.3 Solder paste

- 1. Halide-free flux qualification ROL0 according to ANSI/J-STD-004.
- 2. "No clean" solder paste is recommended.
- 3. Offers a high tack force to resist component movement during high speed.
- 4. Use solder paste with fine particles: powder particle size 20-38 μm.

3.4 **Placement**

- Manual positioning is not recommended.
- It is recommended to use the lead recognition capabilities of the placement system, not the outline centering
- 3. Standard tolerance of ±0.05 mm is recommended.
- 1.0 N placement force is recommended. Too much placement force can lead to squeezed out solder paste and cause solder joints to short. Too low placement force can lead to insufficient contact between package and solder paste that could cause open solder joints or badly centered packages.
- To improve the package placement accuracy, a bottom side optical control should be performed with a high resolution tool.
- For assembly, a perfect supporting of the PCB (all the more on flexible PCB) is recommended during solder paste printing, pick and place and reflow soldering by using optimized tools.

3.5 PCB design preference

- To control the solder paste amount, the closed via is recommended instead of open
- The position of tracks and open vias in the solder area should be well balanced. A symmetrical layout is recommended, to avoid any tilt phenomena caused by asymmetrical solder paste due to solder flow away.

3.6 Reflow profile

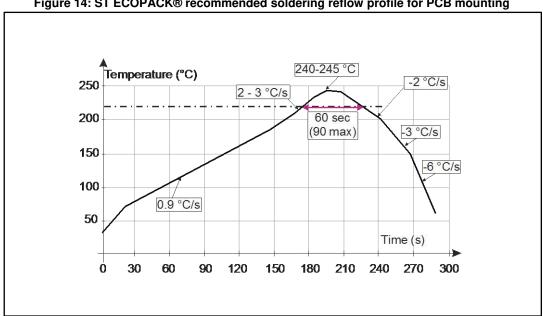


Figure 14: ST ECOPACK® recommended soldering reflow profile for PCB mounting



Minimize air convection currents in the reflow oven to avoid component movement. Maximum soldering profile corresponds to the latest IPC/JEDEC J-STD-020.

Ordering information ESDZV5-1BF4

4 Ordering information

Figure 15: Ordering information scheme

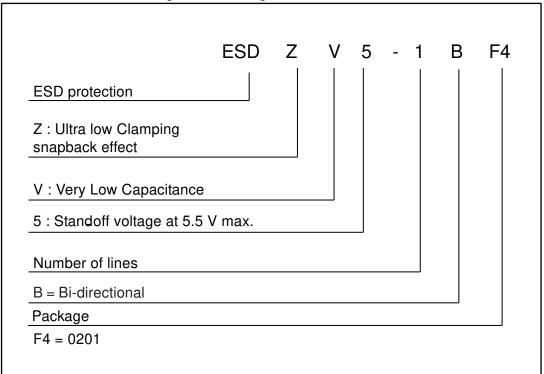


Table 4: Ordering information

Order code Marking		Package	Weight	Base qty.	Delivery mode
ESDZV5-1BF4	A ⁽¹⁾	0201	0.116 mg	15000	Tape and reel

Notes:

5 Revision history

Table 5: Document revision history

Date Revision		Changes		
06-Apr-2017 1		First issue.		
28-Jul-2017	2	Updated footprint title.		

 $^{^{(1)}}$ The marking can be rotated by multiples of 90° to differentiate assembly location

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