



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



FEATURES

- » High performance product with low RC time constant
- » Long lifetime with over 500,000 duty cycles
- » Rated capacitance of 100F
- » Compliant with RoHS and REACH requirements



* Image is not to scale

SPECIFICATIONS

Electrical		ESHSR-0100C0-002R7
Rated Voltage (V_R) at 65°C		2.7VDC
Rated Voltage (V_R) at 85°C		2.3VDC
Surge Voltage ¹		2.85VDC
Rated Capacitance ²		100F
Capacitance Tolerance	Max.	0% / +20%
	Avg. ⁴	0% / +10%
DC-ESR ³	Max.	12mΩ
	Avg. ⁴	8mΩ
Max. Leakage Current ⁵		0.26mA
Maximum Continuous Current	at $\Delta T = 15^\circ\text{C}$	10A
	at $\Delta T = 40^\circ\text{C}$	17A
Maximum Peak Current, Non-repetitive ⁶	at 65°C	61A
	at 85°C	52A
Max. Stored Energy (E_{\max}) at V_R ⁷	at 65°C	0.10Wh
	at 85°C	0.07Wh
Usable Specific Power ⁷	at 65°C	3,470W/kg
	at 85°C	2,510W/kg
Impedance Match / Specific Power ⁷	at 65°C	7,230W/kg
	at 85°C	5,240W/kg
Max. Gravimetric Specific Energy ⁷		4.8Wh/kg

Temperature	
Operating Temperature Range	-40 ~ 65°C (up to 85°C with de-rated voltage) ($\Delta\text{CAP} < 5\%$ and $\Delta\text{ESR} < 100\%$ of initial value measured at 25°C, with linear voltage de-rating to 2.3V @ 85°C)
Storage Temperature Range	-40 ~ 70°C (storage without charge)

Life		
Endurance ^{8,9}	at 65°C, 2.7V	1,500 hours
	at 85°C, 2.3V	1,000 hours
Room Temperature (at V_R and 25°C) ⁸	10 years	
Cycle Life (at 25°C) ⁸	500,000 cycles (Cycled from V_R to $1/2V_R$ using 100mA/F const. current with 10sec rest between charge and discharge steps)	
Shelf Life	2 years (Stored without charge at or under 70°C and under 40% RH)	

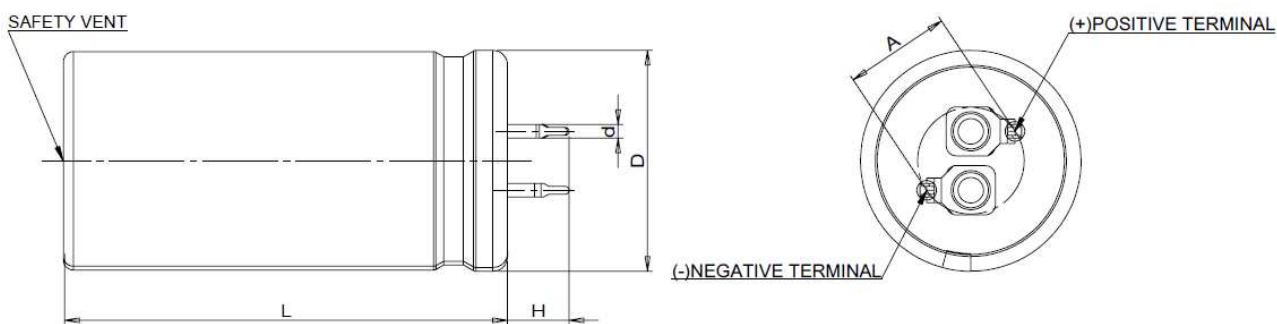
Safety & Certification	
RoHS	Compliant
REACH	Compliant
UL	Complies to 810A, Certificate No.: BBBG2.MH46340

THERMAL

Characteristics	ESHSR-0100C0-002R7
Typical Thermal Resistance, R_{th} (Cell Housing)	11.6°C/W
Typical Thermal Capacitance, C_{th}	34.5J/°C
Cont. Current to $\Delta T = 15^{\circ}\text{C}$	10A
Cont. Current to $\Delta T = 40^{\circ}\text{C}$	17A

PHYSICAL

Drawing



Recommended PCB Pattern Hole Size: 2.0 (± 0.1) mm

See Note on Mounting¹⁰

Dimensions	ESHSR-0100C0-002R7
D (+1.0)	22.0 mm
L (+2.0)	45.0 mm
H (± 1.0)	6.0 mm
d (± 0.05)	1.5 mm
A (± 0.2)	10.0 mm
Nominal Weight	21.0 g

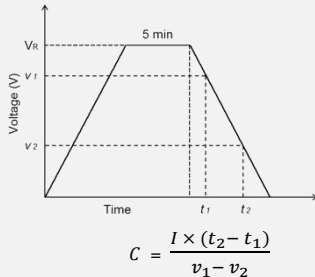
NOTE

1. Surge Voltage

- > Absolute maximum voltage, not repeated and for no longer than 1 second.

2. Rated Capacitance

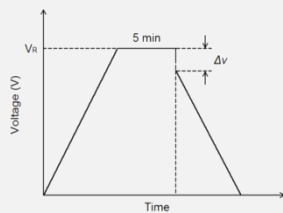
- > Constant current charge with 10mA/F to V_R
- > Constant voltage charge at V_R for 5min
- > Constant current discharge with 10mA/F to 0.1V



Where v_1 is the measurement starting voltage, $0.8 \times V_R$ (V);
 v_2 is the measurement end voltage, $0.4 \times V_R$ (V);
 t_1 is the time from discharge start to reach v_1 (s);
 t_2 is the time from discharge start to reach v_2 (s);
 I is the absolute value of the discharging current (A).

3. ESR (Equivalent Series Resistance)

- > ESR_{DC}
 - Constant current charge to V_R
 - Constant voltage charge at V_R for 5min
 - Constant current discharge to 0.1V



$$R_d = \frac{\Delta v}{I}$$

Where R_d is the ESR_{DC} (Ω);
 Δv is the voltage drop for 10ms (V);
 I is the discharge current (A).

4. Average (or Typical)

- > Percentage spread that may be present in one shipment

5. Leakage Current

- > The capacitor is charged to the rated voltage at 25°C.
- > Leakage current is the current at 72hours that is required to keep the capacitor charged at the rated voltage

6. Max. Current

- > Current for 1sec discharging from rated voltage to half rated voltage under constant current discharging mode.

$$I_{Max.} (A) = \frac{\frac{1}{2}V_R}{\Delta t / C + R_d}$$

Where Δt is the discharge time (sec) and Δt is 1 sec in this case;
 C is the capacitance (F);
 R_d is the ESR_{DC} (Ω);
 V_R is the rated voltage (V).

- > Max. Current **should not** be used in normal operation and is only provided as a reference value.

7. Energy & Power

- > Max. Stored Energy at $V_R = \frac{\frac{1}{2}CV_R^2}{3600}$

Where C is the capacitance (F);
 V_R is the rated voltage (V).

- > Usable Specific Power, IEC 62391-2 (W/kg) = $\frac{0.12 \cdot V^2}{ESR_{DC} \cdot Mass}$

- > Impedance Match Specific Power (W/kg) = $\frac{0.25 \cdot V^2}{ESR_{DC} \cdot Mass}$

- > Gravimetric Specific Energy (Wh/kg) = $\frac{E_{Max.}}{Weight}$

8. Lifetime

- > End-of-Life Conditions
 - Capacitance: -30% from rated min. value
 - ESR: +100% from max. ESR value

9. Endurance

- > Conditions
 - Temperature: $65 \pm 2^\circ\text{C}$ or $85 \pm 2^\circ\text{C}$
 - Test duration: 1500 (+48/-0) h
 - Applied voltage: $V_R \pm 0.02V$
 - Capacitance and ESR measurement are made at 25°C

10. Mounting

- > Provide properly spaced holes for mounting according to the cell dimensions as to minimize leads being mechanically stressed.
- > Do not place any copper patterns, including the ground pattern, or through-hole via underneath the cell or on the underside of the PCB (if a double-sided PCB is used) as the electrolyte inside the cell, if it should leak, can corrode, short-circuit, the patterns and/or damage other components nearby. Spacing of 1mm or more should be provided in between the footprint of the cell and the nearest copper pattern.
- > Protective coating of components nearby on the PCB is recommended to reduce the risk of them being damaged in an event of electrolyte leakage.
- > Provide at least 2mm clearance above the safety vent and do not position anything above the safety vent that may be damaged by vent rupture.
- > Place cells on the PCB taking into account that the cells may not be completely hermetic during its lifetime. Electrolyte vapor and gases generated during normal use may escape the package during normal operation.
- > Soldering recommendation for small and medium size cells available on www.nesscap.com under Support > Download.

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 Nesscap Energy Inc. Suite 3800, Royal Bank Plaza, South Tower, 200 Bay Street, P.O. Box 84, Toronto, Ontario, M5J 2Z4 CANADA	 Nesscap Energy Inc. S24040 Camino Del Avion #A118, Monarch Beach, California, 92629 USA	 Nesscap Co., Ltd. 17, Dongtangiheung-ro 681beon-gil, Giheung-gu, Yongin-si, Gyeonggi-do REPUBLIC OF KOREA	 Nesscap China Room 1608, Block N, Cangson Building, Chegongmiao, Futian District, Shenzhen City, P.R.C CHINA	 Nesscap Energy EASTERN EUROPE 10A, prospect 60-Ietiya Oktyabrya Moscow, 117036 RUSSIA	 Nesscap Energy GmbH Beerengarten 4 D-86938 Schondorf GERMANY
marketing@nesscap.com			info@nesscap-energy.de		