



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

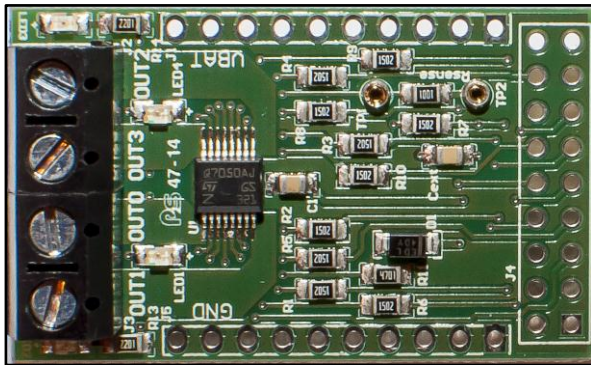
Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



VNQ7050AJ evaluation board

Data brief



Features

Max transient supply voltage	V_{CC}	41 V
Operating voltage range	V_{CC}	4 to 28 V
Typ. on-state resistance (per Ch)	R_{ON}	50 m Ω
Current limitation (typ)	I_{LIMH}	27 A
Stand-by current (max)	I_{STBY}	0.5 μ A

- Simple single IC application board dedicated for VNQ7050AJ
- Provides electrical connectivity and thermal heat-sinking for easy prototyping
- General device features
 - Quad channel smart high side driver with analog feedback
 - Very low standby current
 - Compatible with 3 V and 5 V CMOS outputs

- Diagnostic functions
 - Dedicated high precision proportional load current sense
 - Overload and short to ground (power limitation) indication
 - Thermal shutdown indication
 - OFF-state open-load detection
 - Output short to V_{CC} detection
 - Sense enable/disable
- Protections
 - Undervoltage shutdown
 - Overvoltage clamp
 - Load current limitation
 - Self limiting of fast thermal transients
 - Configurable latch-off on overtemperature or power limitation with dedicated fault reset pin
 - Loss of ground and loss of V_{CC}
 - Reverse battery with external components
 - Electrostatic discharge protection

Application

Typical applications are all types of automotive resistive, inductive and capacitive loads.

Table 1: Device summary

Order code	Reference
EV-VNQ7050AJ	VNQ7050AJ evaluation board

2 Board connections

Figure 2: "Evaluation board connections" shows the placement of the connectors to be used to supply the evaluation board, to connect the load and to control the functionality and diagnostic of the device.

Figure 2: Evaluation board connections

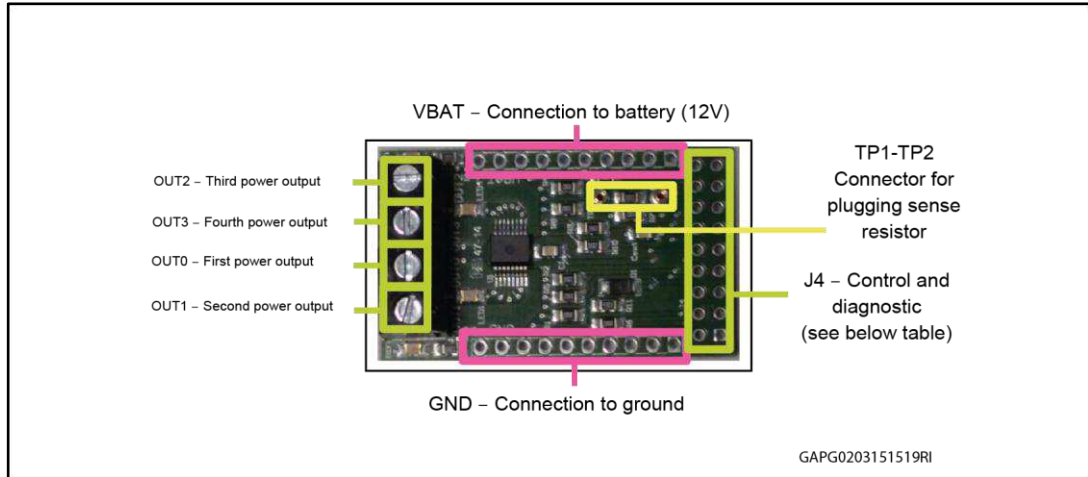


Table 2: J4 connector: pin functions

Connector	Pin number	Pin name	Pin function
J4	1...4	N/A	Not connected
J4	5	IN_PullUP	Connection to optional external pull-up resistor for open load detection in off-state.
J4	6	+5V	5V Power Supply
J4	7	FaultRST	Active low compatible with 3V and 5V CMOS outputs pin; it unlatches the output in case of fault; If kept low, sets the outputs in auto-restart mode.
J4	8	MultiSense	Multiplexed analog sense output pin; it delivers a current proportional to the selected diagnostic: load current, supply voltage or chip temperature.
J4	9	S_EN	Active high compatible with 3V and 5V CMOS outputs pin; it enables the MultiSense diagnostic pin.
J4	10...12	SEL _{0,1,2}	Active high compatible with 3V and 5V CMOS outputs pin; they address the MultiSense multiplexer.
J4	13...16	IN _{0,1,2,3}	Voltage controlled input pin with hysteresis, compatible with 3V and 5V CMOS outputs. They control output switch state.
J4	17...18	N/A	Not connected

In case the user wishes to utilize the Current Sense/MultiSense function of the device, it is necessary to plug a sense resistor in R_{SENSE}.

The package includes a through-hole resistor, to be mounted on TP1-TP2 (see [Figure 4: "Mounting through-hole sense resistor"](#)).

Different R_{SENSE} values can be adopted based on user preference.

Another option is soldering an SMD resistor on the dedicated PCB pad, as shown in [Figure 5: "Pads for soldering SMD resistor"](#).

Figure 3: No sense resistor



Figure 4: Mounting through-hole sense resistor

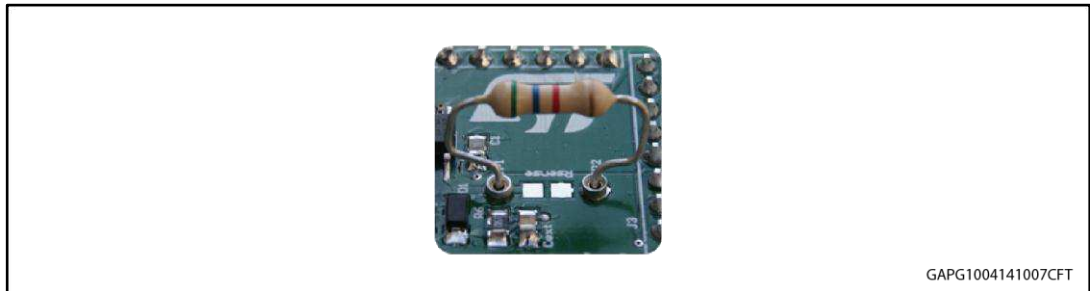
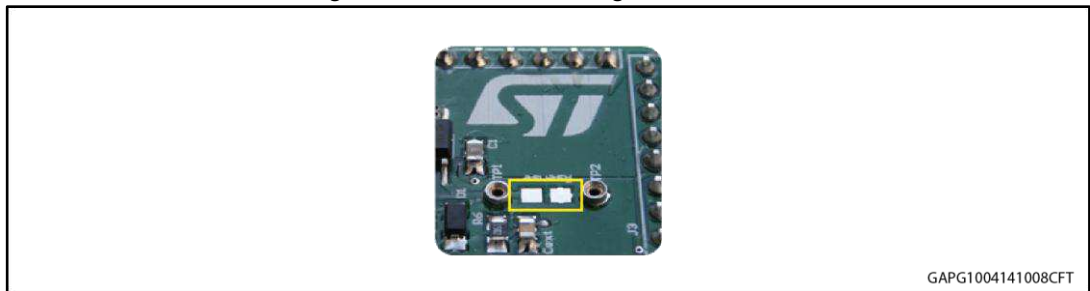


Figure 5: Pads for soldering SMD resistor



3 Thermal data

Table 3: Thermal data

Symbol	Parameter	Max	Unit
$R_{thj-amb}$	Thermal resistance junction-ambient (MAX)	43	°C/W

Table 4: PCB specifications

Parameter	Value
Board dimensions	25 mm x 41.5 mm
Number of Cu layer	2
Layer Cu thickness	35 μ m
Board finish thickness	1.6 mm +/- 10%
Board Material	FR4
Thermal vias diameter	0.3 mm

4 Revision history

Table 5: Revision history

Date	Revision	Changes
27-Feb-2015	1	Initial release.
05-Aug-2015	2	Changed image in the cover page. Updated Section 1: "Description"

IMPORTANT NOTICE – PLEASE READ CAREFULLY

STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, enhancements, modifications, and improvements to ST products and/or to this document at any time without notice. Purchasers should obtain the latest relevant information on ST products before placing orders. ST products are sold pursuant to ST's terms and conditions of sale in place at the time of order acknowledgement.

Purchasers are solely responsible for the choice, selection, and use of ST products and ST assumes no liability for application assistance or the design of Purchasers' products.

No license, express or implied, to any intellectual property right is granted by ST herein.

Resale of ST products with provisions different from the information set forth herein shall void any warranty granted by ST for such product.

ST and the ST logo are trademarks of ST. All other product or service names are the property of their respective owners.

Information in this document supersedes and replaces information previously supplied in any prior versions of this document.

© 2015 STMicroelectronics – All rights reserved