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Ultralow Power, ±200 g Digital Accelerometer Breakout Board

FEATURES

2 sets of spaced vias for population of 5-pin headers Easily attached to prototyping board or PCB Small size and board stiffness minimize impact on the system and acceleration measurements

EQUIPMENT NEEDED

External host processor

DOCUMENTS NEEDED

ADXL372 data sheet

GENERAL DESCRIPTION

The EVAL-ADXL372Z is a simple evaluation board that allows quick evaluation of the performance of the ADXL372 ultralow power, 3-axis, digital output MEMS accelerometer. The EVAL-ADXL372Z is ideal for evaluation of the ADXL372 in an existing system because the stiffness and the small size of the evaluation board minimize the effect of the board on both the system and acceleration measurements.

Full specifications for the ADXL372 are available in the product data sheet; consult the data sheet in conjunction with this user guide when working with the evaluation board.

EVALUATION BOARD PHOTOGRAPH

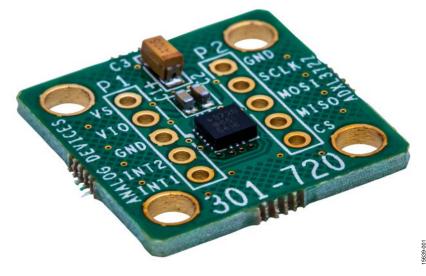


Figure 1.

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REVISION HISTORY

3/2017—Revision 0: Initial Version

EVALUATION BOARD HARDWARE

The EVAL-ADXL372Z has two sets of 0.1 inch spaced vias for populating the 5-pin headers that provide access to all power and signal lines. The vias or headers allow attachment of the evaluation board either to a prototyping board (breadboard) or to a printed circuit board (PCB) in an existing system. Four holes are provided that are set 15 mm × 15 mm at the corners of the PCB for mechanical attachment of the EVAL-ADXL372Z to the application fixture. An external host processor is required for communication to the device.

The dimensions of the EVAL-ADXL372Z are 20 mm \times 20 mm.

CIRCUIT DESCRIPTION

The PCB layout of the EVAL-ADXL372Z is shown in Figure 1. The EVAL-ADXL372Z is equipped with three factory installed capacitors for bypass: two 0.1 μ F capacitors (C1 and C2) and a 10 μ F capacitor (C3). C2 and C3 are V_S bypass capacitors for

reducing analog supply noise and C1 (located between $V_{\text{DDI/O}}$ and GND) is for reducing digital clocking noise.

The schematic of the EVAL-ADXL372Z is shown in Figure 2. See the ADXL372 data sheet for information on configuring the accelerometer following its connection to the application host processor.

HANDLING CONSIDERATIONS

The EVAL-ADXL372Z is not reverse polarity protected. Reversing the Vs or $V_{DDI/O}$ supply and GND pins can cause damage to the ADXL372.

Dropping the EVAL-ADXL372Z on a hard surface can generate several thousand *g* of acceleration, which may exceed the data sheet absolute maximum limits. See the ADXL372 data sheet for more information.

EVALUATION BOARD SCHEMATICS AND ARTWORK

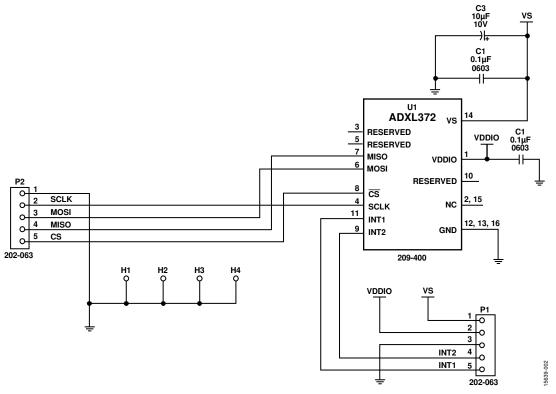


Figure 2. EVAL-ADXL372Z Schematic

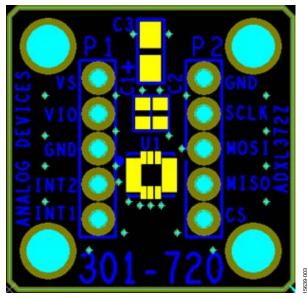


Figure 3. Printed Circuit Board Layout

EVAL-ADXL372Z User Guide

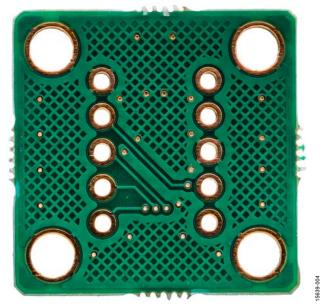


Figure 4. EVAL-ADXL372Z Bottom Layout



Figure 5. EVAL-ADXL372Z Top Layout

ORDERING INFORMATION

BILL OF MATERIALS

Table 1.

| Qty | Reference Designator | Description | Manufacturer | Part Number |
|-----|-----------------------------|--|----------------------|--------------------|
| 1 | U1 | Ultralow power MEMS accelerometer | Analog Devices, Inc. | ADXL372BCCZ-ENG |
| 2 | C1, C2 | 0.1 μF ceramic capacitor, 50 V, 10%, X7R | CAL-CHIP | GMC10X7R104K50NTLF |
| 1 | C3 | 10 μF tantalum capacitor, 10 V, 10% | CAL-CHIP | TCKIA106ATL |

RELATED LINKS

| Resource | Description |
|---------------|-----------------------------------|
| ADXL372 | Product page, ADXL372 |
| EVAL-ADXL372Z | ADXL372 breakout board user guide |



ESD Caution

ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

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