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SOT-23 Single Op Amp Evaluation Board User Guide UG-838

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Evaluation Board for Single, High Speed Op Amps Offered in 5-Lead SOT-23 and 6-Lead SOT-23 Packages

FEATURES

Enables quick breadboarding/prototyping User defined circuit configuration Edge mounted SMA connector provisions Easy connection to test equipment and other circuits RoHS compliant

GENERAL DESCRIPTION

The Analog Devices, Inc., SOT-23 evaluation board evaluates single, high speed op amps offered in 5-lead and 6-lead SOT-23 packages. The evaluation board is a bare board that enables users to quickly prototype a variety of single op amp circuits, which minimizes risk and reduces time to market. Figure 1 shows the component side of the bare evaluation board. Figure 2 shows the circuit side of the bare evaluation board.

The 6-layer evaluation board accepts SMA edge-mounted connectors on the inputs and outputs for efficient connection to test equipment or other circuitry. The ground plane, component placement, and supply bypassing minimize parasitic inductance and capacitance. The evaluation board components are primarily SMT 0603 case size, with the exception of the electrolytic bypass capacitors (C1 and C2), which are 3528 case size.

Figure 3 shows the evaluation board schematic. The printed circuit board (PCB) assembly drawings are shown in Figure 4 and Figure 5. The layout pattern for the PCB is shown in Figure 6 and Figure 7.

EVALUATION BOARD IMAGES

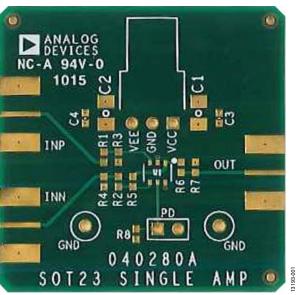


Figure 1. Component Side of Evaluation Board



Figure 2. Circuit Side of Evaluation Board

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TABLE OF CONTENTS

| Features | 1 |
|-------------------------|---|
| General Description | 1 |
| Evaluation Board Images | 1 |
| Revision History | 2 |

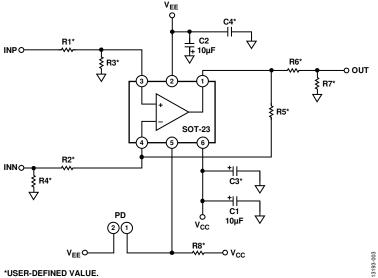
| Evaluation Board Schematic and Artwork | 3 |
|--|---|
| Ordering Information | 5 |
| Bill of Materials | 5 |

REVISION HISTORY

10/15—Revision 0: Initial Version

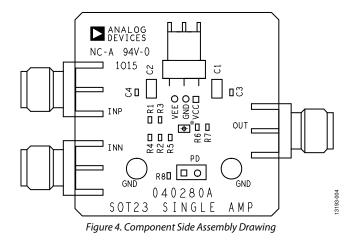
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EVALUATION BOARD SCHEMATIC AND ARTWORK



*USER-DEFINED VALUE.

Figure 3. Evaluation Board Schematic



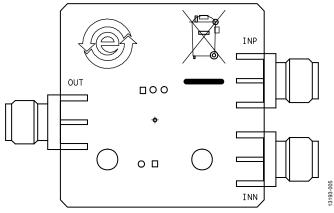


Figure 5. Circuit Side Assembly Drawing

SOT-23 Single Op Amp Evaluation Board User Guide

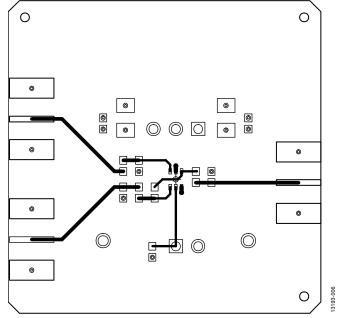


Figure 6. Component Side Layout Pattern

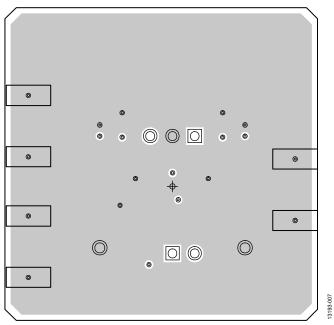


Figure 7. Circuit Side Layout Pattern

ORDERING INFORMATION

BILL OF MATERIALS

Table 1.

| Quantity | Reference Designator | Description | Package |
|----------|----------------------|-------------------------|-----------------------|
| 1 | VEE, VCC, GND | Power connector | 3-pin power connector |
| 1 | PD | Power-down/disable pin | 2-pin header |
| 2 | GND | Test point | Test point pin |
| 2 | C1, C2 | 10 μF | 3528 |
| 2 | C3, C4 | Capacitor, user defined | C603 |
| 3 | INP, INN, OUT | SMA SMT | SMA SMT |
| 8 | R1 to R8 | Resistor, user defined | R603 |
| 1 | Device under test | Amplifier | SOT-23-5 or SOT-23-6 |



ESD Caution

ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

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