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MODEL NO. :   TM047NBH01

ISSUED DATE: 2008-11-19

VERSION   :   Ver 2.0

□**Preliminary Specification**
■**Final Product Specification**

Customer :

Approved by	Notes

SHANGHAI TIANMA Confirmed :

Prepared by	Checked by	Approved by

This technical specification is subjected to change without notice

Table of Contents

Record of Revision

Rev	Issued Date	Description	Editor
1.0	2008-07-31	Rev 1.0 was issued	Yun Xu
2.0	2008-11-19	1, Add panel weight 2, Updated Model name from TS047NAARB01-00 to TM047NBH01	Haijun He

1 General Specifications

Feature		Spec
Display Spec.	Size	4.7 inch
	Resolution	480(RGB) x 272
	Interface	RGB 24 bits
	Color Depth	16.7M
	Technology type	a-Si
	Pixel pitch (mm)	0.216 x 0.216
	Pixel Configuration	R.G.B. Vertical Stripe
	Display Mode	TM with Normally White
	Surface Treatment(Up Polarizer)	Clear type (3H)
	Surface Treatment(TSP)	Anti-glare type (3H)
	Viewing Direction	12 o'clock
	Gray Scale Inversion Direction	6 o'clock
Mechanical Characteristics	LCM (W x H x D) (mm)	114.3x72.5x5.0
	Active Area(mm)	103.680 x 58.752
	With /Without TSP	With TSP
	Weight(g)	76.5
	LED Numbers	10 LEDs

Note 1：Viewing direction for best image quality is different from TFT definition, there is a 180 degree shift.

Note 2 : Requirements on Environmental Protection: RoHS

Note 3: The weight tolerance:±5%

2   Input/Output Terminals

2.1   TFT LCD Panel

Recommended connector: HIROSE FH19SC-45S-0.5SH

No	Symbol	I/O	Description	Remark
1	VLED-	P	Power for LED	
2	VLED+	P	Power for LED	
3	GND	P	Power Ground	
4	VDD	P	Power Supply (+3.3V)	
5	R0	I	Red data	
6	R1	I	Red data	
7	R2	I	Red data	
8	R3	I	Red data	
9	R4	I	Red data	
10	R5	I	Red data	
11	R6	I	Red data	
12	R7	I	Red data	
13	G0	I	Green data	
14	G1	I	Green data	
15	G2	I	Green data	
16	G3	I	Green data	
17	G4	I	Green data	
18	G5	I	Green data	
19	G6	I	Green data	
20	G7	I	Green data	
21	B0	I	Blue data	
22	B1	I	Blue data	
23	B2	I	Blue data	
24	B3	I	Blue data	
25	B4	I	Blue data	
26	B5	I	Blue data	
27	B6	I	Blue data	
28	B7	I	Blue data	
29	GND	P	Power Ground	
30	PCLK	I	Pixel clock	
31	DISP	I	Display on/off	
32	HSYNC	I	Horizontal sync signal	
33	VSYNC	I	Vertical sync signal	
34	DE	I	Date enable	
35	NC	-	No connection	
36	GND	P	Power Ground	
37	X1	P	Touch Panel X(Right Side)	
38	Y1	P	Touch Panel Y(6 Clock Side)	
39	X2	P	Touch Panel X(Left Side)	
40	Y2	P	Touch Panel Y(12 Clock Side)	
41	NC	-	No connection	
42	SCS	I	SPI chip select pin	
43	SCL	I	SPI Serial Clock	

| 44 | SDI | I | SPI Serial Data Input | |
| 45 | SDO | O | SPI Serial Data output | |

Note2-1:   I/O definition：

   I-----Input

   O---Output

   P----Power/Ground

3 Absolute Maximum Ratings

3.1 Driving TFT LCD Panel

Ta = 25°C

Item	Symbol	Min	Max	Unit	Remark
Power Supply Voltage	VDD	-0.3	4.6	V	
Input Signal Voltage	R0-R7,G0-G7,B0-B7,PCLK,DISP, HSYNC, VSYNC, DE, SCS, SCL, SDI	-0.3	VDD +0.3	V	
Back Light Forward Current	I_{LED}	--	25	mA	For each LED
Touch panel pin voltage	X1,X2,Y1,Y2	--	7	V	
Operating Temperature	T_{OPR}	-20	60	℃	
Storage Temperature	T_{STG}	-30	70	℃	

4 Electrical Characteristics

4.1 Driving TFT LCD Panel

GND=0V, Ta=25℃

Item		Symbol	Min	Typ	Max	Unit	Remark
Power Supply Voltage		VDD	3.0	3.3	3.6	V	
Input Signal Voltage	Low Level	V_{IL}	-0.3	--	0.2xVDD	V	R0-R7,G0-G7,B0-B7 PCLK, DISP, HSYNC, VSYNC, DE, SCS, SCL, SDI
	High Level	V_{IH}	0.8xVDD	--	VDD	V	
Output Signal Voltage	Low Level	V_{OL}	0	--	0.2xVDD	V	SDO
	High Level	V_{OH}	0.8xVDD	--	VDD-	V	
(Panel+ LSI) Power Consumption	Black Mode (60Hz)	--	85	90	mW		
	Standby Mode	--	0.8	1.0	uW		

4.2 Driving Backlight   Ta=25℃

Item	Symbol	Min	Typ	Max	Unit	Remark
Forward Current	I_F	--	40	50	mA	5LEDs serial x 2
Forward Voltage	V_F	--	16	--	V	
Power Consumption	W_{BL}	--	640	--	mW	

Note 4-2: The figure below shows the connection of backlight LED.

4.3 Block Diagram

```
┌─────────────────────────────────────────────────────────────────────────────┐
│                                                                               │
│   ┌──────────────┐     ┌──────────────────┐                                   │
│   │  LCD PANEL   │     │  Source +Gate    │      R(0~7), G(0~7), B(0~7)       │
│   └──────────────┘     │     Driver       │      HSYNC, VSYNC          RGB I/F │
│                        └──────────────────┘      PCLK,DE，DISP                 │
│   ┌──────────────┐     ┌──────────────────┐                                   │
│   │              │     │ Grayscale Manipulation │                      ┌───┐   │
│   │   4.7inch    │     │      Voltage     │                         SPI│   │   │
│   │ 480(RGB)X272 │     └──────────────────┘      SCS,SCL,SDI           │ F │   │
│   │              │     ┌────────┐  ┌────────┐                          │ P │   │
│   │              │     │  Tcon  │  │  Vcom  │     VDD           Power   │ C │   │
│   │              │     └────────┘  └────────┘                   Supply │   │   │
│   │              │     ┌────────────────┐        SDO                   │   │   │
│   └──────────────┘     │  Charge Pump   │                              │   │   │
│                        └────────────────┘        VLED+                 │   │   │
│   ┌──────┐                                        VLED-          LED    │   │   │
│   │ BLU  │                                                              │   │   │
│   └──────┘                                        X1,Y1,X2,Y2   Touch   │   │   │
│   ┌──────┐                                                       Panel  └───┘   │
│   │ TSP  │            LCD Driver Chip (COG)                                     │
│   └──────┘                                                                     │
│                                                                               │
└─────────────────────────────────────────────────────────────────────────────┘
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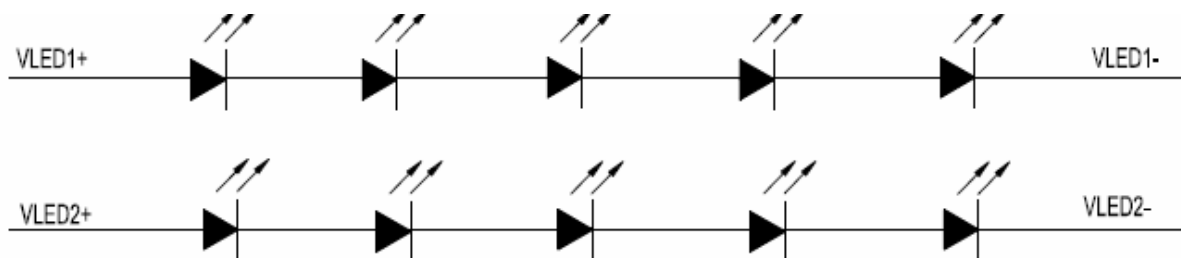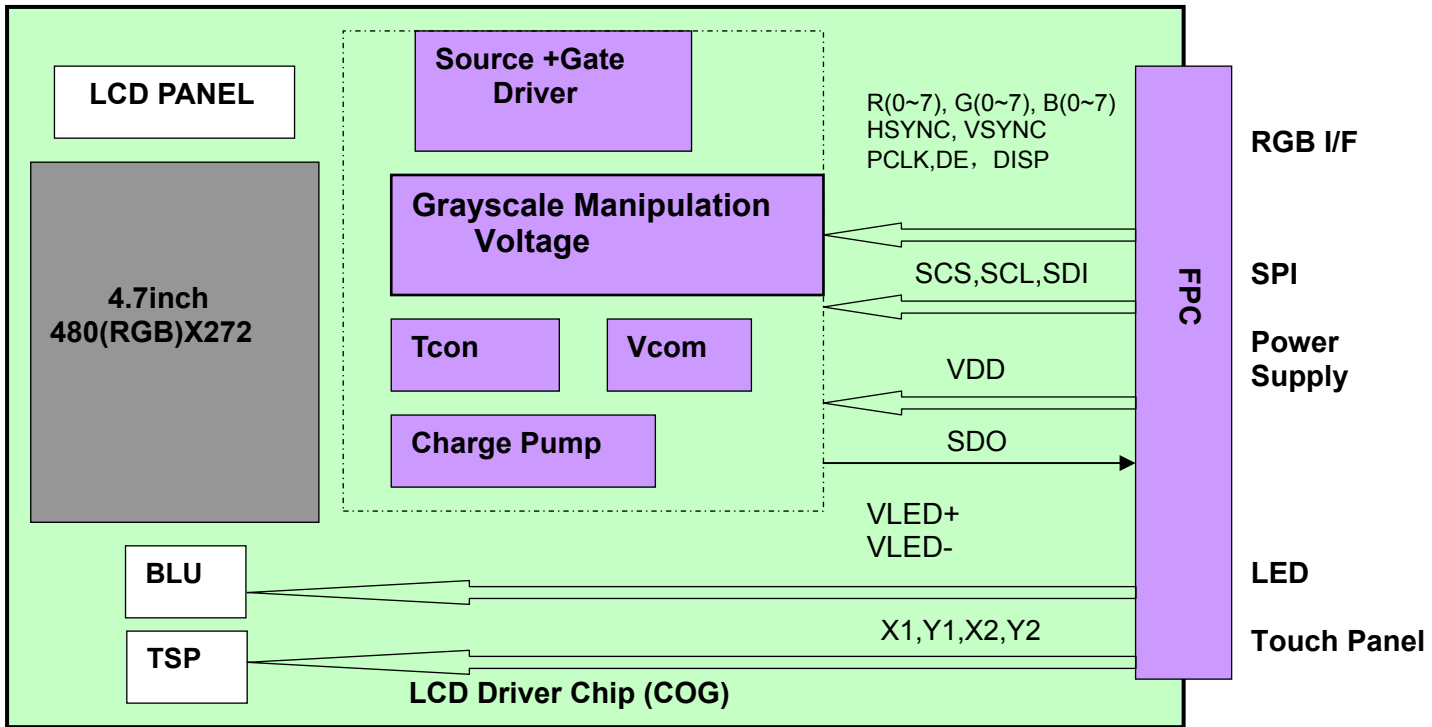
5 Timing Chart

5.1 RGB Timing Parameter

Item	Symbol	Values			Unit	Remark
		Min	Typ	Max		
Clock cycle	1/tc	-	9.00	15	MHz	
Hsync cycle	1/fH	-	17.14	-	KHz	
Vsync cycle	1/fV	-	59.94	-	Hz	
Horizontal signal	Th	525	525	605	CLK	
Horizontal display period	Thd	480	480	480-	CLK	
Horizontal Front porch	Thf	2	2	82	CLK	
Horizontal Pulse width	Thp	2	41	41	CLK	
Horizontal Back porch	Thb	2	2	41	CLK	
Vertical cycle	Tv	285-	286	511	H	
Vertical display period	Tvd	272	272	272	H	
Vertical Front porch	Tvf	1	2	227	H	
Vertical Pulse width	Tvp	1	10	11	H	
Vertical Back porch	Tvb	1	2	11	H	
DISP Setup Time	Tdiss	10	-	-	ns	
DISP Hold Time	Tdish	10	-	-	ns	
Clock Period	PW CLK	66.7	-	-	ns	
Clock Pulse High Period	PWH	26.7	-	-	ns	
Clock Pulse Low Period	PWL	26.7	-	-	ns	
Hsync Setup Time	Ths	10	-	-	ns	
Hsync Hold Time	Thh	10	-	-	ns	
Data Setup Time	Tds	10	-	-	ns	
Data Hold Time	Tdh	10	-	-	ns	
DE Setup Time	Tdes	10	-	-	ns	
DE Hold Time	Tdeh	10	-	-	ns	
Vsync Setup Time	Tvhs	10	-	-	ns	
Vsync Hold Time	Tvhh	10	-	-	ns	

Note 1: Thd=480CLK, Thf= 2CLK, Thp= 41CLK, Thb= 2CLK

       525CLK=480CLK + 2CLK + 41CLK + 2CLK

Note 2: Thf+ Thp+ Thb > 44 CLK

5.2 RGB Timing Chart
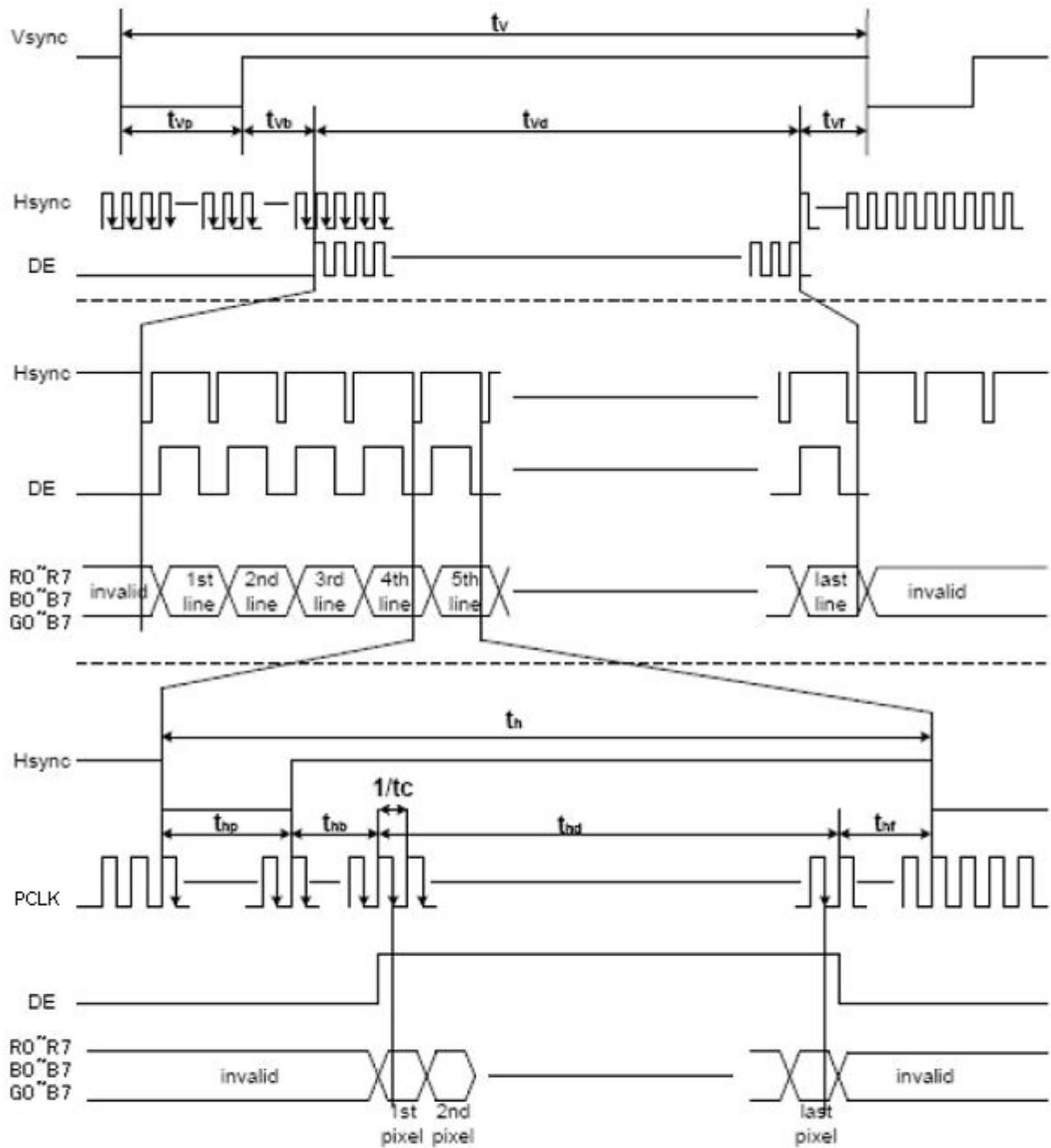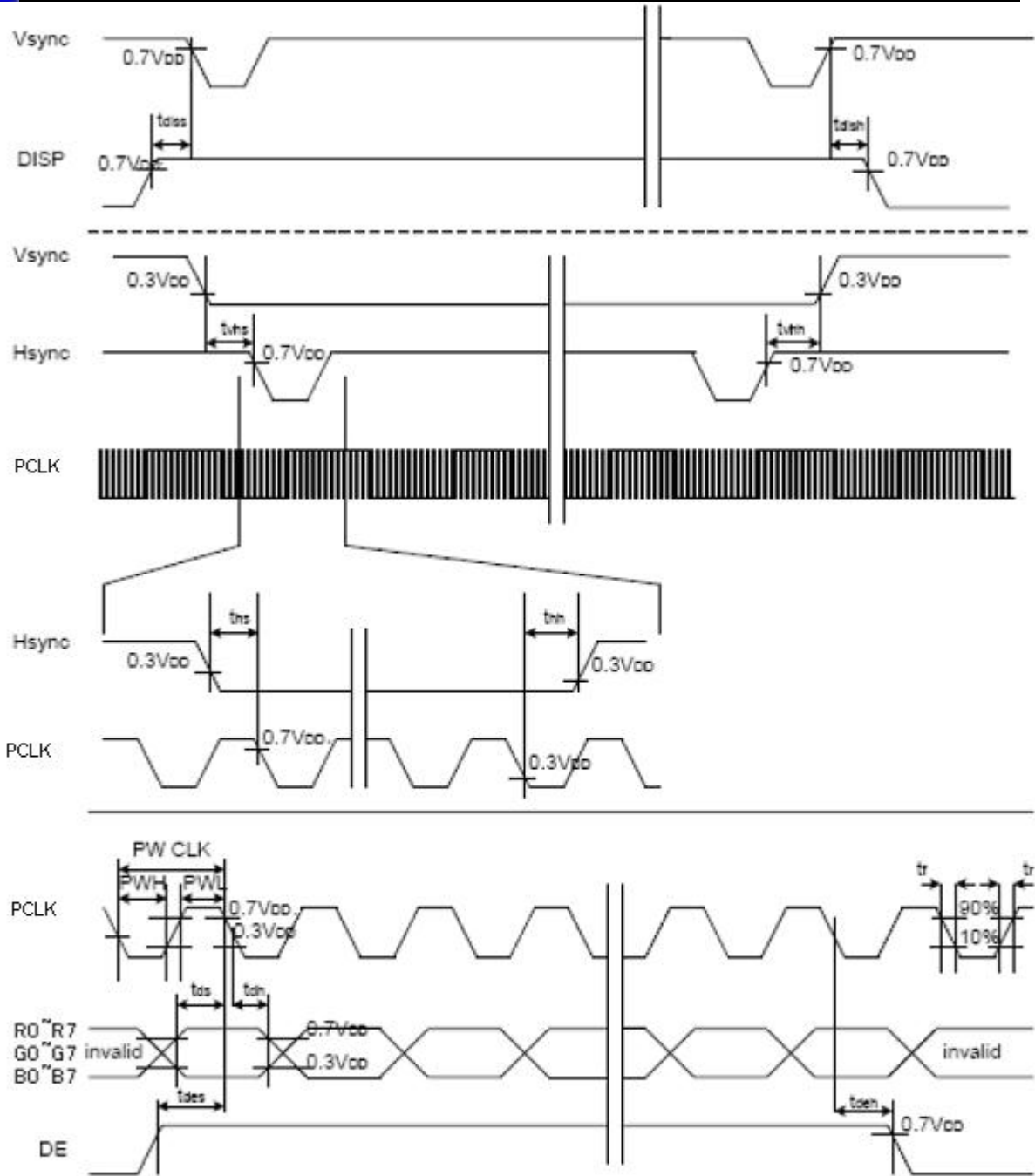
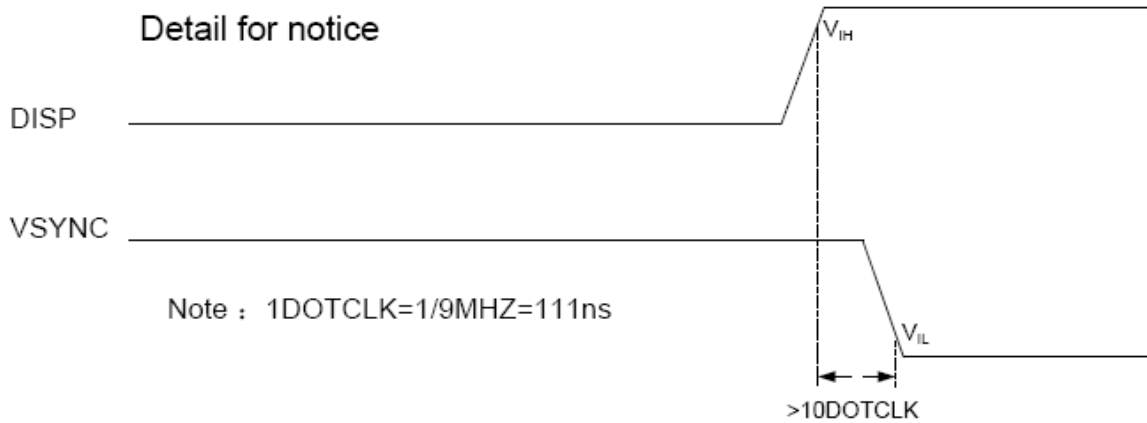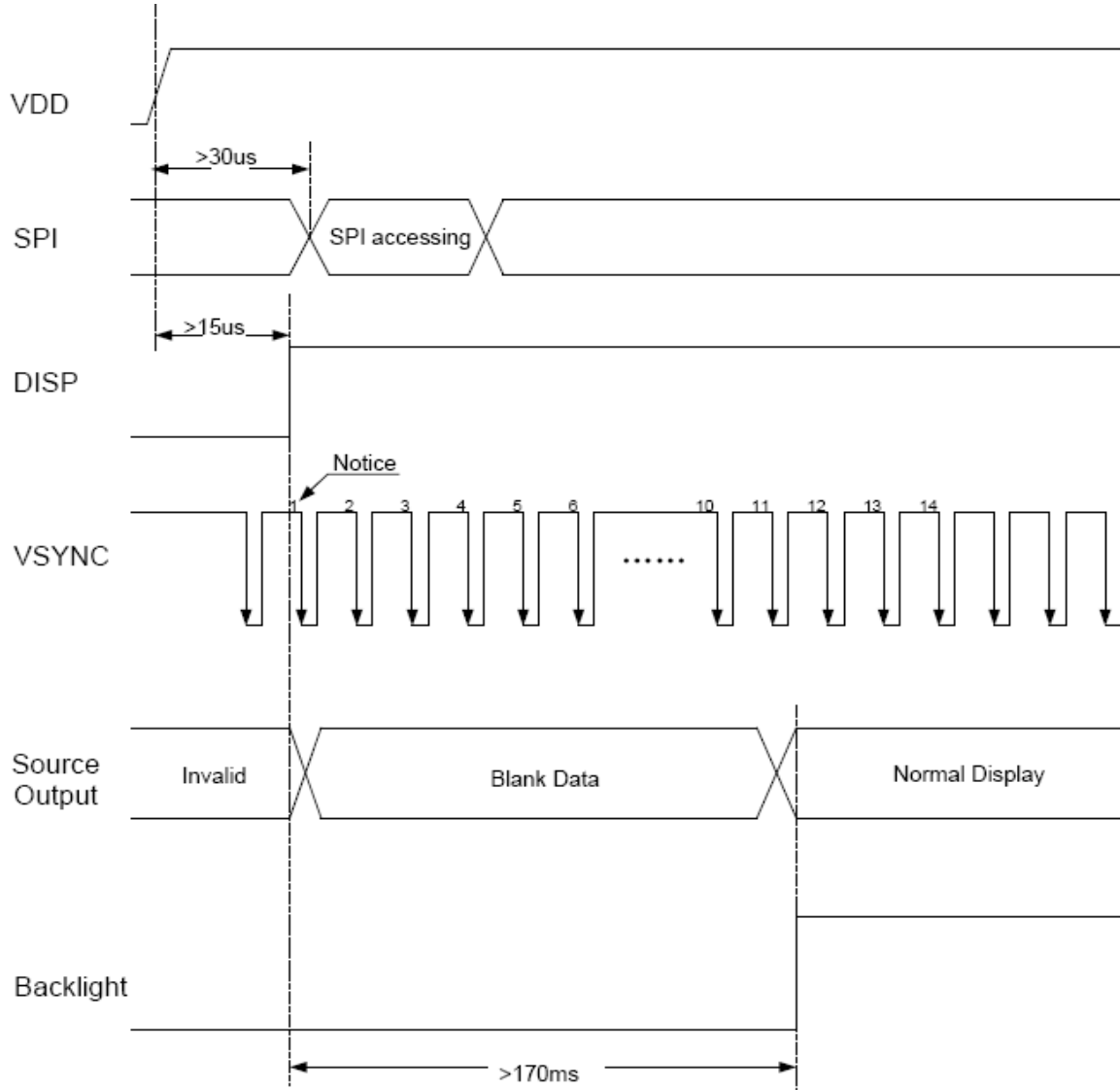### 5.3 SPI Timing Chart

(VIH=0.8*VDD, VIL=0.2*VDD)

First Transmission (Register)

Second Transmission (Data)
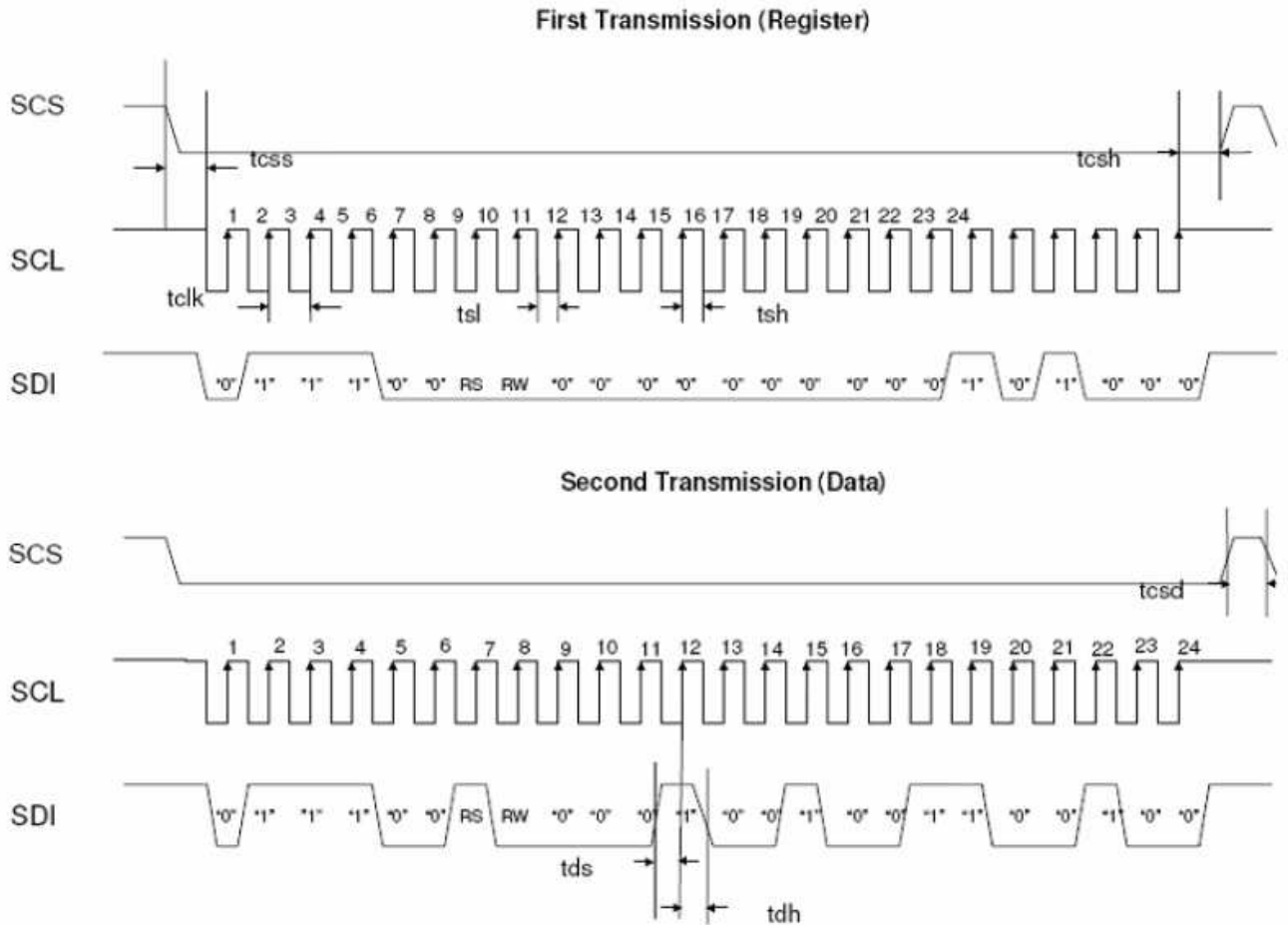
Parameter	Symbol	MIN	TYP	MAX	Unit
Serial Clock Frequency	f_{CLK}	-	-	20	MHz
Serial Clock Cycle Time	t_{CLK}	50	-	-	ns
Clock Low Width	t_{SL}	25	-	-	ns
Clock High Width	t_{SH}	25	-	-	ns
Chip Select Setup Time	t_{CSS}	0	-	-	ns
Chip Select Hold Time	t_{CSh}	10	-	-	ns
Chip Select High Delay Time	t_{CSD}	20	-	-	ns
Data Setup Time	t_{dS}	5	-	-	ns
Data Hold Time	t_{dh}	10	-	-	ns

5.4 Power On Sequence

VDD

>30us

SPI

SPI accessing

>15us

DISP

Notice

VSYNC

1 2 3 4 5 6 10 11 12 13 14 ......

Source
Output

Invalid Blank Data Normal Display

Backlight

>170ms

Detail for notice

DISP

V_{IH}

VSYNC

V_{IL}

Note: 1DOTCLK=1/9MHZ=111ns

>10DOTCLK

5.5   Power Off Sequence

VDD

DISP

VSYNC

Source Output

Valid   Blank Data

Backlight

>170ms

6 Optical Characteristics

6.1 Optical Specification

Ta=25℃

Item		Symbol	Condition	Min	Typ	Max	Unit	Remark
View Angles		ΘT	CR≧10	40	50	-	Degree	Note 2
		ΘB		60	70	-		
		ΘL		60	70	-		
		ΘR		60	70	-		
Contrast Ratio		CR	Θ=0°	400	500	-		Note1 Note3
Response Time		T$_{ON}$	25℃	-	25	40	ms	Note1 Note4
		T$_{OFF}$						
Chromaticity	White	x	Backlight is on	0.260	0.310	0.360		Note5, Note1
		y		0.280	0.330	0.380		
	Red	x		0.530	0.580	0.630		
		y		0.290	0.340	0.390		
	Green	x		0.290	0.340	0.390		
		y		0.530	0.580	0.630		
	Blue	x		0.100	0.150	0.200		
		y		0.040	0.090	0.140		
Uniformity		U		75	80	-	%	Note1 Note6
NTSC				-	50	-	%	Note 5
Luminance(with TSP)		L		230	280	-	cd/m^2	Note1 Note7
Luminance(without TSP)		L		300	350	-	cd/m^2	Note1 Note7

Test Conditions:

1. VDD=3.3V, I$_L$=20mA(LED current), the ambient temperature is 25℃.

2. The test systems refer to Note 1 and Note 2.

Note 1: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.

Item	Photo detector	Field
Contrast Ratio	SR-3A	1°
Luminance		
Chromaticity		
Lum Uniformity		
Response Time	BM-7A	2°
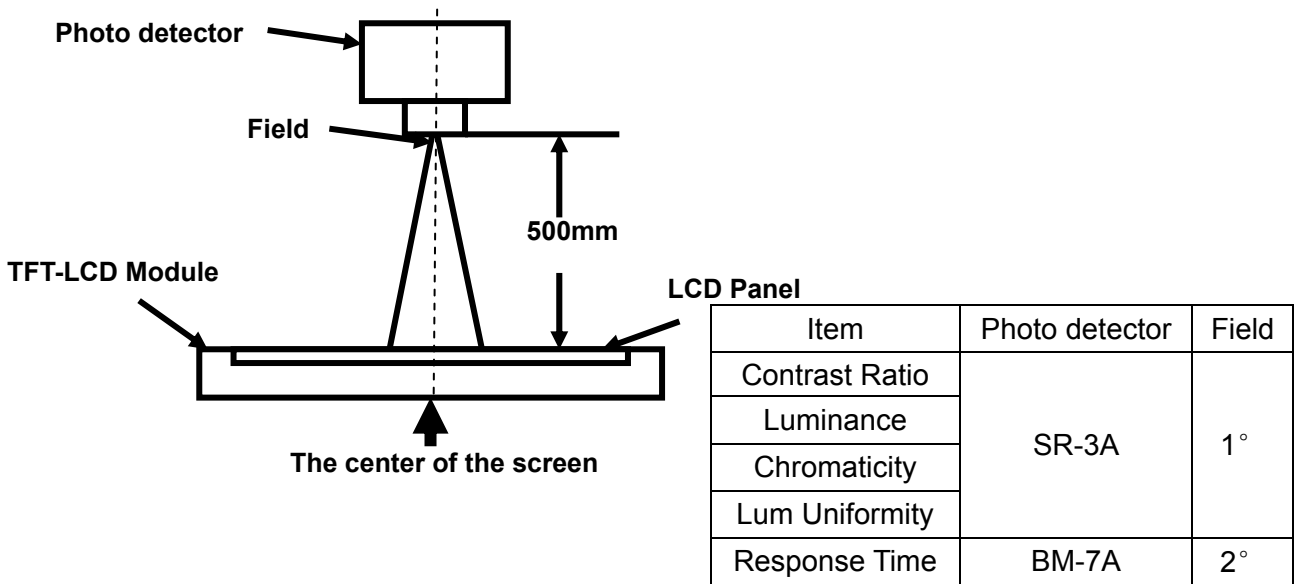
Note 2: Definition of viewing angle range and measurement system.

viewing angle is measured at the center point of the LCD by CONOSCOPE(ergo-80)。

Fig. 1 Definition of viewing angle

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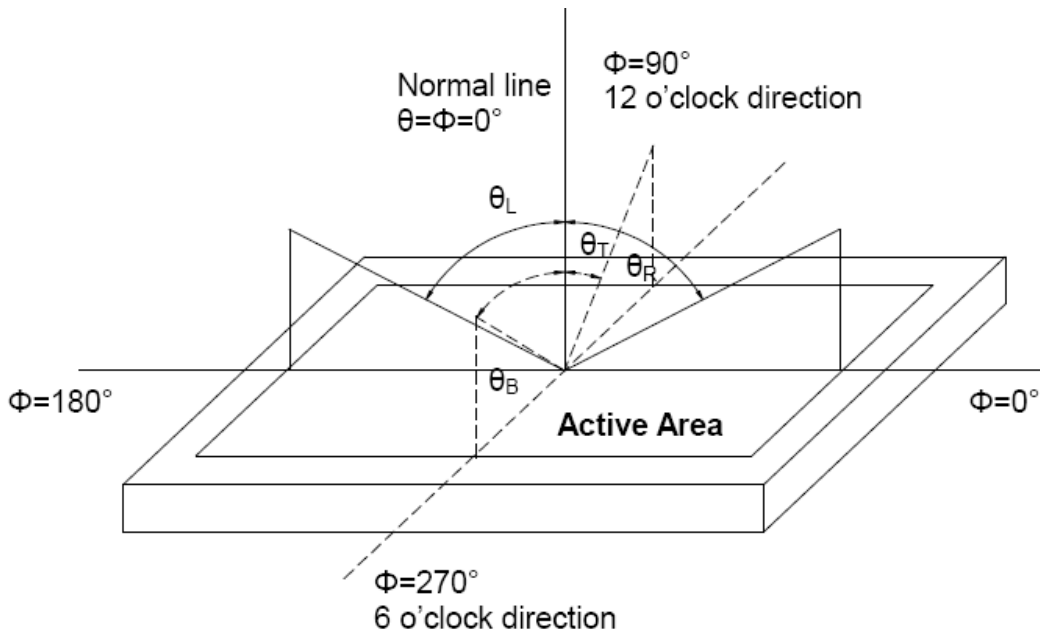
Note 3: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$$

"White state ":The state is that the LCD should driven by Vwhite.

"Black state": The state is that the LCD should driven by Vblack.

Vwhite: To be determined   Vblack: To be determined.

Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (TON) is the time between photo detector output intensity changed from 90% to 10%. And fall time (TOFF) is the time between photo detector output intensity changed from 10% to 90%.
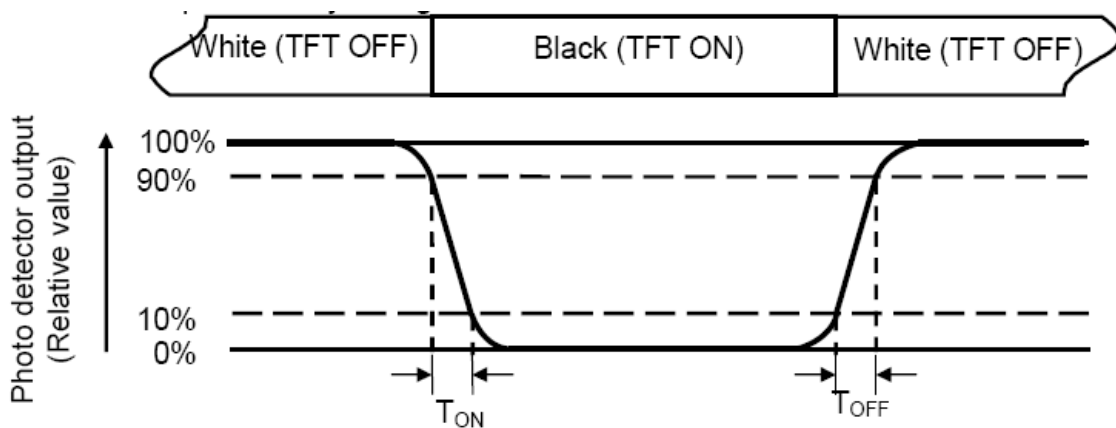
Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity(U) = Lmin/ Lmax

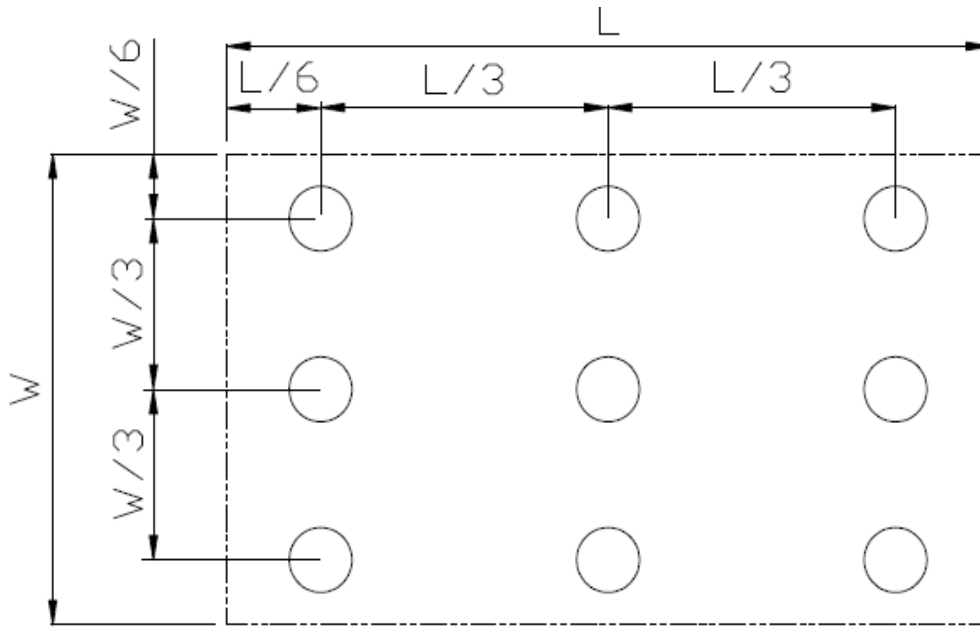L-------Active area length W----- Active area width

Fig. 2 Definition of uniformity

Lmax: The measured maximum luminance of all measurement position.

Lmin: The measured minimum luminance of all measurement position.

Note 7: Definition of Luminance :

Measure the luminance of white state at center point.

7 Environmental / Reliability Test

No	Test Item	Condition	Remarks
1	High Temperature Operation	Ts=+60℃, 240hrs	Note1 IEC60068-2-2,GB2423.2—89
2	Low Temperature Operation	Ta=-20℃, 240hrs	IEC60068-2-1 GB2423.1—89
3	High Temperature Storage	Ta=+70℃, 240hrs	IEC60068-2-2, GB2423.2—89
4	Low Temperature Storage	Ta=-30℃, 240hrs	IEC60068-2-1 GB2423.1—89
5	High Temperature & High Humidity Storage	Ta=+60℃, 90% RH 240 hours	Note2 IEC60068-2-3, GB/T2423.3—2006
6	Thermal Shock (Non-operation)	-30℃ 30 min~+70℃ 30 min, Change time:5min, 20 Cycles	Start with cold temperature, End with high temperature, IEC60068-2-14,GB2423.22—87
7	Electro Static Discharge (Operation)	C=150pF, R=330Ω，5points/panel Air:±8KV, 5times; Contact:±4KV, 5 times; （Environment: 15℃～35℃, 30%～60%, 86Kpa～106Kpa）	IEC61000-4-2 GB/T17626.2—1998
8	Vibration (Non-operation)	Frequency range:10~55Hz, Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X.Y.Z. (6 hours for total)(Package condition)	IEC60068-2-6 GB/T2423.10—1995
9	Package Drop Test	Height:80 cm, 1 corner, 3 edges, 6 surfaces	IEC60068-2-32 GB/T2423.8—1995

Note1: Ts is the temperature of panel's surface.

Note2: Ta is the ambient temperature of sample.

8 Register Setting

write address<=16'h0001;   write DATA<=16'h7946;

write address<=16'h0002;   write DATA<=16'h2037;

write address<=16'h0003;   write DATA<=16'hDB30;

write address<=16'h0004;   write DATA<=16'h00aF;

write address<=16'h0005;   write DATA<=16'h1FCC;

write address<=16'h0006;   write DATA<=16'h372e;

write address<=16'h0007;   write DATA<=16'h000c;

write address<=16'h0008;   write DATA<=16'h002b;

write address<=16'h0009;   write DATA<=16'h4008;

write address<=16'h000f;   write DATA<=16'h0140;

write address<=16'h0010;   write DATA<=16'h0301;

write address<=16'h0011;   write DATA<=16'h0201;

write address<=16'h0012;   write DATA<=16'h0005;

write address<=16'h0013;   write DATA<=16'h0101;

write address<=16'h0014;   write DATA<=16'h0207;

write address<=16'h0015;   write DATA<=16'h0300;

write address<=16'h0016;   write DATA<=16'h0201;

write address<=16'h0017;   write DATA<=16'h0306;

write address<=16'h0018;   write DATA<=16'h0c02;

write address<=16'h0019;   write DATA<=16'h0905;

9　Mechanical Drawing
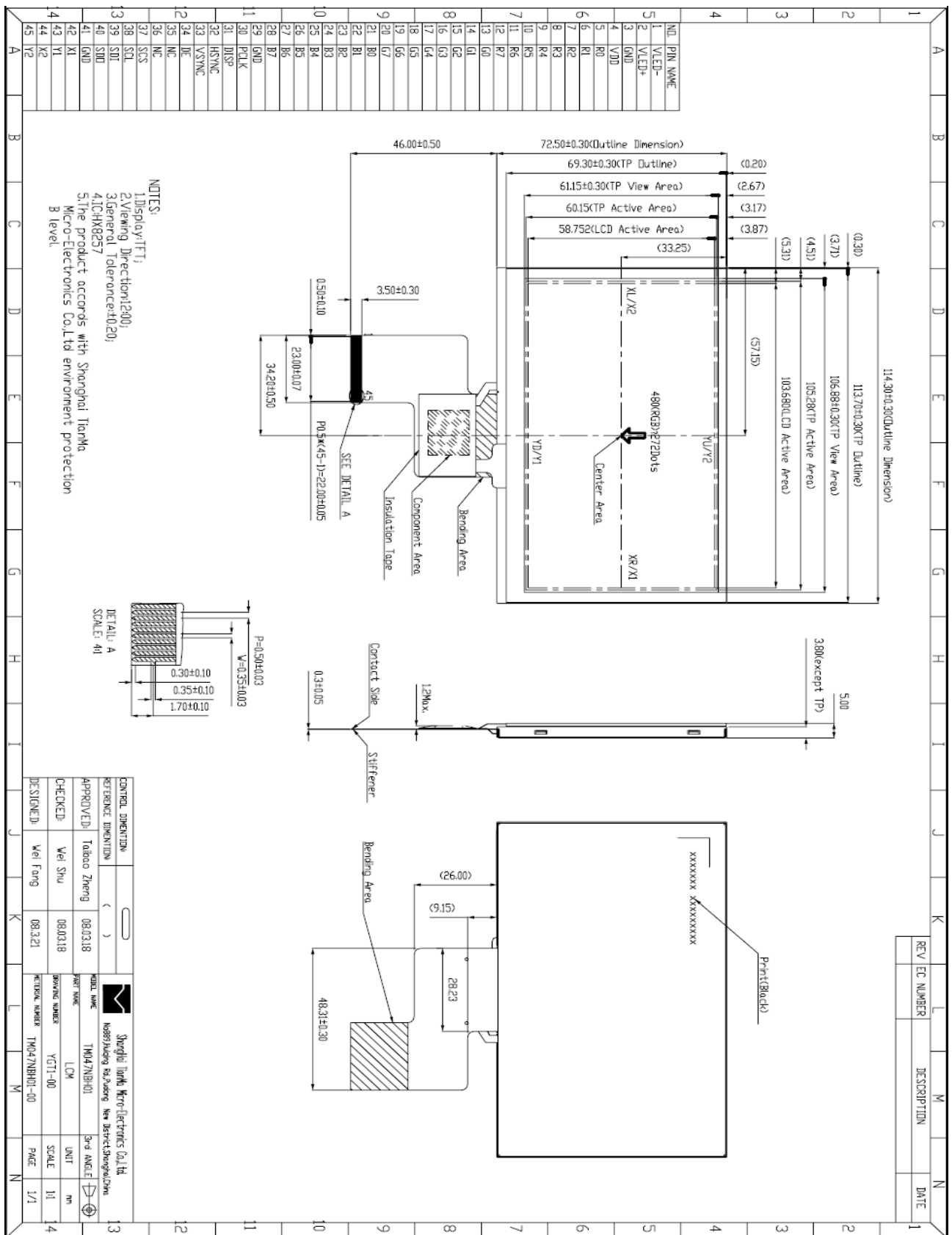
10 Packing Drawing

No	Item	Model (Material)	Dimensions (mm)	Unit Weight (Kg)	Quantity	Remark
1	LCM module	TM047NBH01	114.3x72.5x5.0	0.0765	112	
2	Partition_1	Corrugated Paper	513x333x106	0.70000	2	
3.	Anti-Static Bag	PE	175.8x125x0.05	0.0007	112	Anti-static
4	Dust-Proof Bag	PE	-----------------	0.0600	1	
5	Partition_2	Corrugated Paper	505x332x4.00	0.0900	3	
6	Corrugated Bar	Corrugated paper	513x160x3	0.0400	8	
8	Carton	Corrugated Paper	530x350x250	1.1000	1	
9	Total weight(kg)	Approximately:11.5				
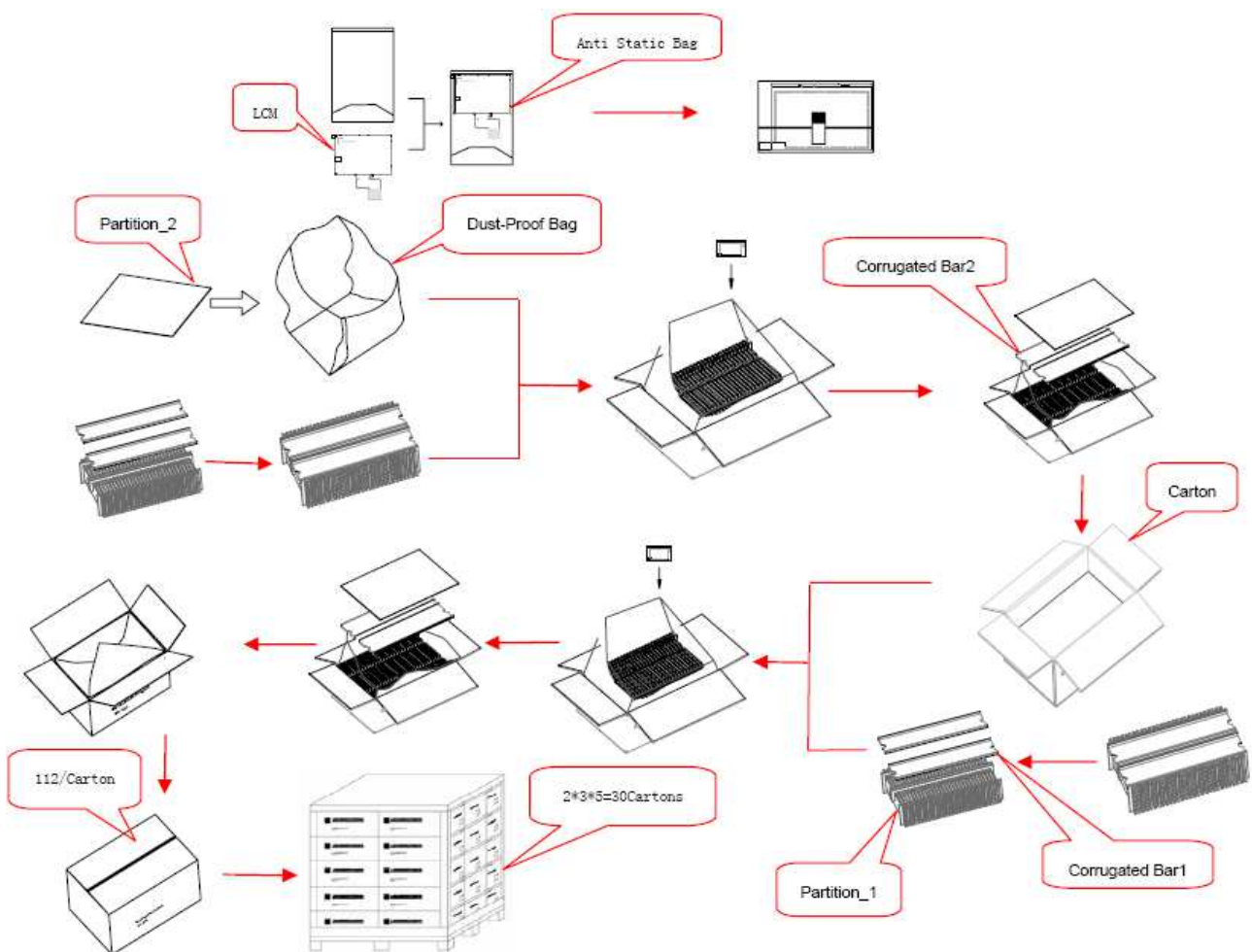
11 Precautions for Use of LCD Modules

11.1   Handling Precautions

11.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

11.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

11.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

11.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

11.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

－ Isopropyl alcohol

－ Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

－ Water

－ Ketone

－ Aromatic solvents

11.1.6 Do not attempt to disassemble the LCD Module.

11.1.7 If the logic circuit power is off, do not apply the input signals.

11.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

11.1.8.1 Be sure to ground the body when handling the LCD Modules.

11.1.8.2 Tools required for assembly, such as soldering irons, must be properly ground.

11.1.8.3 To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

11.1.8.4 The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

11.2 Storage precautions

11.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

11.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0℃ ～ 40℃   Relatively humidity: ≤80%

11.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

11.3 Transportation Precautions:

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.