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FDA8440

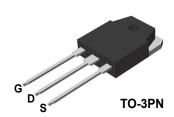
N-Channel Logic Level PowerTrench $^{(\!R\!)}$ MOSFET 40 V, 100 A, 2.1 m Ω

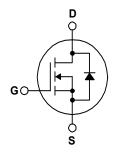
Features

- $R_{DS(on)}$ = 1.46 m Ω (Typ.) @ V_{GS} = 10 V, I_D = 80 A
- Q_{G(tot)} = 345 nC (Typ.) @ V_{GS} = 10 V
- · Low Miller Charge
- Low Q_{rr} Body Diode
- UIS Capability (Single Pulse and Repetitive Pulse)
- 160 A Guarantee for 2 sec
- RoHS Compliant

Application

- · Power tools
- · Motor drives and Uninterruptible Power Supplies
- · Synchronous Rectification
- · Battery Protection Circuit





MOSFET Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol	Parameter	FDA8440	Unit
V _{DSS}	Drain to Source Voltage	40	V
V _{GSS}	Gate to Source Voltage	±20	V
	Drain Current - Continuous (T _C = 155°C)	100	А
I _D	- Continuous ($T_A = 25^{\circ}C$, $V_{GS} = 10 \text{ V}$, $R_{\theta JA} = 40^{\circ}C/W$)	30	Α
	- Pulsed	500	Α
E _{AS}	Single Pulsed Avalanche Energy (Note 1)	1682	mJ
P _D	Power dissipation	306	W
гD	Derate above 25°C	2.04	W/°C
T _{J.} T _{STG}	Operating and Storage Temperature	-55 to +175	°C

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	0.49	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max. (Note 2)	40	°C/W

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FDA8440	FDA8440	TO-3PN	Tube	N/A	N/A	30 units

Electrical Characteristics $T_C = 25$ °C unless otherwise noted

Symbol	Parameter	Conditio	Min.	Тур.	Max.	Unit	
Off Charac	Off Characteristics						
BV _{DSS}	Drain to Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA		40			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 32 V				1	μΑ
		V _{GS} = 0 V	$T_{\rm C} = 150^{\rm o}{\rm C}$			250	μΑ
I _{GSS}	Gate to Body Leakage Current	V _{GS} = ±20 V				±100	nA
On Charac	teristics	1					
V _{GS(th)}	Gate to Source Threshold Voltage	V _{DS} = V _{GS} , I _D = 250 μA	ı	1		3	V
		V _{GS} = 4.5 V, I _D = 80 A				2.2	mΩ
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 80 A				2.1	
		$V_{GS} = 10 \text{ V}, I_D = 80 \text{ A},$ $T_C = 175^{\circ}\text{C}$		2.82	4.1	11122	
Dynamic C	haracteristics	- 1					
C _{iss}	Input Capacitance		V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz		18600	24740	pF
C _{oss}	Output Capacitance				1840	2450	pF
C _{rss}	Reverse Transfer Capacitance	I - I WITZ			1400	2100	pF
R_{G}	Gate Resistance	V _{GS} = 0.5 V, f = 1 MHz			1.1		Ω
Q _{g(tot)}	Total Gate Charge at 10V	V _{GS} = 0 V to 10 V			345	450	nC
Q _{g(2)}	Threshold Gate Charge	V _{GS} = 0 V to 2 V	V _{DD} = 20 V		32.5		nC
Q _{gs}	Gate to Source Gate Charge		I _D = 80 A		49		nC
Q _{gs2}	Gate Charge Threshold to Plateau		I _g = 1.0 mA		16.5		nC
Q _{gd}	Gate to Drain "Miller" Charge				74		nC
Switching	Characteristics						
t _{ON}	Turn-On Time				175	360	ns
t _{d(on)}	Turn-On Delay Time	V _{DD} = 20 V,I _D = 80 A			43	95	ns
t _r	Rise Time		$V_{DD} = 20 \text{ V,I}_{D} = 80 \text{ A}$ $V_{GS} = 10 \text{ V, R}_{GEN} = 7 \Omega$		130	275	ns
t _{d(off)}	Turn-Off Delay Time				435	875	ns
t _f	Fall Time				290	590	ns
t _{OFF}	Turn-Off Time				730	1470	ns
Drain-Source Diode Characteristics and Maximum Ratings							
V _{SD}	Source to Drain Diode Voltage	I _{SD} = 80 A			1.25	V	
▼SD		I _{SD} = 40 A				1.0	V
t _{rr}	Reverse Recovery Time	I _{SD} = 75 A, dI _{SD} /dt = 100 A/μs			59		ns
Q _{RR}	Reverse Recovery Charge	$I_{SD} = 75 \text{ A}, dI_{SD}/dt = 100 \text{ A}/\mu\text{s}$			77		nC

NOTES:

^{1:} Starting T_J = 25°C, L = 1 mH, I_{AS} = 58 A, V_{DD} = 36 V, V_{GS} = 10 V.

^{2:} Pulse width = 100 s.

Typical Performance Characteristics

Figure 1. On-Region Characteristics

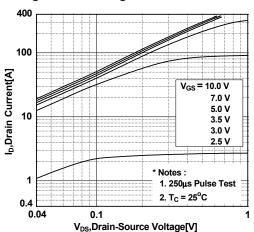


Figure 3. On-Resistance Variation vs. **Drain Current and Gate Voltage**

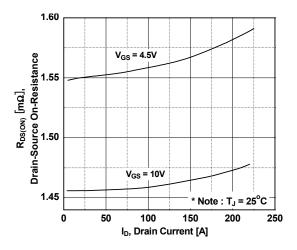


Figure 5. Capacitance Characteristics

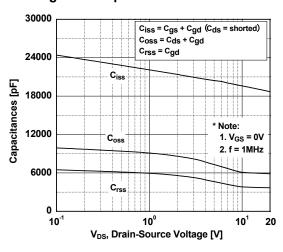


Figure 2. Transfer Characteristics

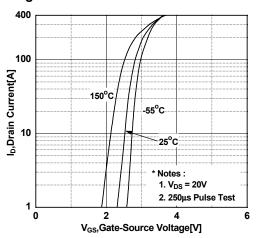


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperatue

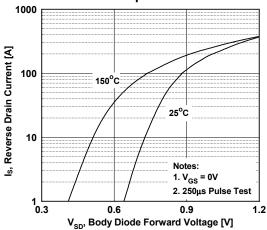
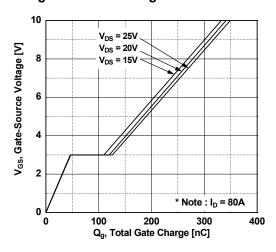


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

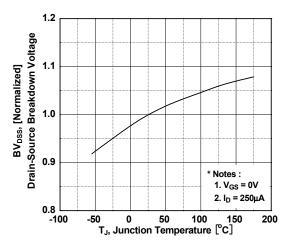


Figure 9. Unclamped Inductive Switching Capability

Figure 8. On-Resistance Variation vs. Temperature

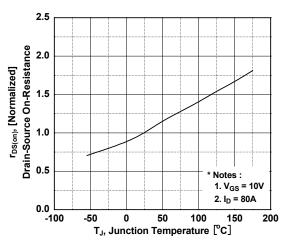
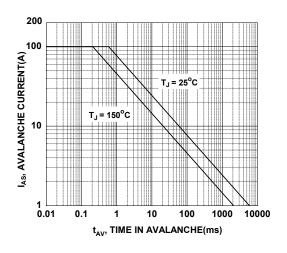


Figure 10. Safe Operating Area



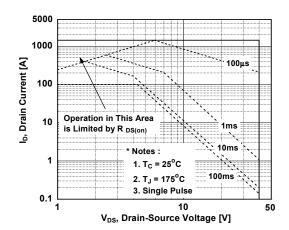
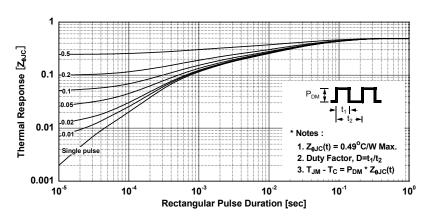


Figure 11. Transient Thermal Response Curve



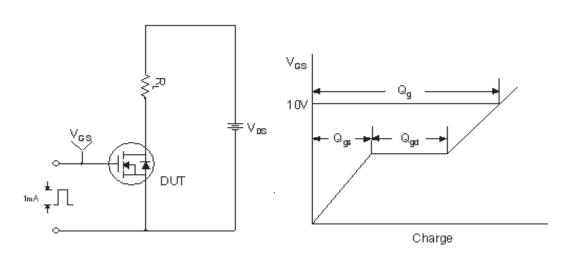


Figure 12. Gate Charge Test Circuit & Waveform

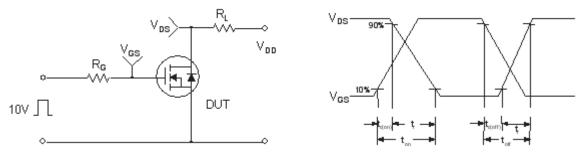


Figure 13. Resistive Switching Test Circuit & Waveforms

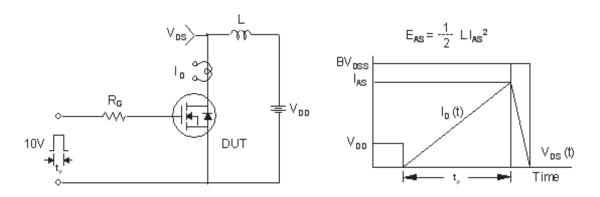
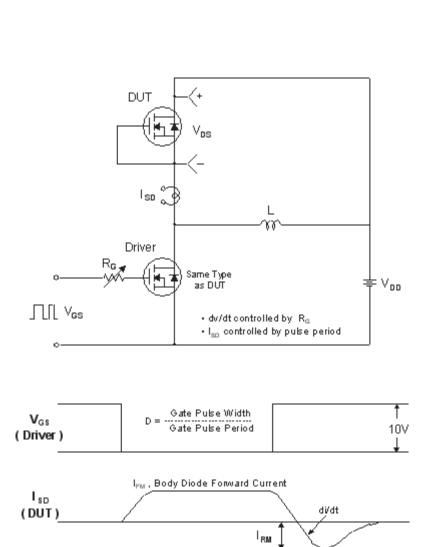


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms



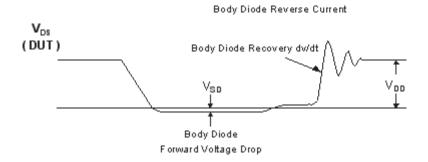
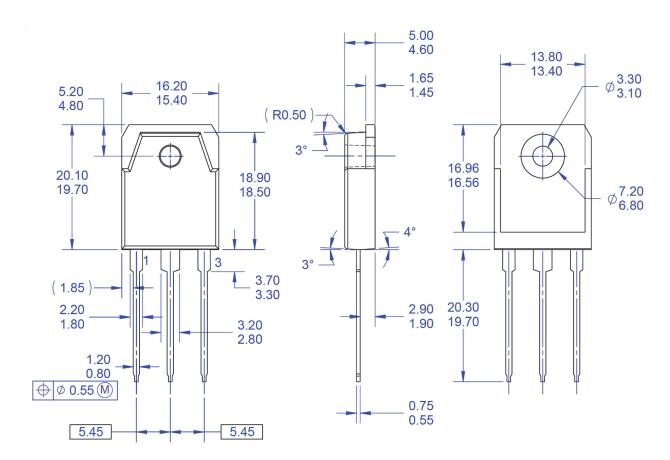
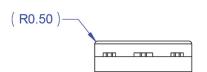


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

Mechanical Dimensions





- NOTES: UNLESS OTHERWISE SPECIFIED
- A) THIS PACKAGE CONFORMS TO EIAJ SC-65 PACKAGING STANDARD.
- ALL DIMENSIONS ARE IN MILLIMETERS.
- **DIMENSION AND TOLERANCING PER** ASME14.5-2009.
- D) DIMENSIONS ARE EXCLUSSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSSIONS.
 E) DRAWING FILE NAME: TO3PN03AREV1.
- F) FAIRCHILD SEMICONDUCTOR.

Figure 16. TO3PN, 3-Lead, Plastic, EIAJ SC-65

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