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June 2014

FDB024N04AL7

N-Channel PowerTrench[®] MOSFET 40 V, 219 A, 2.4 m Ω

Features

- $R_{DS(on)}$ = 2.0 m Ω (Typ.)@ V_{GS} = 10 V, I_D = 80 A
- · Fast Switching Speed
- · Low Gate Charge
- High Performance Trench Technology for Extremely Low $R_{DS(on)}$
- · High Power and Current Handling Capability
- · RoHS Compliant

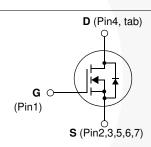
Description

This N-Channel MOSFET is produced using Fairchild Semiconductor's advance PowerTrench [®] process that has been tailored to minimize the on-state resistance while maintaining superior switching performance.

Applications

- Synchronous Rectification for ATX / Server / Telecom PSU
- · Battery Protection Circuit
- · Motor drives and Uninterruptible Power Supplies





MOSFET Maximum Ratings $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol		Parameter	FDB024N04AL7	Unit
V _{DSS}	Drain to Source Voltage		40	V
V _{GSS}	Gate to Source Voltage		±20	V
- N		- Continuous (T _C = 25°C, Silicon Limited)	219*	
I _D	Drain Current	- Continuous (T _C = 100°C, Silicon Limited)	155*	Α
		- Continuous (T _C = 25°C, Package Limited)	100	
I _{DM}	Drain Current	- Pulsed (Note 1)	876	Α
E _{AS}	Single Pulsed Avalanche E	Energy (Note 2)	864	mJ
dv/dt	Peak Diode Recovery dv/c	t (Note 3)	6.0	V/ns
n	Dawer Dissination	$(T_C = 25^{\circ}C)$	214	W
P _D Power Dissipation		- Derate Above 25°C	1.43	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +175	°C
Γ _L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds		300	°C

^{*}Calculated continuous current based on maximum allowable junction temperature. Package limitation current is 100 A.

1. Gate 2. Source 3. Source 4. Drain 5. Source 6. Source

7. Source

Thermal Characteristics

Symbol	Parameter	FDB024N04AL7	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	0.7	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	°C/VV

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FDB024N04AL7	FDB024N04A	D2PAK-7L	Tape and Reel	330 mm	24 mm	800 units

Electrical Characteristics $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Charac	cteristics					
BV _{DSS}	Drain to Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V, T_C = 25^{\circ}C$	40		-	V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	$I_D = 250 \mu A$, Referenced to $25^{\circ}C$	-	30	-	mV/°C
1	Zero Gate Voltage Drain Current	$V_{DS} = 32 \text{ V}, V_{GS} = 0 \text{ V}$	-	-	10	
I _{DSS}	Zero Gate Voltage Drain Gurrent	$V_{DS} = 32 \text{ V}, T_{C} = 150^{\circ}\text{C}$	-	-	500	μΑ
I _{GSS}	Gate to Body Leakage Current	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	-	-	±100	nA

On Characteristics

V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 250 \mu A$	1.0	-	3.0	V
R _{DS(on)}	Static Drain to Source On Resistance	$V_{GS} = 10 \text{ V}, I_D = 80 \text{ A}$	-	2.0	2.4	mΩ
9 _{FS}	Forward Transconductance	$V_{DS} = 10 \text{ V}, I_{D} = 80 \text{ A}$	ı	368	ı	S

Dynamic Characteristics

C _{iss}	Input Capacitance	V 05 V V 0 V	-	5490	7300	pF
C _{oss}	Output Capacitance	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$ - f = 1 MHz	-	1220	1620	pF
C _{rss}	Reverse Transfer Capacitance	1 - 1 1011 12	- \	155	233	pF
Q _{g(tot)}	Total Gate Charge at 10V		- \	84	109	nC
Q_{gs}	Gate to Source Gate Charge	$V_{DS} = 32 \text{ V}, I_{D} = 80 \text{ A},$	-	19	-	nC
Q _{gs2}	Gate Charge Threshold to Plateau	V _{GS} = 10 V	-	9.5	-	nC
Q_{gd}	Gate to Drain "Miller" Charge	(Note 4)	-	12	-	nC

Switching Characteristics

t _{d(on)}	Turn-On Delay Time		-	17	44	ns
t _r	Turn-On Rise Time	$V_{DD} = 20 \text{ V}, I_D = 80 \text{ A},$	-	8	26	ns
t _{d(off)}	Turn-Off Delay Time	$R_{G} = 4.7 \Omega, V_{GS} = 10 V$	-	71	152	ns
t _f	Turn-Off Fall Time	(Note 4)	- /	17	44	ns
ESR	Equivalent Series Resistance (G-S)	f = 1 MHz	- /	1.1	-	Ω

Drain-Source Diode Characteristics

I _S	Maximum Continuous Drain to Source Diode Forward Current			-	219	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	876	Α
V_{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0 V, I _{SD} = 80 A	-	-	1.3	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _{SD} = 80 A,	-	54	_	ns
Q _{rr}	Reverse Recovery Charge	$dI_F/dt = 100 A/\mu s$	-	49	-	nC

Notes:1. Repetitive rating: pulse-width limited by maximum junction temperature.

^{2.} L = 3 mH, I_{AS} = 24 A, V_{DD} = 40 V, R_G = 25 Ω , starting T_J = 25°C.

^{3.} $I_{SD} \le 80$ A, di/dt ≤ 200 A/ μ s, $V_{DD} \le BV_{DSS}$, starting $T_J = 25^{\circ}C$.

^{4.} Essentially independent of operating temperature typical characteristics.

Typical Performance Characteristics

Figure 1. On-Region Characteristics

100

100

V_{GS} = 10.0V
8.0V
7.0V
6.0V
4.0V
3.5V
2. T_C = 25°C
3.0V

0.1

0.01

Figure 3. On-Resistance Variation vs.

Drain Current and Gate Voltage

V_{DS}, Drain-Source Voltage[V]

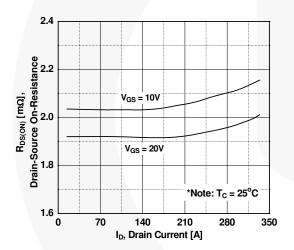


Figure 5. Capacitance Characteristics

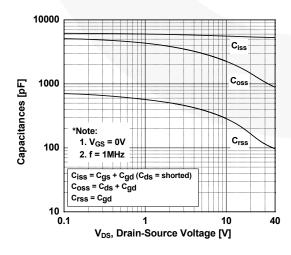


Figure 2. Transfer Characteristics

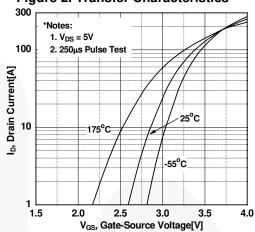


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

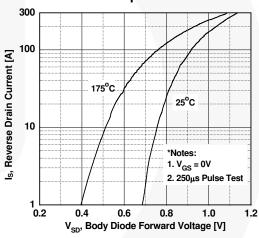
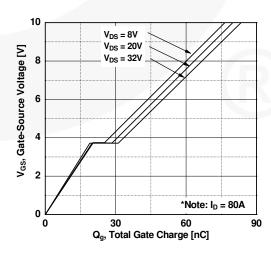
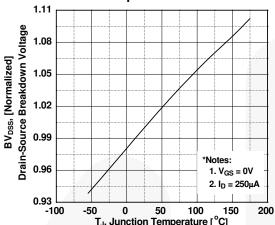


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature



T_J, Junction Temperature [°C]

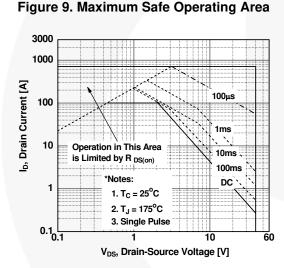


Figure 11. Unclamped Inductive Switching Capability

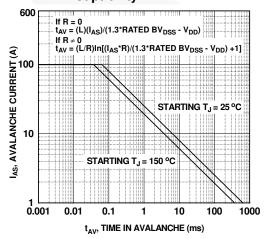


Figure 8. On-Resistance Variation vs. **Temperature**

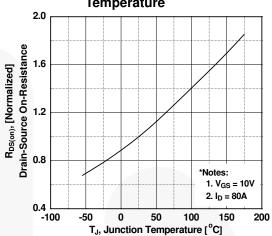
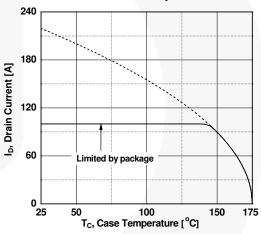


Figure 10. Maximum Drain Current vs. **Case Temperature**



Typical Performance Characteristics (Continued)

Figure 12. Transient Thermal Response Curve

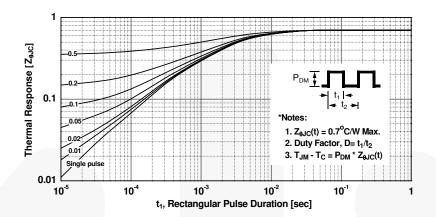


Figure 13. Gate Charge Test Circuit & Waveform

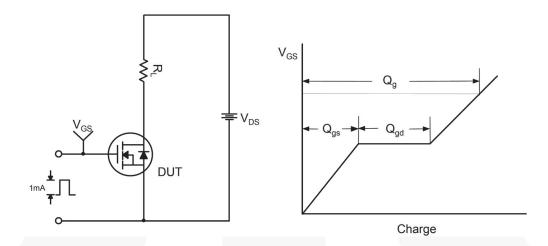


Figure 14. Resistive Switching Test Circuit & Waveforms

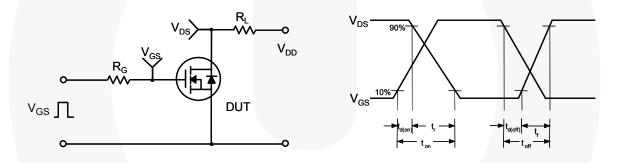
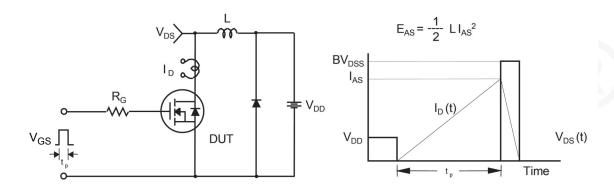
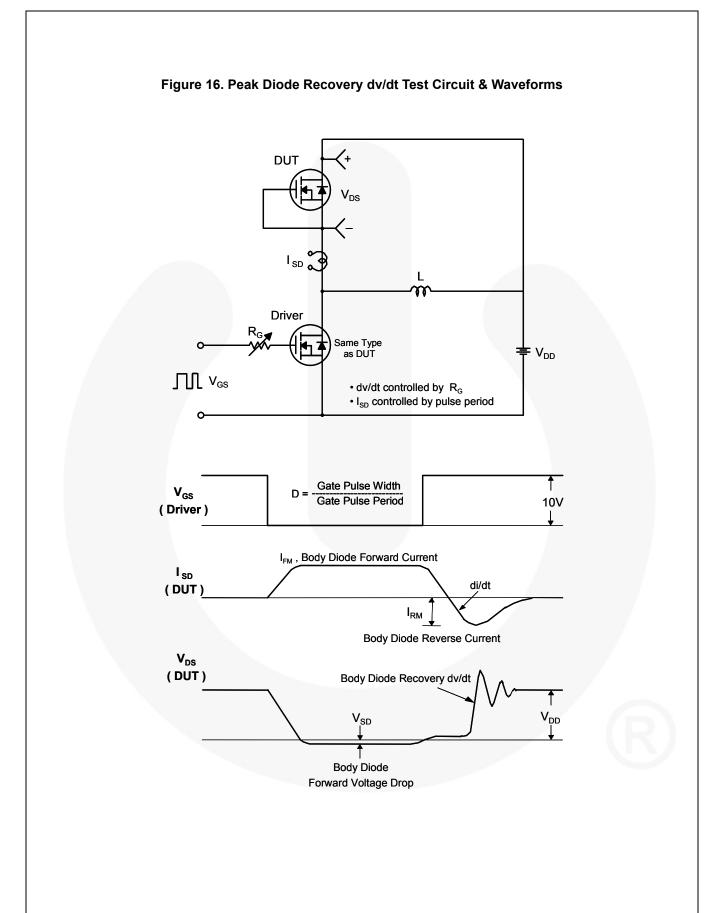


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms





Mechanical Dimensions

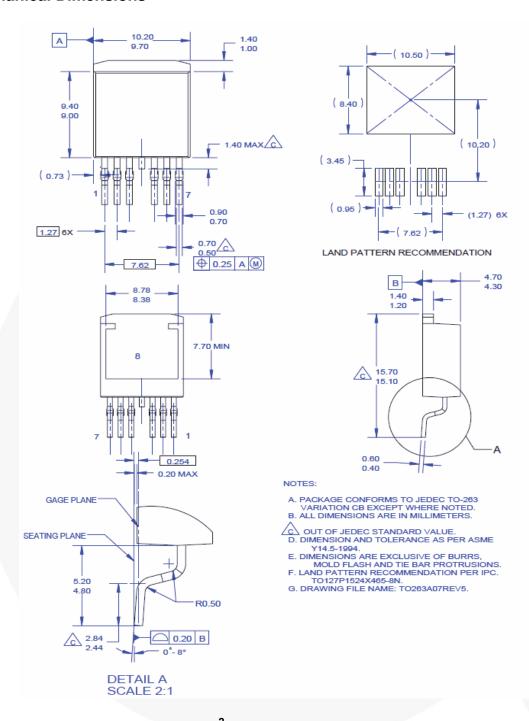


Figure 17. TO263 (D²PAK), Molded, 7-Lead, Surface Mount

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