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March 2015

FDD4243

40V P-Channel PowerTrench® MOSFET

-40V, -14A, 44m Ω

Features

- Max $r_{DS(on)}$ = 44m Ω at V_{GS} = -10V, I_D = -6.7A
- Max $r_{DS(on)}$ = 64m Ω at V_{GS} = -4.5V, I_D = -5.5A
- High performance trench technology for extremely low r_{DS(on)}
- RoHS Compliant

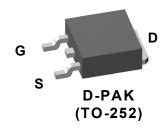


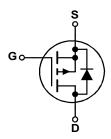
General Description

This P-Channel MOSFET has been produced using Fairchild Semiconductor's proprietary PowerTrench® technology to deliver low $r_{DS(on)}$ and optimized Bvdss capability to offer superior performance benefit in the applications.

Application

- Inverter
- Power Supplies





MOSFET Maximum Ratings T_C = 25°C unless otherwise noted

Symbol	Parameter			Ratings	Units	
V_{DS}	Drain to Source Voltage			-40	V	
V_{GS}	Gate to Source Voltage			±20	V	
	Drain Current -Continuous (Package limited)	T _C = 25°C		-14		
	-Continuous (Silicon limited)	T _C = 25°C	(Note 1)	-24	_	
ID	-Continuous	T _A = 25°C	(Note 1a)	-6.7	A	
	-Pulsed			-60	1	
E _{AS}	Single Pulse Avalanche Energy		(Note 3)	84	mJ	
P _D	Power Dissipation	T _C = 25°C		42	W	
	Power Dissipation		(Note 1a)	3	VV	
T _J , T _{STG}	Operating and Storage Junction Temperature Range			-55 to +150	°C	

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance, Junction to Case		3.0	°C/W
R _{e.IA}	Thermal Resistance, Junction to Ambient	(Note 1a)	40	C/VV

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDD4243	FDD4243	D-PAK(TO-252)	13"	16mm	2500 units

Electrical Characteristics $T_J = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Chara	acteristics					
BV _{DSS}	Drain to Source Breakdown Voltage	$I_D = -250 \mu A, V_{GS} = 0 V$	-40			V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	I_D = -250 μ A, referenced to 25°C		-32		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = -32V,$ $V_{GS} = 0V$ $T_{J} = 125^{\circ}C$			-1 -100	μА
I _{GSS}	Gate to Source Leakage Current	$V_{GS} = \pm 20V, V_{DS} = 0V$			±100	nA

On Characteristics

V _{GS(th)}	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_D = -250 \mu A$	-1.4	-1.6	-3.0	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate to Source Threshold Voltage Temperature Coefficient	I_D = -250 μ A, referenced to 25°C		4.7		mV/°C
		$V_{GS} = -10V, I_D = -6.7A$		36	44	
r _{DS(on)}	Drain to Source On Resistance	$V_{GS} = -4.5V, I_D = -5.5A$		48	64	mΩ
, ,		$V_{GS} = -10V, I_D = -6.7A, T_J = 125^{\circ}C$		53	69	
9 _{FS}	Forward Transconductance	$V_{DS} = -5V, I_{D} = -6.7A$		16		S

Dynamic Characteristics

C _{iss}	Input Capacitance	V = 20V V = 0V	1165	1550	pF
C _{oss}	Output Capacitance	$V_{DS} = -20V, V_{GS} = 0V,$ f = 1MHz	165	220	pF
C _{rss}	Reverse Transfer Capacitance	f = 1MHz	90	135	pF
R_g	Gate Resistance	f = 1MHz	4		Ω

Switching Characteristics

t _{d(on)}	Turn-On Delay Time		6	12	ns
t _r	Rise Time	V_{DD} = -20V, I_{D} = -6.7A V_{GS} = -10V, R_{GEN} = 6 Ω	15	26	ns
t _{d(off)}	Turn-Off Delay Time	$V_{GS} = -10V$, $R_{GEN} = 6\Omega$	22	35	ns
t _f	Fall Time		7	14	ns
$Q_{g(TOT)}$	Total Gate Charge at 10V	V _{DD} = -20V, I _D = -6.7A	21	29	nC
Q_{gs}	Gate to Source Gate Charge	V _{GS} = -10V	3.4		nC
Q_{gd}	Gate to Drain "Miller" Charge		4		nC

Drain-Source Diode Characteristics

V_{SD}	Source to Drain Diode Forward Voltage	$V_{GS} = 0V, I_S = -6.7A$ (Note 2)	0.86	1.2	V
t _{rr}	Reverse Recovery Time	I _F = -6.7A, di/dt = 100A/μs	29	43	ns
Q _{rr}	Reverse Recovery Charge		30	44	nC

^{1:} R_{0,JA} is sum of junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R_{0,JC} is guaranteed by design while R_{0,JC} is determined by the user's board design.

a. 40°C/W when mounted on a 1 in² pad of 2 oz copper

b. 96°C/W when mounted on a minimum pad.

^{2:} Pulse Test: Pulse Width < 300 μ s, Duty cycle < 2.0%. 3: Starting T $_J$ = 25°C, L = 3mH, I $_{AS}$ = 7.5A, V $_{DD}$ = 40V, V $_{GS}$ = 10V.

Typical Characteristics T_J = 25°C unless otherwise noted

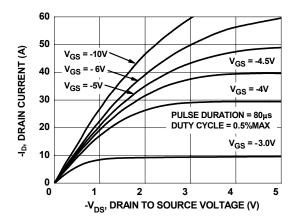


Figure 1. On Region Characteristics

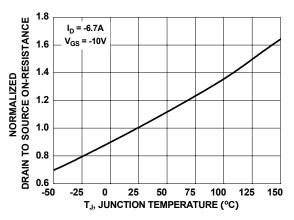


Figure 3. Normalized On Resistance vs Junction Temperature

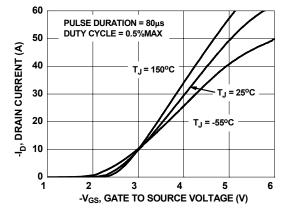


Figure 5. Transfer Characteristics

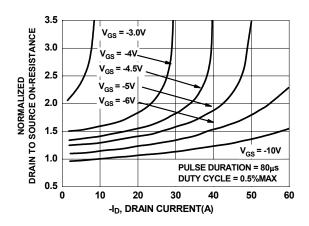


Figure 2. Normalized On-Resistance vs Drain Current and Gate Voltage

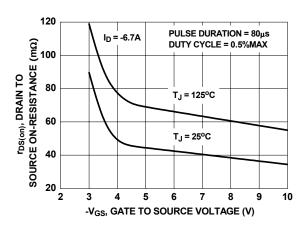


Figure 4. On-Resistance vs Gate to Source Voltage

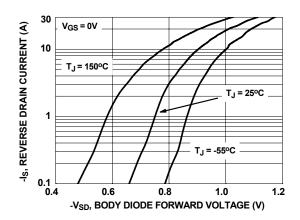


Figure 6. Source to Drain Diode Forward Voltage vs Source Current

Typical Characteristics T_J = 25°C unless otherwise noted

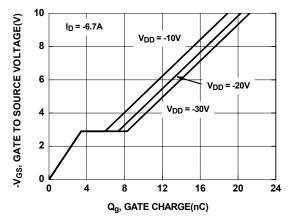


Figure 7. Gate Charge Characteristics

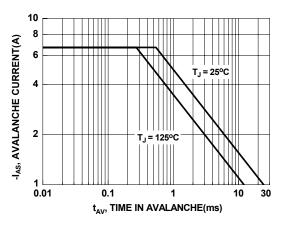


Figure 9. Unclamped Inductive Switching Capability

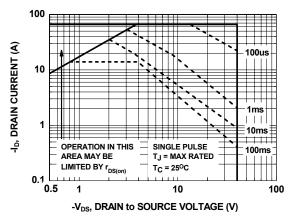


Figure 11. Forward Bias Safe Operating Area

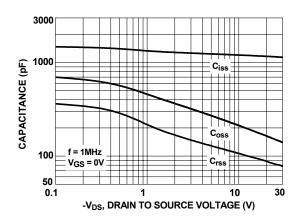


Figure 8. Capacitance vs Drain to Source Voltage

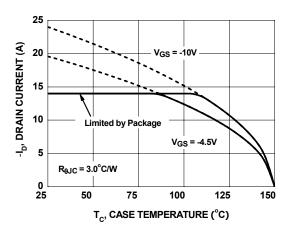


Figure 10. Maximum Continuous Drain Current vs Case Temperature

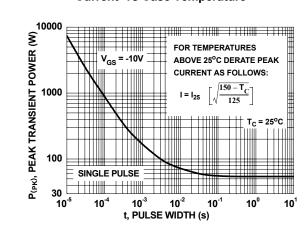


Figure 12. Single Pulse Maximum Power Dissipation

Typical Characteristics T_J = 25°C unless otherwise noted

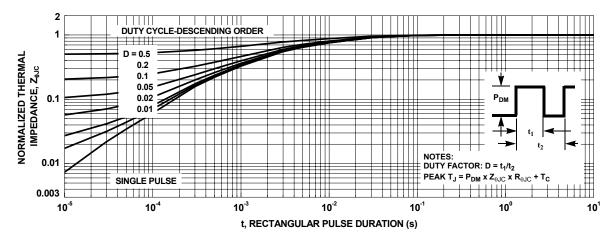
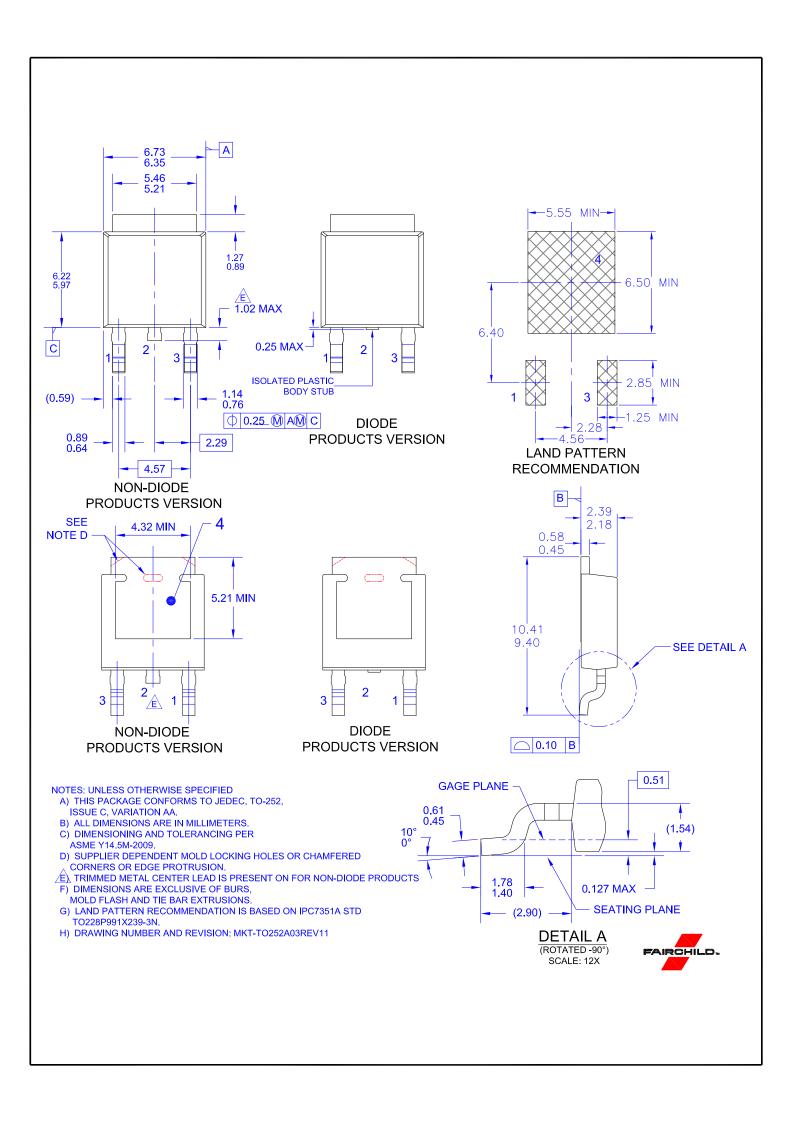


Figure 13. Transient Thermal Response Curve



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