

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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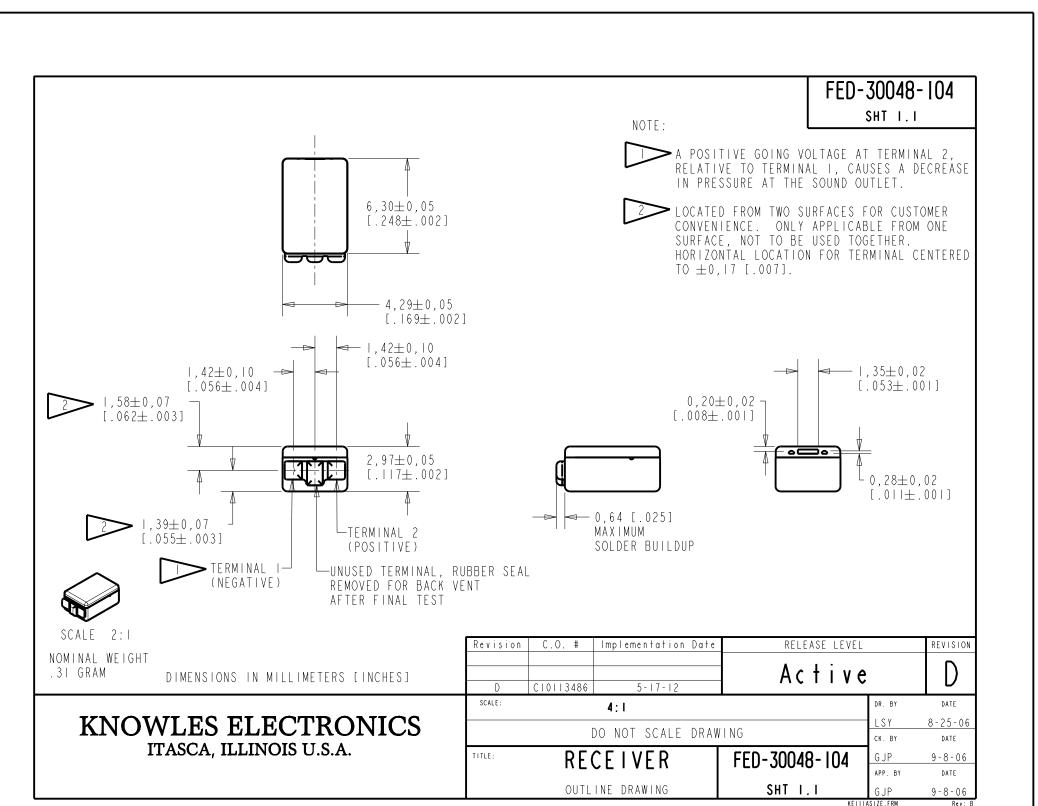
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PORT LOCATION: 12N

FED-30048-104 SHEET 2.1

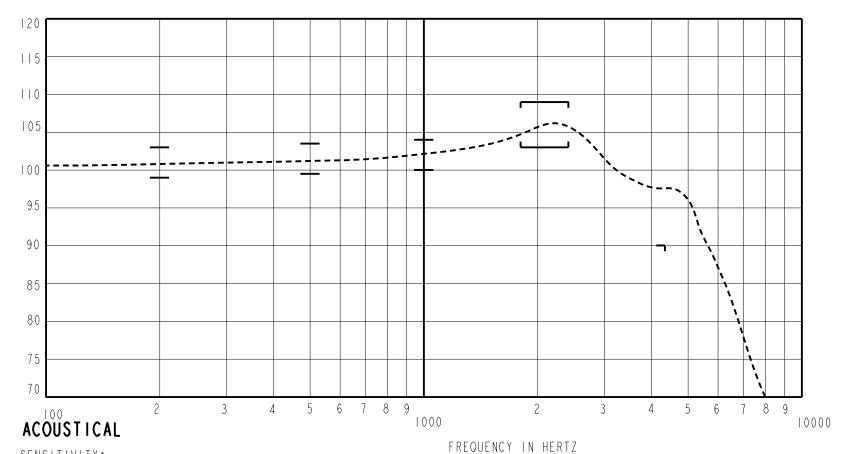
DESCRIPTION

FOR

SENSITIVITY IN dB RELATIVE TO CONDITIONS SHOWN BELOW

THE FED-30048-104 IS A FERROFLUID DAMPED, MAGNETIC BALANCED ARMATURE RECEIVER, INTENDED FOR USE IN HEADSETS. THIS MODEL IS SHOCK PROTECTED, HAS LOW IMPEDANCE, INCREASED DCR/IMPEDANCE RATIO, AND A REAR VENT.

CONSTANT VOLTAGE DRIVE CONDITIONS (NOTE: REAR VENT, CENTER TERMINAL PAD, BLOCKED FOR TEST)



SENSITIVITY*

DEVICE WILL PRODUCE THE SPL LISTED BELOW UNDER THE TEST CONDITIONS DESCRIBED IN TABLE 3. NOMINAL SENSITIVITY AT I kHz IS dB RELATIVE TO 20uPa. ALL OTHER VALUES IN dB RELATIVE TO THE SENSITIVITY AT 1 kHz.

FREQUENCY (Hz)	MINIMUM	NOMINAL	MAXIMUM
200	- 3 . 0	- . 0	+ . 0
500	-2.5	-0.5	+1.5
1000	-2.0	102.0	+2.0
1850-2650	+ . 0	+4.0	+7.0
4400	-12 0		

TABLE I.

TOTAL HARMONIC DISTORTION

DEVICE WILL NOT EXCEED TOTAL HARMONIC DISTORTION LEVELS LISTED BELOW.

FREQUENCY (Hz)	DRIVE (V RMS)	DC BIAS (MA)	LIMIT (%)	
500	0.363	0	10	
780	0.13	0	5	
1175	0.13	0	5	

TABLE 2.

TEST CONDITIONS

NOMINAL SOURCE VOLTAGE	0.13 Vrms, 0 mA DC BIAS
SOURCE IMPEDANCE	< Ι Ω
TUBING	10 mm (.394) LONG, mm (.039) D.
COUPLER CAVITY	2 CM ³ SIMULATED ANSI S3.7 TYPE HA-3. (IEC 60318-5)

TABLE 3.

ELECTRICAL

DC RESISTANCE	23 <u>\(\Omega\)</u> ±10%
IMPEDANCE @ 500 Hz	26Ω ±15%
IMPEDANCE @ I kHz	29\(\Omega\) ±15%

TABLE 4.

ISOLATION: CASE WILL BE ELECTRICALLY ISOLATED FROM THE COIL CIRCUIT.

TEMPERATURE: OPERATING RANGE FROM 0°C TO 63°C (SENSITIVITY WILL NOT VARY BY MORE THAN ±3 dB WITHIN RANGE) SENSITIVITY AT 0°C IS 2dB LOWER THAN THE SENSITIVITY AT ROOM TEMPERATURE.

DELTA PEAK IS IdB HIGHER AT BODY TEMPERATURE (37°C) STORAGE RANGE FROM -40°C TO 63°C.

	Revision C.O. # Implementation Date RELEASE LEVEL			REVISION		
		010112400	5 17 10	Active		D
	U	C10113486	5-17-12	<u> </u>		
KNOWLES ELECTRONICS	WHEN TEST LIMITS ARE USED TO ESTABLISH INCOMING INSPECTION ACCEPTANCE/REJECTION CRITERIA, CORRELATION OF TEST EQUIPMENT WITH KNOWLES IS ALSO REQUIRED FOR ELIMINATION OF EQUIPMENT AND TEST METHOD VARIATION			DR. BY	DATE	
				LSY	8 - 25 - 06	
				CK. BY	DATE	
ITASCA, ILLINOIS U.S.A.	TITLE:	RF	CEIVER	FED-30048-104	GJP	9 - 8 - 06
TIASCA, IDDINOIS C.S.A.		11 L	CLIVLIN	1 60 30040 104	APP. BY	DATE
		PERFORMAN	ICE SPECIFICATION	SHT 2.1	GJP	9 - 8 - 06