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## FFSD08120A

# Silicon Carbide Schottky Diode 1200 V, 8 A

#### **Features**

- Max Junction Temperature 175 °C
- · Avalanche Rated 80 mJ
- · High Surge Current Capacity
- · Positive Temperature Coefficient
- · Ease of Paralleling
- · No Reverse Recovery / No Forward Recovery

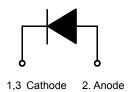
## **Applications**

- · General Purpose
- · SMPS, Solar Inverter, UPS
- · Power Switching Circuits

### **Description**

Silicon Carbide (SiC) Schottky Diodes use a completely new technology that provides superior switching performance and higher reliability compared to Silicon. No reverse recovery current, temperature independent switching characteristics, and excellent thermal performance sets Silicon Carbide as the next generation of power semiconductor. System benefits include highest efficiency, faster operating frequency, increased power density, reduced EMI, and reduced system size & cost.





#### **Absolute Maximum Ratings** T<sub>C</sub> = 25 °C unless otherwise noted.

Symbol	Parameter		FFSD08120A	Unit
$V_{RRM}$	Peak Repetitive Reverse Voltage		1200	V
E <sub>AS</sub>	Single Pulse Avalanche Energy (Note 1)		80	mJ
I <sub>F</sub>	Continuous Rectified Forward Current @ Tc < 168 °C		8	Α
	Continuous Rectified Forward Current @ Tc < 135 °C		22.5	Α
I <sub>F, Max</sub>	Non-Repetitive Peak Forward Surge Current	T <sub>C</sub> = 25 °C, 10 μs	530	Α
		T <sub>C</sub> = 150 °C, 10 μs	480	Α
I <sub>F,SM</sub>	Non-Repetitive Forward Surge Current	Half-Sine Pulse, t <sub>p</sub> = 8.3 ms	77	Α
I <sub>F,RM</sub>	Repetitive Forward Surge Current	Half-Sine Pulse, t <sub>p</sub> = 8.3 ms	45	Α
Ptot	Dower Dissipation	T <sub>C</sub> = 25 °C	263	W
	Power Dissipation	T <sub>C</sub> = 150 °C	44	W
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		-55 to +175	°C

#### **Thermal Characteristic**

Symbol	Parameter	Ratings	Unit
R <sub>e.IC</sub>	Thermal Resistance, Junction to Case, Max	0.57	°C/W

#### **Package Marking and Ordering Information**

Part Number	Top Mark	Package	Reel Size	Tape Width	Quantity
FFSD08120A	FFSD08120A	D-PAK	13"	12 mm	2500 units

## **Electrical Characteristics** $T_C = 25$ °C unless otherwise noted.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V <sub>F</sub>	Forward Voltage	I <sub>F</sub> = 8 A, T <sub>C</sub> = 25 °C	-	1.45	1.75	V
		I <sub>F</sub> = 8 A, T <sub>C</sub> = 125 °C	-	1.7	2.0	
		$I_F = 8 \text{ A}, T_C = 175 ^{\circ}\text{C}$	-	2.0	2.4	
I <sub>R</sub>	Reverse Current	$V_R = 1200 \text{ V}, T_C = 25  ^{\circ}\text{C}$	-	-	200	μА
		$V_R = 1200 \text{ V}, T_C = 125  ^{\circ}\text{C}$	-	-	300	
		$V_R = 1200 \text{ V}, T_C = 175 ^{\circ}\text{C}$	-	-	400	
$Q_C$	Total Capacitive Charge	V = 800 V	-	55	-	nC
С	Total Capacitance	V <sub>R</sub> = 1 V, f = 100 kHz	-	538	-	pF
		$V_R = 400 \text{ V}, f = 100 \text{ kHz}$	-	50	-	
		V <sub>R</sub> = 800 V, f = 100 kHz	-	40	-	

Notes:

## Typical Characteristics T<sub>J</sub> = 25 °C unless otherwise noted.

Figure 1. Forward Characteristics

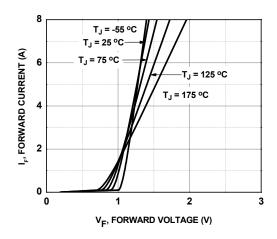


Figure 3. Reverse Characteristics

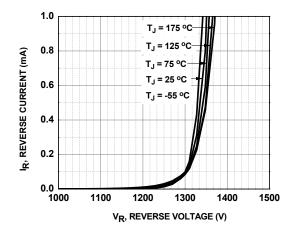


Figure 2. Reverse Characteristics

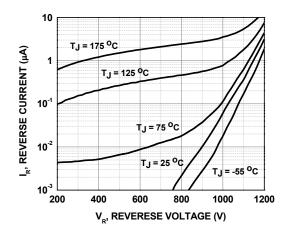
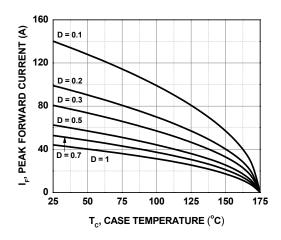


Figure 4. Current Derating



<sup>1:</sup>  $E_{AS}$  of 80 mJ is based on starting  $T_J$  = 25 °C, L = 0.5 mH,  $I_{AS}$  = 18 A, V = 50 V.

## **Typical Characteristics** T<sub>J</sub> = 25 °C unless otherwise noted.

Figure 5. Power Derating

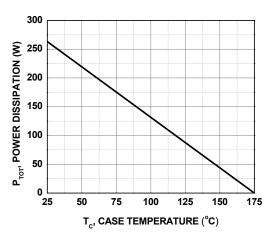


Figure 7. Capacitance vs. Reverse Voltage

Figure 6. Capacitive Charge vs. Reverse Voltage

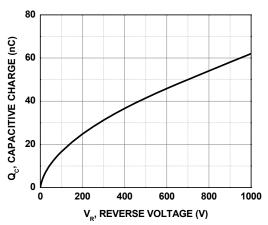
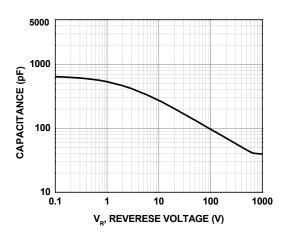


Figure 8. Capacitance Stored Energy



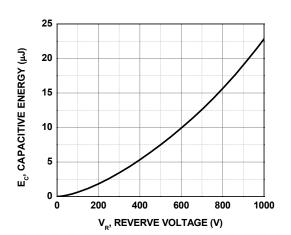
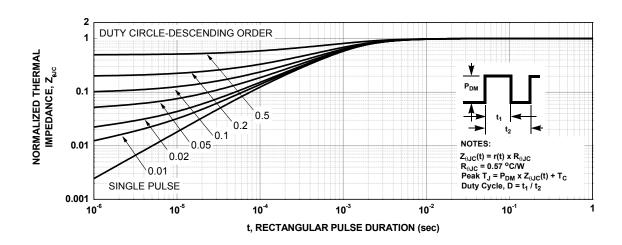
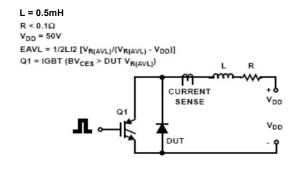


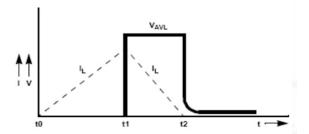
Figure 9. Junction-to-Case Transient Thermal Response Curve



## **Test Circuit and Waveforms**

Figure 10. Unclamped Inductive Switching Test Circuit & Waveform





#### **Mechanical Dimensions**

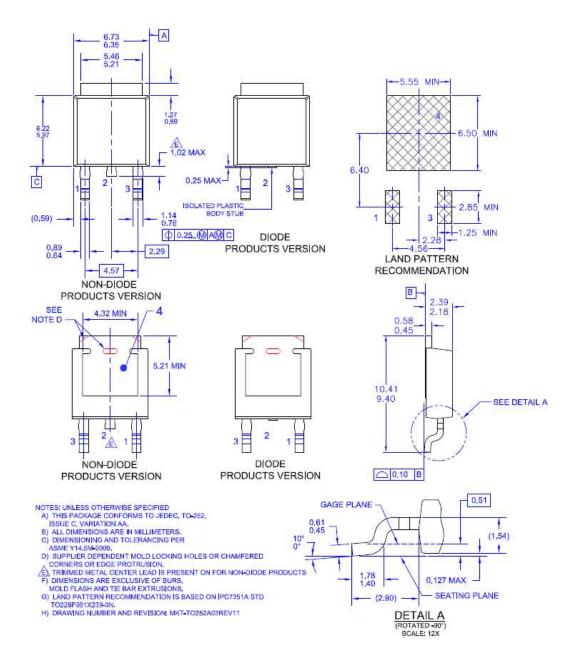


Figure 11. TO252 (D-PAK), MOLDED, 3 LEAD, OPTION AA&AB

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