

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China









June 2006

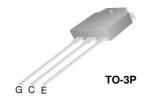
FGA120N30D

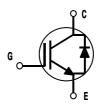
Features

- · High Current Capability
- Low saturation voltage: $V_{CE(sat)}$, Typ = 1.1V@ I_C = 25A
- · High Input Impedance

Description

Employing Unified IGBT Technology, FGA120N30D provides low conduction and switching loss. FGA120N30D offers the optimum solution for PDP applications where low condution loss is essential.





Absolute Maximum Ratings $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Description		FGA120N30D	Units
V _{CES}	Collector-Emitter Voltage		300	V
V _{GES}	Gate-Emitter Voltage		± 30	V
I _C	Collector Current	@ T _C = 25°C	120	Α
I _{CM}	Pulsed Collector Current (Note 1)	@ T _C = 25°C	300	Α
I _F	Diode Continuous Forward Current	@ T _C = 100°C	10	Α
I _{FM}	Diode Maximum Forward Current		40	Α
P _D	Maximum Power Dissipation	@ T _C = 25°C	290	W
	Maximum Power Dissipation	@ T _C = 100°C	116	W
TJ	Operating Junction Temperature		-55 to +150	°C
T _{stg}	Storage Temperature Range		-55 to +150	°C
T _L	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds		300	°C

Notes

(1) Repetitive test , pulse width = 100usec , Duty = 0.2

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Units
$R_{\theta JC}(IGBT)$	Thermal Resistance, Junction-to-Case for IGBT		0.43	°C/W
$R_{\theta JC}(DIODE)$	Thermal Resistance, Junction-to-Case for Diode		1.56	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient		40	°C/W

^{*} Ic_pulse limited by max Tj

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FGA120N30D	FGA120N30D	TO-3P			30

Electrical Characteristics of the IGBT $T_C = 25$ °C unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Off Charac	teristics					
BV _{CES}	Collector-Emitter Breakdown Voltage	$V_{GE} = 0V, I_{C} = 250 \mu A$	300			V
ΔBV _{CES} / ΔΤ _J	Temperature Coefficient of Breakdown Voltage	$V_{GE} = 0V, I_{C} = 250\mu A$		0.6		V/°C
I _{CES}	Collector Cut-Off Current	$V_{CE} = V_{CES}, V_{GE} = 0V$			100	μΑ
I _{GES}	G-E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0V$			± 250	nA
On Charac	teristics					
V _{GE(th)}	G-E Threshold Voltage	$I_C = 250uA$, $V_{CE} = V_{GE}$	2.5	4.0	5.0	V
		I _C = 25A, V _{GE} = 15V		1.1	1.4	V
V	Calleston to Facilities	I _C = 120A, V _{GE} = 15V		1.9		V
V _{CE(sat)}	Collector to Emitter Saturation Voltage	$I_C = 120A, V_{GE} = 15V,$ $T_C = 125^{\circ}C$		2.0		V
Dynamic C	Characteristics	,	,			I
C _{ies}	Input Capacitance			2310	-	pF
C _{oes}	Output Capacitance	$V_{CE} = 30V, V_{GE} = 0V,$		360	-	pF
C _{res}	Reverse Transfer Capacitance	f = 1MHz		100	-	pF
	Characteristics		<u> </u>	1	1	
t _{d(on)}	Turn-On Delay Time	_		30		ns
t _r	Rise Time	_		270		ns
t _{d(off)}	Turn-Off Delay Time	$V_{CC} = 200V, I_C = 25A,$ $R_G = 8.7\Omega, V_{GE} = 15V,$ Resistive Load, $T_C = 25^{\circ}C$		100		ns
t _f	Fall Time			130	300	ns
E _{on}	Turn-On Switching Loss	_		0.17		mJ
E _{off}	Turn-Off Switching Loss	_		0.56		mJ
E _{ts}	Total Switching Loss			0.73		mJ
t _{d(on)}	Turn-On Delay Time	_		30		ns
t _r	Rise Time	_		280		ns
t _{d(off)}	Turn-Off Delay Time	V_{CC} = 200V, I_{C} = 25A, R_{G} = 8.7 Ω , V_{GE} = 15V, Resistive Load, T_{C} = 125°C		105		ns
t _f	Fall Time			180		ns
E _{on}	Turn-On Switching Loss			0.18		mJ
E _{off}	Turn-Off Switching Loss	4		0.9		mJ
E _{ts}	Total Switching Loss	N 000V I 074		1.08		mJ
Q _g	Total Gate Charge	$V_{CE} = 200V, I_{C} = 25A,$ $V_{GE} = 15V$		120	180	nC
Q _{ge}	Gate-Emitter Charge	VGE = 10 V		15	22	nC
Q_{gc}	Gate-Collector Charge			60	90	nC

Electrical Characteristics of DIODE $T_{C} = 25^{\circ}\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions		Min.	Тур.	o. Max.	Units
V_{FM}	Diode Forward Voltage	I _F = 10A	$T_C = 25^{\circ}C$		1.1	1.4	V
			T _C = 125°C		0.9		
t _{rr}	Diode Reverse Recovery Time	I _F = 10A	$T_C = 25^{\circ}C$		21		ns
		dI/dt = 200A/μs	T _C = 125°C		35		
I _{rr}	Diode Peak Reverse Recovery Cur-		$T_C = 25^{\circ}C$		2.8		Α
	rent		T _C = 125°C		5.6		
Q _{rr}	Diode Reverse Recovery Charge		$T_C = 25^{\circ}C$		29.4		nC
			T _C = 125°C		98		

Typical Performance Characteristics

Figure 1. Typical Output Characteristics

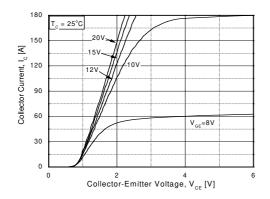


Figure 2. Typical Output Characteristics

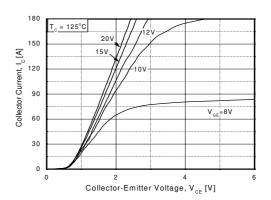


Figure3. Typical Saturation Voltage Characteristics

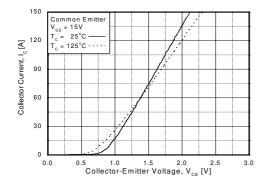


Figure 4. Transfer characteristics

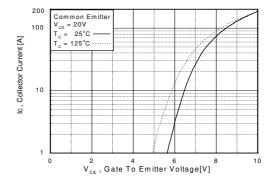


Figure 5. Saturation Voltage vs. Case
Temperature at Variant Current Level

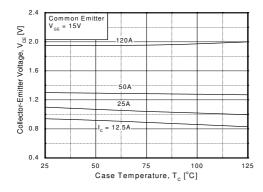
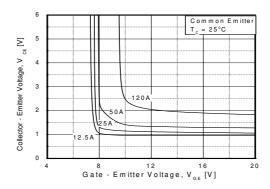


Figure 6. Saturation Voltage vs. V_{GE}



Typical Performance Characteristics (Continued)

Figure 7. Saturation Voltage vs. V_{GE}

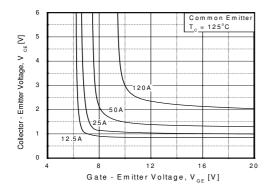


Figure 8. Capacitance Charaacteristics

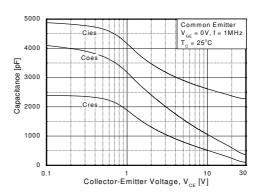


Figure 9. Gate Charge Characteristics

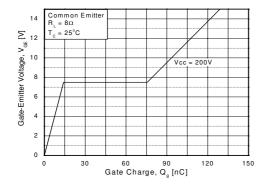


Figure 10. SOA Characteristics

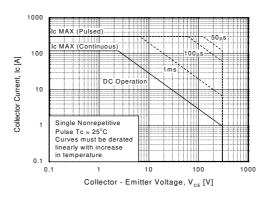


Figure 11. Turn-On Characteristics vs. Gate Resistance

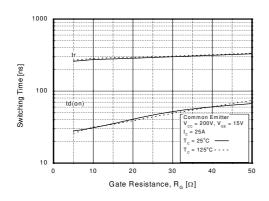
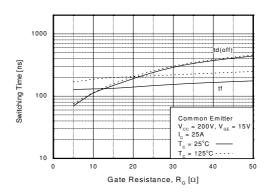


Figure 12. Turn-Off Characteristics vs. Gate Resistance



Typical Performance Characteristics (Continued)

Figure 13. Turn-On Characteristics vs. **Collector Current**

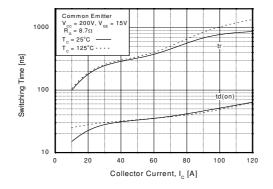


Figure 14. Turn-Off Characteristics vs. **Collector Current**

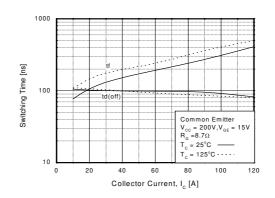


Figure 15. Switching Loss vs. Gate Resistance

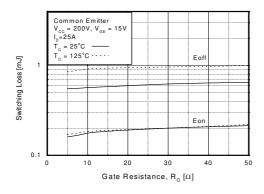


Figure 16. Switching Loss vs. **Collector Current**

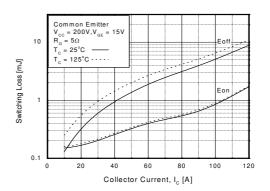
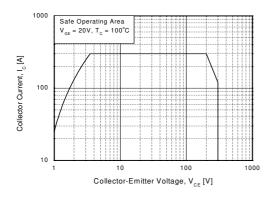


Figure 17. Turn-Off SOA Figure

FGA120N30D Rev. B



6 www.fairchildsemi.com

Typical Performance Characteristics (Continued)

Figure 18. Transient Thermal Impedance of IGBT

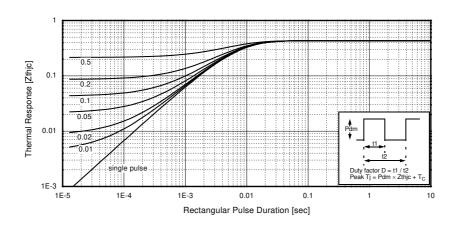


Figure 19. Forward Characteristics

T_J = 125°C

T_J = 125°C

T_J = 25°C

T_C = 25°C

T_C = 125°C

Forward Voltage , V_F[V]

Figure 20. Typical Reverse Recovery Current

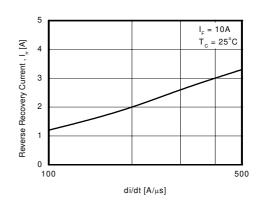
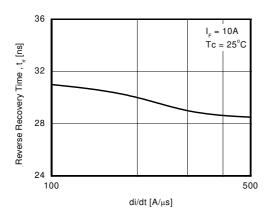
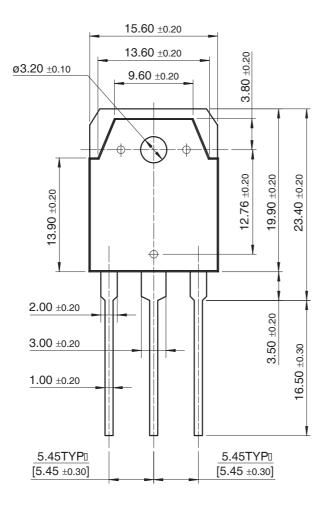


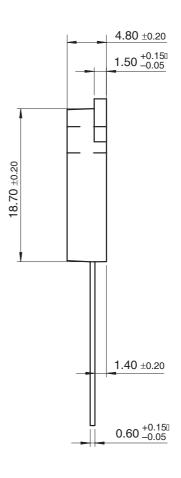
Figure 21. Typical Reverse Recovery Time

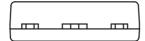


Mechanical Dimensions

TO-3P







TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACEx™	FAST [®]	ISOPLANAR™	PowerEdge™	SuperFET™
ActiveArray™	FASTr™	LittleFET™	PowerSaver™	SuperSOT™-3
Bottomless™	FPS™	MICROCOUPLER™	PowerTrench [®]	SuperSOT™-6
Build it Now™	FRFET™	MicroFET™	QFET [®]	SuperSOT™-8
CoolFET™	GlobalOptoisolator™	MicroPak™	QS™	SyncFET™
CROSSVOLT TM	GTO™	MICROWIRE™	QT Optoelectronics™	TCM™
DOME™	HiSeC™	MSX™	Quiet Series™	TinyLogic [®]
EcoSPARK™	I^2C^{TM}	MSXPro™	RapidConfigure™	TINYOPTO™
E ² CMOS™	i-Lo™	OCX™	RapidConnect™	TruTranslation™
EnSigna™	ImpliedDisconnect™	OCXPro™	mSerDes™	UHC™
FACT™	IntelliMAX™	OPTOLOGIC [®]	ScalarPump™	UniFET™
FACT Quiet Series™		OPTOPLANAR™	SILENT SWITCHER®	UltraFET [®]
Across the board. Around	d the world.™	PACMAN™	SMART START™	VCX™
The Power Franchise®		POP™	SPM™	Wire™
Programmable Active Dr	оор™	Power247™	Stealth™	

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.
As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the

2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

Rev. I19