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Features

- Intrinsic Anti-parallel Diode for Soft-switching Applications
- High Switching Frequency Range 10 kHz to 50kHz
- High Temperature Stable Behavior (T_{jmax} = 175^oC)
- Low Saturation Voltage Drop : VCE(sat) = 2.06 V @ IC = 50 A
- Robust Pot Detection Noise Immunity
- RoHS Compliant (Pb-free lead plating)

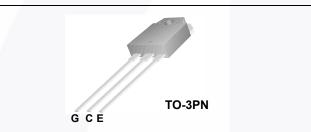
Applications

- · Induction Cooker, Rice-jar, and Microwave Oven
- Soft-switching Applications

General Description

Using advanced field stop trench and shorted-anode technology, Fairchild's shorted-anode trench IGBTs offer superior conduction and switching performances for switching applications. This device is tailored to induction cooker and microwave oven.

∩C



Absolute Maximum Ratings

Symbol	Description		Ratings	Unit V	
V _{CES}	Collector to Emitter Voltage	1100			
V _{GES}	Gate to Emitter Voltage		±25	V	
I _C	Collector Current	@ T _C = 25°C	50	А	
	Collector Current	@ T _C = 100 ^o C	30	A	
I _{CM (1)}	Pulsed Collector Current		120	A	
IF	Diode Continuous Forward Current	@ T _C = 25°C	50	A	
	Diode Continuous Forward Current	@ T _C = 100 ^o C	30	A	
P _D	Maximum Power Dissipation	@ T _C = 25°C	300	W	
	Maximum Power Dissipation	@ T _C = 100°C	150	W	
TJ	Operating Junction Temperature		-55 to +175	°C	
T _{stg}	Storage Temperature Range		-55 to +175	°C	
TL	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 second	ls	300	°C	

Thermal Characteristics

Symbol	Symbol Parameter		Max.	Unit	
$R_{\theta JC}(IGBT)$	Thermal Resistance, Junction to Case, Max	-	0.5	°C/W	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max	-	40	°C/W	

Notes:

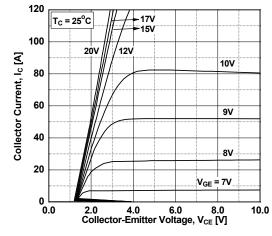
1: Limited by Tjmax

March 2016

Device Marking Device Pa		Package	ackage Reel Size		Tape Width		Quantity		
FGA50S1	-		TO-3PN	FO-3PN -		-		30	
Electrical	Char	acteristics of th		5°C unless otherwise noted					
Symbol		Parameter	Test	Conditions	Min.	Тур.	Max.	Unit	
Off Character	ristics		·						
	Collector Cut-Off Current		V _{CE} = 1100	V _{CE} = 1100 V, V _{GE} = 0 V		-	1	mA	
0L0	G-E Leakage Current		-	$V_{GE} = V_{GES}, V_{CE} = 0 V$		-	±500	nA	
GLG		<u> </u>	GL GLS						
On Character	ristics								
V _{GE(th)} G	B-E Thres	hold Voltage		I_{C} = 50 mA, V_{CE} = V_{GE}		5.6	7.5	V	
	Collector to Emitter Saturation Voltage		$I_{\rm C} = 50 \text{ A}, V_{\rm C}$ $T_{\rm C} = 25^{\rm o}{\rm C}$	I _C = 50 A, V _{GE} = 15 V T _C = 25 ^o C		2.06	2.6	V	
V _{CE(sat)} C			age $I_C = 50 \text{ A}, V_C$ $T_C = 125^{\circ}\text{C}$	$I_{C} = 50 \text{ A}, V_{GE} = 15 \text{ V}$ $T_{C} = 125^{\circ}\text{C}$		2.54	-	V	
			$I_{\rm C} = 50 \text{ A}, V_{\rm C}$ $T_{\rm C} = 175^{\rm o}{\rm C}$	I _C = 50 A, V _{GE} = 15 V, T _C = 175°C		2.7	-	V	
	Diode Forward Voltage		I _F = 50 A, T _C	I _F = 50 A, T _C = 25°C		1.96	2.6	V	
V _{FM} C			I _F = 50 A, T _C	I _F = 50 A, T _C = 175 ^o C		2.67	-	V	
	nput Capa			V _{CE} = 30 V, V _{GE} = 0 V, f = 1 MHz		2056 47.8	-	pF pF	
000		•					-		
C _{res} R	everse I	ransfer Capacitance			-	35.8	-	pF	
Switching Ch	aracteri	stics							
t _{d(on)} T	urn-On D	elay Time			-	24	-	ns	
. ,	lise Time				-	294	-	ns	
t _{d(off)} T	urn-Off D	elay Time	V _{CC} = 600 V	/, I _C = 50 A,	-	280	-	ns	
t _f F	all Time		R _G = 10 Ω V	/ _{GE} = 15 V,	-	95	-	ns	
E _{on} T	urn-On S	witching Loss	Resistive Lo	Resistive Load, T _C = 25 ^o C		2240	-	uJ	
E _{off} T	urn-Off S	witching Loss			-	990	-	uJ	
E _{ts} T	otal Swite	ching Loss			-	3230	-	uJ	
t _{d(on)} T	urn-On D	elay Time			-	24	-	ns	
	lise Time				-	346	-	ns	
t _{d(off)} T	urn-Off D	elay Time	V _{CC} = 600 V	$V_{CC} = 600 \text{ V}, I_C = 50 \text{ A},$ $R_G = 10 \Omega, V_{GE} = 15 \text{ V},$ Residue London T. = 175%	-	308	-	ns	
	all Time		R _G = 10 Ω \		-	184	-	ns	
E _{on} T	urn-On S	witching Loss	Resistive Lo	Resistive Load,, T _C = 175 ^o C		2640	-	uJ	
E _{off} T	urn-Off S	witching Loss			-	1820	-	uJ	
E _{ts} T	otal Swite	ching Loss			-	4460	-	uJ	
Q _g T	otal Gate	Charge				195	-	nC	
Q _{ge} G	Sate to Er	mitter Charge	V _{CE} = 600 V V _{OF} = 15 V	ν, I _C = 50 A,	-	15.4	-	nC	
		ollector Charge	•GE = 13 V	V _{GE} = 15 V		99.9	-	nC	

Typical Performance Characteristics







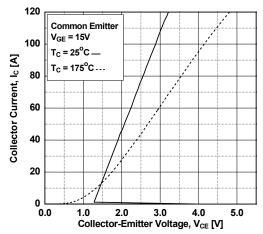


Figure 5. Saturation Voltage vs. Case Temperature at Variant Current Level

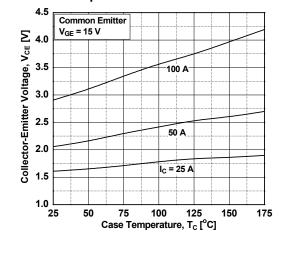


Figure 2. Typical Output Characteristics

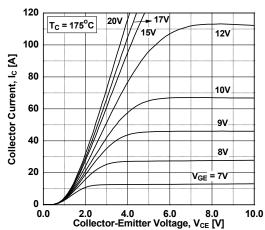


Figure 4. Transfer Characteristics

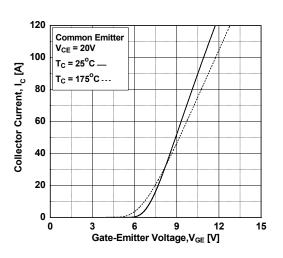
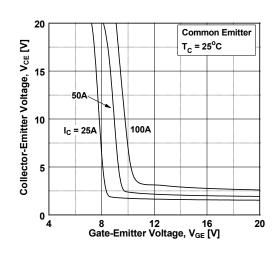


Figure 6. Saturation Voltage vs. $\rm V_{GE}$



Typical Performance Characteristics

Figure 7. Saturation Voltage vs. V_{GE}

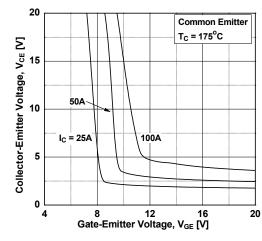
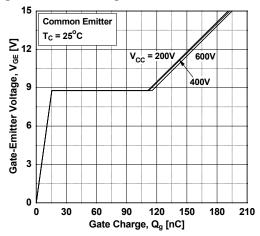


Figure 9. Gate charge Characteristics





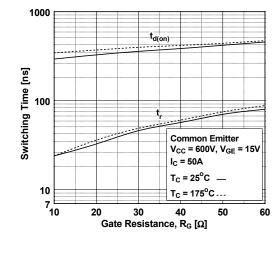
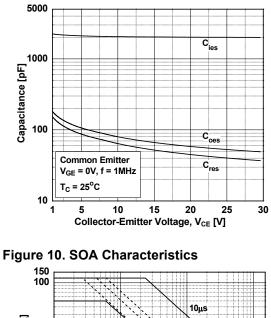
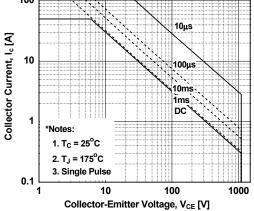
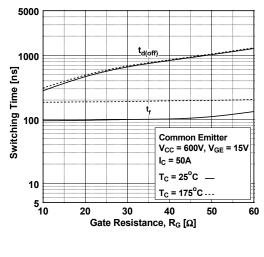


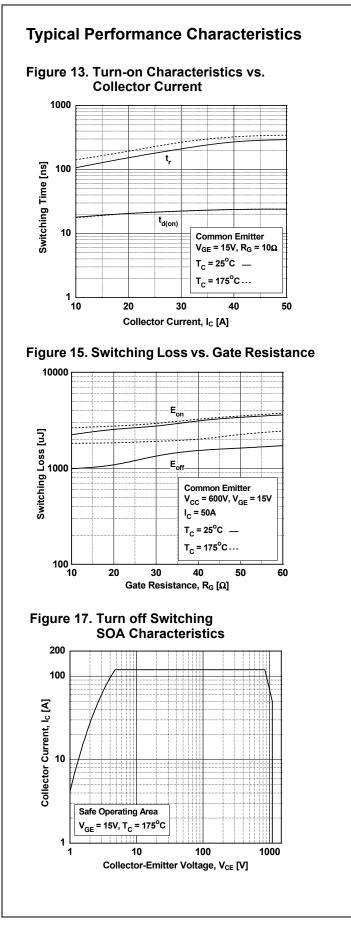
Figure 8. Capacitance Characteristics

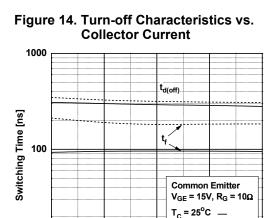


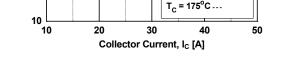




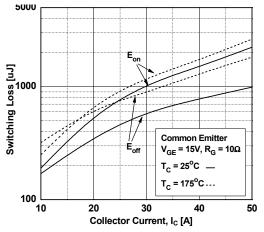


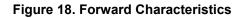


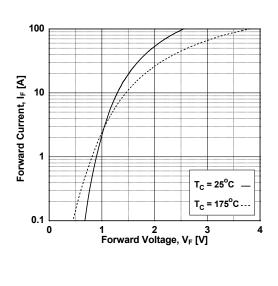


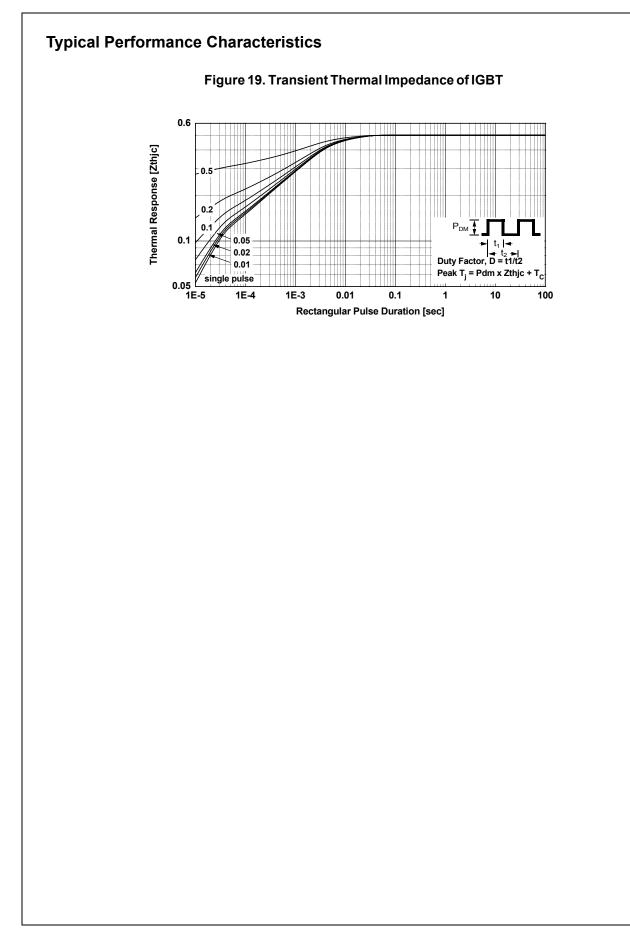


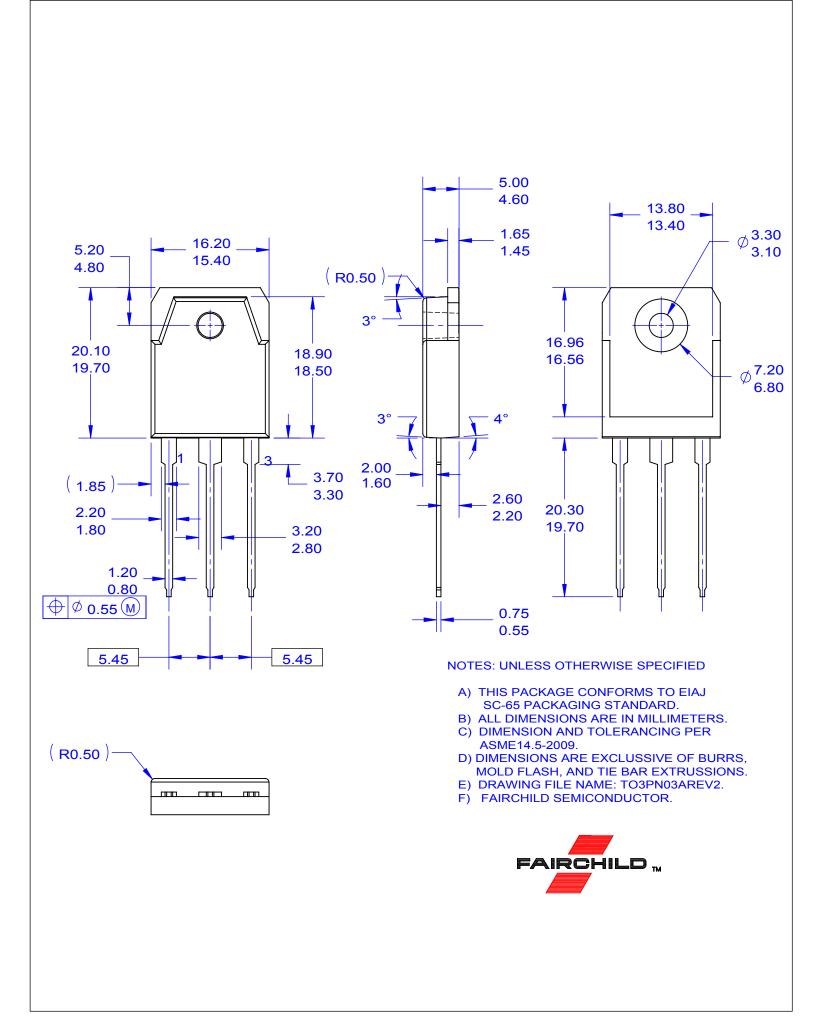












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