# imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



## Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832 Email & Skype: info@chipsmall.com Web: www.chipsmall.com Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China





## FGPF70N30 300V, 70A PDP IGBT

#### Features

- · High Current Capability
- Low saturation voltage: V<sub>CE</sub>(sat) =1.4V @ I<sub>C</sub> = 40A
- High Input Impedance
- · Fast switching
- · RoHS Complaint

### Application

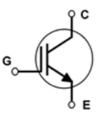
. PDP System



### **General Description**

Employing Unified IGBT Technology, Fairchild's PDP IGBTs provides low conduction and switching loss. FGPF70N30 offers the optimum solution for PDP applications where low-condution loss is essential.





#### **Absolute Maximum Ratings**

Symbol	Description		FGPF70N30	Units
V <sub>CES</sub>	Collector-Emitter Voltage		300	V
V <sub>GES</sub>	Gate-Emitter Voltage		±30	V
I <sub>C pulse(1)</sub>	Pulsed Collector Current	@ T <sub>C</sub> = 25°C	160	A
P <sub>D</sub>	Maximum Power Dissipation	@ T <sub>C</sub> = 25°C	52	W
	Maximum Power Dissipation	@ T <sub>C</sub> = 100°C	20.8	W
TJ	Operating Junction Temperature		-55 to +150	°C
T <sub>stg</sub>	Storage Temperature Range		-55 to +150	°C
TL	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds		300	°C

#### **Thermal Characteristics**

Symbol	Parameter	Тур.	Max.	Units
$R_{\theta JC}(IGBT)$	HeJC(IGBT) Thermal Resistance, Junction-to-Case		2.4	°C/W
R <sub>0JA</sub> Thermal Resistance, Junction-to-Ambient			62.5	°C/W

#### Notes:

(1)Repetitive test , pluse width = 100usec , Duty = 0.1  $\,$ 

\* Ic\_pluse limited by max Tj

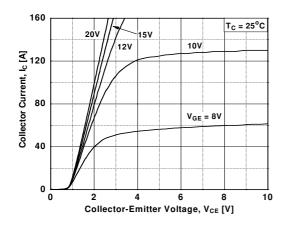
October 2006

П
G
Τ
Ť
1
Ö
Z
ω
Ö
6.3
õ
S
Ž
7
,70
, 70A
70A
70/
70A

Device M	arking	Device	Pac	ckage	Packaging age Type		Qty per Tube		Max Qty per Box	
		TO	O-220F Rail / Tube		50ea		-			
Electrica	al Char	racteristics T <sub>c = 2</sub>	5ºC unless	otherwise noted						
Symbol		Parameter		Tes	st Conditions	Min.	Тур.	Max.	Units	
Off Charact	orietice					·				
		Emitter Breakdown Vol	tage	V <sub>GE</sub> = 0V, I <sub>C</sub> = 250uA		300			V	
ΔB <sub>VCES</sub> /		ure Coefficient of Break	0				0.6		V/ºC	
-		Cut-Off Current		$V_{CE} = V_{CI}$	<sub>ES</sub> , V <sub>GE</sub> = 0V			100	uA	
	G-E Leak	age Current		$V_{GE} = V_{GES}, V_{CE} = 0V$				± 250	nA	
On Characte	eristics					·		•		
r		shold Voltage		$I_{C}$ = 250uA, $V_{CE}$ = $V_{GE}$		2.5	4.0	5	V	
	Collector to Emitter Saturation Voltage		I <sub>C</sub> =20A, V <sub>GE</sub> = 15V			1.2	1.5	V		
			I <sub>C</sub> =40A, V <sub>GE</sub> = 15V			1.4		V		
			$I_{C} = 70A, V_{GE} = 15V$ $T_{C} = 25^{\circ}C$			1.8		V		
			$I_{C} = 70A, V_{GE} = 15V$ $T_{C} = 125^{o}C$			1.9		V		
Dynamic Ch	aracteris	tics								
C <sub>ies</sub>	Input Capacitance			V <sub>CE</sub> = 30V, V <sub>GE</sub> = 0V f = 1MHz			1300		pF	
	Output Capacitance Reverse Transfer Capacitance					180		pF		
						60		pF		
Switching C	haracteri	stics								
		Delay Time					17		ns	
	Rise Time	)		$V_{\rm CC} = 200$	$V, I_{C} = 40A$		83		ns	
	Turn-Off E	Delay Time		$R_G = 15\Omega$ , $V_{GE} = 15V$ Resistive Load, $T_C = 25^{\circ}C$			103		ns	
	Fall Time						160	300	ns	
	Turn-On E	Delay Time		$V_{CC} = 200 \text{ V}, \text{ I}_{C} = 40\text{A}$ $R_{G} = 15\Omega, V_{GE} = 15\text{V}$ Resistive Load, $T_{C} = 125^{\circ}\text{C}$			18		ns	
	Rise Time	)					83		ns	
t <sub>d(off)</sub>	Turn-Off E	Delay Time					104		ns	
	Fall Time			]	-		250		ns	
Qg	Total Gate	e Charge					71		nC	
Q <sub>ge</sub>	Gate-Emit	tter Charge		$V_{CE} = 200 \text{ V}, I_C = 40\text{ A}$			10		nC	
	Gate-Colle	ector Charge		V <sub>GE</sub> = 15	v		34		nC	

#### Typical Performance CharacteristicsTypical Saturation VoltageCharacteristics

#### Figure 1. Typical Output Characteristics





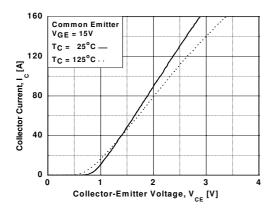
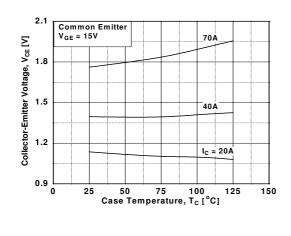
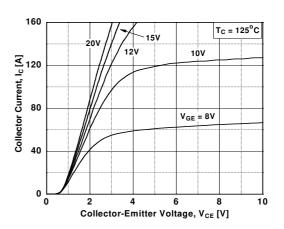


Figure 5. Saturation Voltage vs. Case Temperature at Variant Current Level



**Figure 2. Typical Output Characteristics** 



**Figure 4. Transfer Characteristics** 

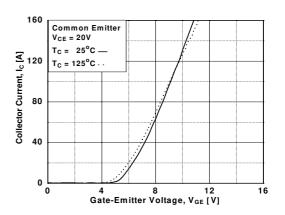
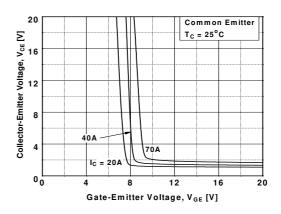


Figure 6. Saturation Voltage vs.V<sub>GE</sub>



#### Typical Performance Characteristics (Continued)

Figure 7. Saturation Voltage vs. V<sub>GE</sub>

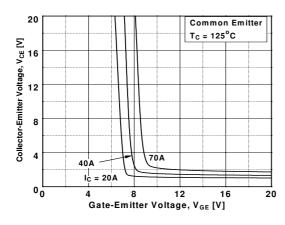


Figure 9. Gate Charge Characteristics

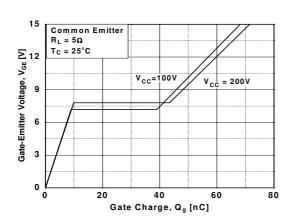
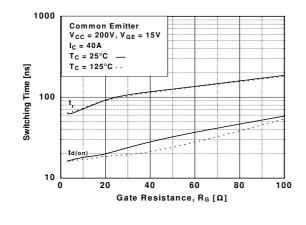


Figure 11. Turn-On Characteristics vs. Gate Resistance



**Figure 8. Capacitance Characteristics** 

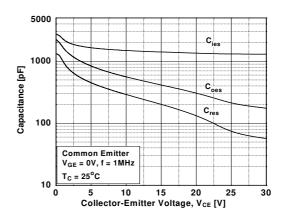


Figure 10. SOA Characteristics

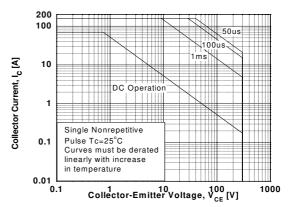
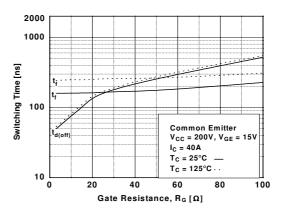
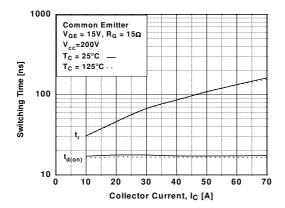


Figure 12. Turn Off Characteristics vs. Gate Resistance

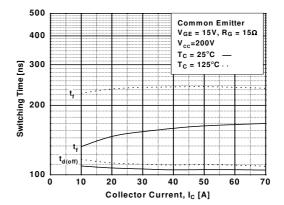


#### Typical Performance Characteristics (Continued)

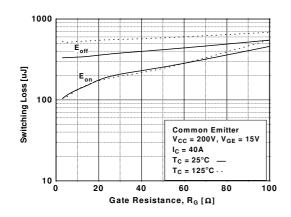
Figure 13. Turn-On Characteristics vs. Collector Current



#### Figure 14. Turn-Off Characteristics vs. Collector Current



#### Figure 15. Switching Loss vs Gate Resistance





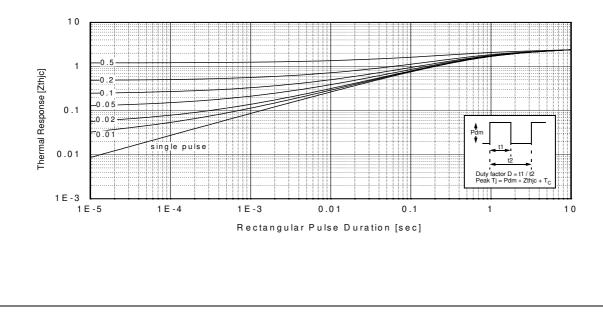
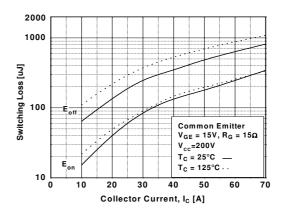
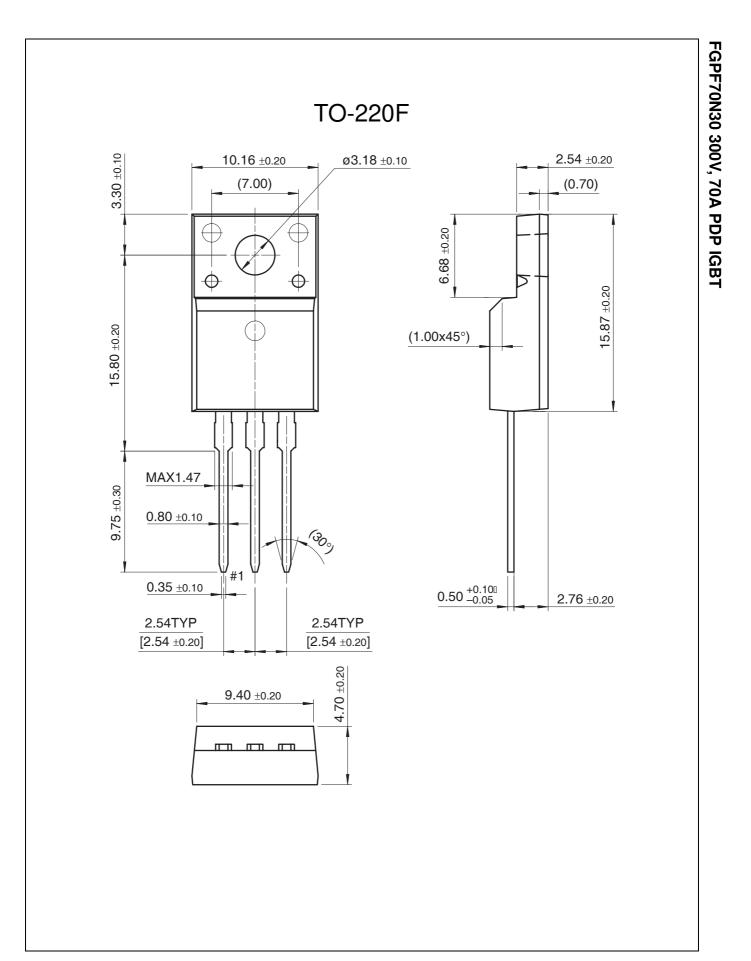


Figure 16. Switching Loss VS Collector Current





#### TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACEx™ ActiveArray™ Bottomless™ Build it Now™ CoolFET™ *CROSSVOLT*™ DOME™ EcoSPARK™ E<sup>2</sup>CMOS™ EnSigna™ FACT™ FAST® FASTr™ FPS™ FRFET™

FACT Quiet Series™ GlobalOptoisolator™ GTO™ HiSeC™ I<sup>2</sup>C™ i-Lo™ ImpliedDisconnect<sup>™</sup> IntelliMAX<sup>™</sup> **ISOPLANAR™** LittleFET™ MICROCOUPLER™ MicroFET™ MicroPak™ MICROWIRE™ MSX™ MSXPro™ Across the board. Around the world.™

OCX™ OCXPro™ OPTOLOGIC<sup>®</sup> **OPTOPLANAR™** PACMAN™ POP™ Power247™ PowerEdge™ PowerSaver™ PowerTrench<sup>®</sup> QFET<sup>®</sup> QS™ QT Optoelectronics™ Quiet Series™ RapidConfigure™ RapidConnect™ µSerDes™ ScalarPump™

SILENT SWITCHER<sup>®</sup> SMART START™ SPM™ Stealth™ SuperFET™ SuperSOT™-3 SuperSOT<sup>™</sup>-6 SuperSOT™-8 SyncFET™ TCM™ TinyBoost™ TinyBuck™ TinyPWM™ TinyPower™ TinyLogic® TINYOPTO™ TruTranslation™ UHC™

UniFET™ UltraFET<sup>®</sup> VCX™ Wire™

The Power Franchise<sup>®</sup>

Programmable Active Droop™

DISCLAIMER FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARDANTY THEREIN. WHICH COVERS THESE PRODUCTS.

LIFE SUPPORT POLICY FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.

2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

#### **PRODUCT STATUS DEFINITIONS Definition of Terms**

Datasheet Identification	Product Status	Definition   This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.		
Advance Information	Formative or In Design			
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.		
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.		
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.		

Rev. I20