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FJL6920

High Voltage Color Display Horizontal Deflection Output

- High Collector-Base Breakdown Voltage : BV_{CBO} = 1700V
- Low Saturation Voltage : V_{CE}(sat) = 3V (Max.)
- · For Color Monitor



NPN Triple Diffused Planar Silicon Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V _{CBO}	Collector-Base Voltage	1700	V
V _{CEO}	Collector-Emitter Voltage	800	V
V _{EBO}	Emitter-Base Voltage	6	V
I _C	Collector Current (DC)	20	Α
I _{CP} *	Collector Current (Pulse)	30	Α
P _C	Collector Dissipation	200	W
T _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	-55 ~ 150	°C

^{*} Pulse Test: PW=300µs, duty Cycle=2% Pulsed

Electrical Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
I _{CES}	Collector Cut-off Current	V _{CB} =1400V, R _{BE} =0			1	mA
I _{CBO}	Collector Cut-off Current	V _{CB} =800V, I _E =0			10	μΑ
I _{EBO}	Emitter Cut-off Current	$V_{EB}=4V$, $I_{C}=0$			1	mA
BV _{CBO}	Collector-Base Breakdown Voltage	I _C =500μA, I _E =0	1700			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	I _C =5mA, I _B =0	800			V
BV _{EBO}	Emitter-Base Breakdown Voltage	I _E =500μA, I _C =0	6			V
h _{FE1}	DC Current Gain	V _{CE} =5V, I _C =1A	8			
h _{FE2}		V _{CE} =5V, I _C =11A	5.5		8.5	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C =11A, I _B =2.75A			3	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C =11A, I _B =2.75A			1.5	V
t _{STG} *	Storage Time	V_{CC} =200V, I_{C} =10A, R_{L} =20 Ω			3	μs
t _F *	Fall Time	I _{B1} =2.0A, I _{B2} = - 4.0A		0.15	0.2	μs

^{*} Pulse Test: PW=20µs, duty Cycle=1% Pulsed

Thermal Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Тур	Max	Units
$R_{\theta iC}$	Thermal Resistance, Junction to Case		0.625	°C/W

Typical Characteristics

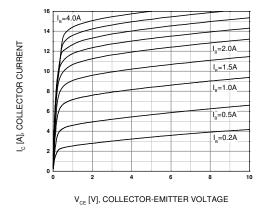


Figure 1. Static Characteristics

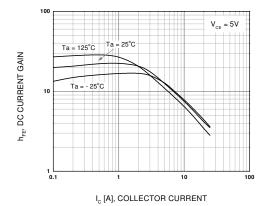


Figure 2. DC Current Gain

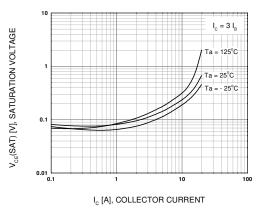


Figure 3. Collector-Emitter Saturation Voltage

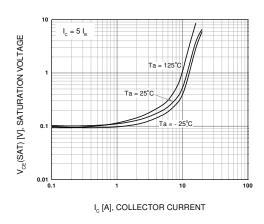


Figure 4. Collector-Emitter Saturation Voltage

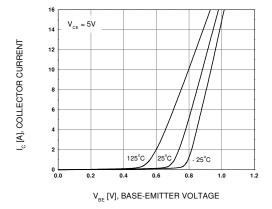


Figure 5. Base-Emitter On Voltage

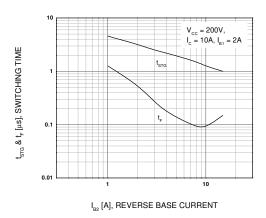


Figure 6. Resistive Load Switching Time

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Typical Characteristics (Continued)

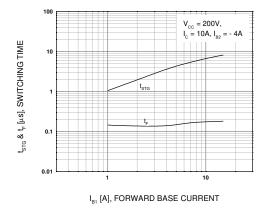


Figure 7. Resistive Load Switching Time

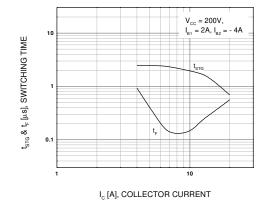


Figure 8. Resistive Load Switching Time

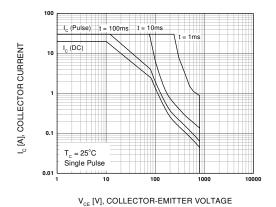


Figure 9. Forward Bias Safe Operating Area

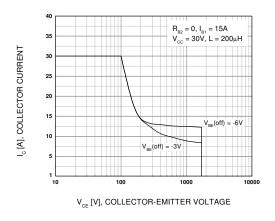


Figure 10. Reverse Bias Safe Operating Area

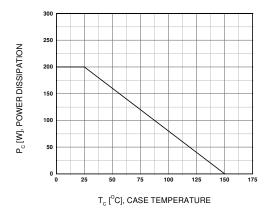
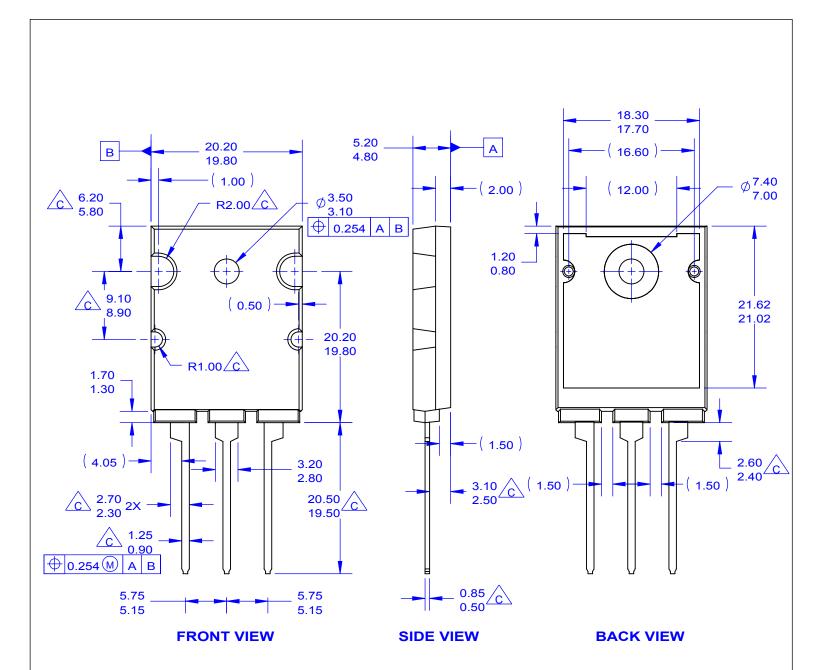
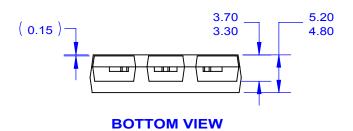


Figure 11. Power Derating

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