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Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China









FJN4308R

Switching Application (Bias Resistor Built In) - Switching circuit, Inverter, Interface circuit, Driver Circuit

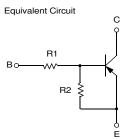
- Built in bias Resistor ($R_1=47K\Omega$, $R_2=22K\Omega$)
- Complement to FJN3308R



PNP Epitaxial Silicon Transistor

Absolute Maximum Ratings T_a=25°C unless otherwise noted

Symbol	Parameter	Value	Units	
V _{CBO}	Collector-Base Voltage	-50	V	
V _{CEO}	Collector-Emitter Voltage	-50	V	
V _{EBO}	Emitter-Base Voltage	-10	V	
I _C	Collector Current	-100	mA	
P _C	Collector Power Dissipation	300	mW	
TJ	Junction Temperature	150	°C	
T _{STG}	Storage Temperature	-55 ~ 150	°C	



Electrical Characteristics T_a=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	$I_{C} = -10\mu A, I_{E} = 0$	-50			٧
BV _{CEO}	Collector-Emitter Breakdown Voltage	I _C = -100μA, I _B =0	-50			V
I _{CBO}	Collector Cut-off Current	V_{CB} = -40V, I_{E} =0			-0.1	μΑ
h _{FE}	DC Current Gain	V_{CE} = -5V, I_{C} = -5mA	56			
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = -10mA, I _B = -0.5mA			-0.3	V
f _T	Current Gain Bandwidth Product	V_{CE} = -10V, I_{C} = -5mA		200		MHz
C _{ob}	Output Capacitance	V _{CB} = -10V, I _E =0 f=1.0MHz		5.5		pF
V _I (off)	Input Off Voltage	V_{CE} = -5V, I_{C} = -100 μ A	-0.8			V
V _I (on)	Input On Voltage	$V_{CE} = -0.3V, I_{C} = -2mA$			-4	V
R ₁	Input Resistor		32	47	62	ΚΩ
R ₁ /R ₂	Resistor Ratio		1.9	2.1	2.4	

Typical Characteristics

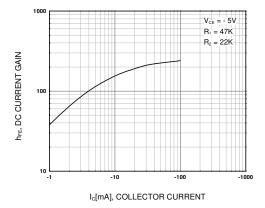


Figure 1. DC current Gain

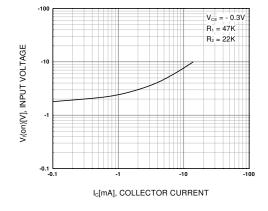


Figure 2. Input On Voltage

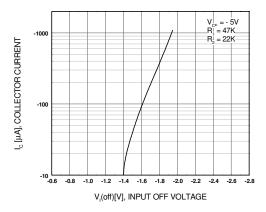


Figure 3. Input Off Voltage

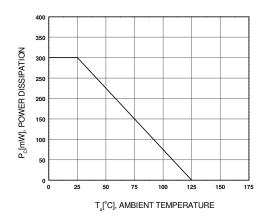
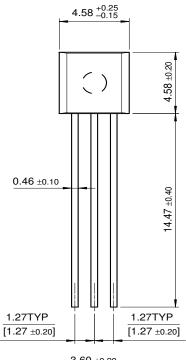


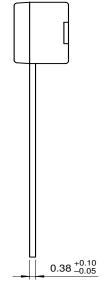
Figure 4. Power Derating

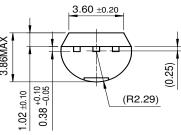


FJN4308R

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Rev. I1

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