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April 2013



FJPF2145 ESBC[™] Rated NPN Power Transistor

ESBC Features (FDC655 MOSFET)

V _{CS(ON)}	Ι _C	Equiv. R _{CS(ON)} ⁽¹⁾
0.21 V	2 A	0.105 Ω

- · Low Equivalent On Resistance
- Very Fast Switch: 150 kHz
- Wide RBSOA: Up to 1100 V
- Avalanche Rated
- Low Driving Capacitance, no Miller Capacitance
- Low Switching Losses
- · Reliable HV Switch: No False Triggering due to High dv/dt Transients

Applications

- High-Voltage, High-Speed Power Switches
- Emitter-Switched Bipolar/MOSFET Cascode (ESBC[™])
- Smart Meters, Smart Breakers, SMPS, **HV Industrial Power Supplies**
- Motor Drivers and Ignition Drivers

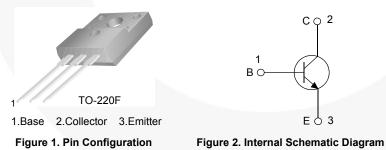
Description

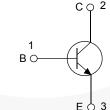
The FJPF2145 is a low-cost, high-performance power switch designed to provide the best performance when used in an ESBC[™] configuration in applications such as: power supplies, motor drivers, smart grid, or ignition switches. The power switch is designed to operate up to 1100 volts and up to 5 amps, while providing exceptionally low on-resistance and very low switching losses.

The ESBC[™] switch can be driven using off-the-shelf power supply controllers or drivers. The ESBC[™] MOS-FET is a low-voltage, low-cost, surface-mount device that combines low-input capacitance and fast switching. The ESBC[™] configuration further minimizes the required driving power because it does not have Miller capacitance.

The FJPF2145 provides exceptional reliability and a large operating range due to its square reverse-bias-safe-operating-area (RBSOA) and rugged design. The device is avalanche rated and has no parasitic transistors, so is not prone to static dv/dt failures.

The power switch is manufactured using a dedicated high-voltage bipolar process and is packaged in a highvoltage TO-220F package.





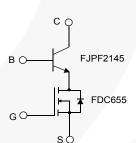


Figure 3. ESBC Configuration⁽²⁾

Ordering Information

Part Number	Marking	Package	Packing Method
FJPF2145TU	J2145	TO-220F	TUBE

Notes:

- 1. Figure of Merit.
- 2. Other Fairchild MOSFETs can be used in this ESBC application.

Absolute Maximum Ratings⁽³⁾

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}$ C unless otherwise noted..

Symbol	Parameter	Value	Units V	
V _{CBO}	Collector-Base Voltage	1100		
V _{CEO}	Collector-Emitter Voltage	800	V	
V _{EBO}	Emitter-Base Voltage	7	V	
Ι _C	Collector Current (DC)	5	Α	
I _B	Base Current	1.5	Α	
P _C	Collector Dissipation ($T_C = 25^{\circ}C$)	40	W	
T _J Operating and Junction Temperature Range		-55 to +125	°C	
T _{STG} Storage Temperature Range		-55 to +150	°C	
EAR ⁽⁴⁾	Avalanche Energy (T _J = 25°C, 1.2 mH)	15	mJ	

Notes:

3. Pulse test is pulse width \leq 5 ms, duty cycle \leq 10%.

4. Lab characterization data only for reference.

Thermal Characteristics

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Max.	Units
R _{θjc}	Thermal Resistance, Junction to Case	3.125	°C/W
R _{θja}	Thermal Resistance, Junction to Ambient	70.44	°C/W

Electrical Characteristics⁽⁵⁾

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Volt- age	I _C = 1 mA, I _E = 0	1100			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	I _C = 5 mA, I _B = 0	800			V
BV _{EBO}	Emitter-Base Breakdown Voltage	I _E = 1 mA, I _C = 0	7			V
I _{CBO}	Collector Cut-off Current	V _{CB} = 800 V, I _E = 0			10	μA
I _{EBO}	Emitter Cut-off Current	V _{EB} = 5 V, I _C = 0			10	μA
h _{FE1}	DC Current Gain	$V_{CE} = 5 V, I_{C} = 0.2 A$	20		40	
h _{FE2}		V _{CE} = 5 V, I _C = 1 A	8			
	Collector-Emitter Saturation Volt- age	I _C = 0.25 A, I _B = 0.05 A		0.051		V
V/ (cot)		I _C = 0.5 A, I _B = 0.167 A		0.055		V
V _{CE} (sat) a		I _C = 1 A, I _B = 0.33 A		0.085		V
		I _C = 1.5 A, I _B = 0.3 A	· · · · ·	0.159	2.000	V
	Base-Emitter Saturation Voltage	I _C = 500 mA, I _B = 50 mA		0.756		V
V _{BE} (sat)		I _C = 1.5 A, I _B = 0.3 A		0.840	1.500	V
		I _C = 2 A, I _B = 0.4 A		0.863		V
CIB	Input Capacitance	V _{EB} = 5 V, I _C = 0, f = 1 MHz		1.618		pF
C _{OB}	Output Capacitance	V _{CB} = 200 V, I _E = 0, f = 1 MHz		11.39		pF
f _T	Current Gain Bandwidth Product	V _{CE} = 10 V, I _C = 0.2 A		15		MHz

Note:

5. Pulse test is pulse width \leq 5 ms, duty cycle \leq 10%.

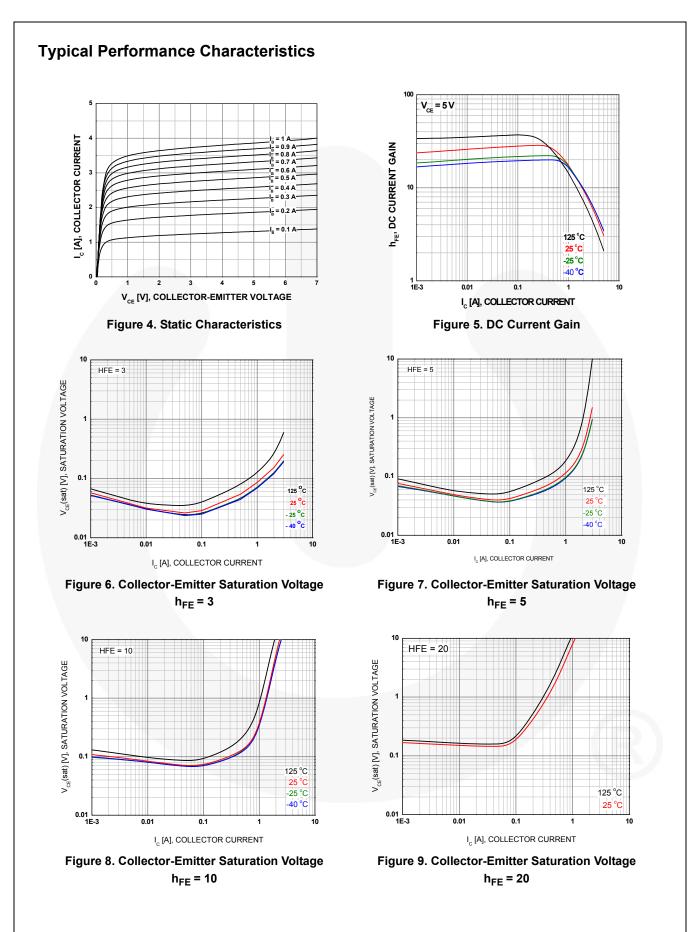
ESBC-Configured Electrical Characteristics⁽⁶⁾

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
f _T	Current Gain Bandwidth Product	I _C = 0.1 A, V _{CE} = 10 V		28.40		MHz
lt _f	Inductive Current Fall Time			95		ns
t _s	Inductive Storage Time	V _{CC} = 100 V, V _{GS} = 10 V, R _G = 4 7Ω,		0.13		ns
Vt _f	Inductive Voltage Fall Time	V _{Clamp} = 500 V, I _C = 0.5 A, I _B = 0.05 A, H _{FE} = 10, L _C = 166 μH,		135		ns
Vt _r	Inductive Voltage Rise Time	SRF = 684 kHz		80		ns
t _c	Inductive Crossover Time			115		ns
lt _f	Inductive Current Fall Time			50		ns
t _s	Inductive Storage Time	V_{CC} = 100 V, V_{GS} = 10 V, R_{G} = 47 Ω ,		0.34		ns
Vt _f	Inductive Voltage Fall Time	V_{Clamp} = 500 V, I _C = 1 A, I _B = 0.2 A, H _{FE} = 5, L _C = 166 µH,		150		ns
Vt _r	Inductive Voltage Rise Time	SRF = 684 kHz		60		ns
t _c	Inductive Crossover Time			95		ns
V _{CSW}	Maximum Collector- Source Voltage at Turn-off without Snubber	h _{FE} = 5, I _C = 2 A	1100			V
I _{GS(OS)}	Gate-Source Leakage Current	V _{GS} = ±20 V		1		nA
		V_{GS} = 10 V, I _C =2 A, I _B = 0.67 A, h _{FE} = 3		0.209		V
V	Collector-Source On Voltage	V_{GS} = 10 V, I_{C} = 1 A, I_{B} = 0.33 A, h_{FE} = 3		0.114		V
V _{CS(ON)}		V_{GS} = 10 V, I _C = 0.5 A,I _B = 0.17 A, h _{FE} = 3		0.068		V
		V_{GS} = 10 V, I _C = 0.3 A, I _B = 0.06 A, h _{FE} = 5		0.062		V
V _{GS(th)}	Gate Threshold Voltage	$V_{BS} = V_{GS}, I_B = 250 \ \mu A$		1.9		V
C _{iss}	Input Capacitance $(V_{GS} = V_{CB} = 0)$	V _{CS} = 25 V, f = 1 MHz		470		pF
Q _{GS(tot)}	Gate-Source Change V _{CB} = 0	V_{GS} = 10 V, I _C = 6.3 A, V _{CS} = 25 V		9		nC
	Statia Drain to Course	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 6.3 \text{ A}$		21	/	mΩ
R _{DS(ON)}	Static Drain-to-Source On Resistance	V _{GS} = 4.5 V, I _D = 5.5 A		26		mΩ
		V _{GS} = 10 V, I _D = 6.3 A, T _J = 125°C		30		mΩ

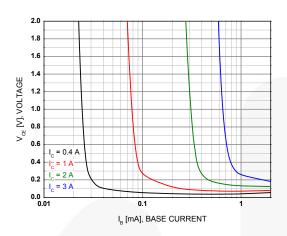
Note:

6. A typical FDC655 MOSFET was used for the specifications above. Values could vary if other Fairchild MOSFETs are used.











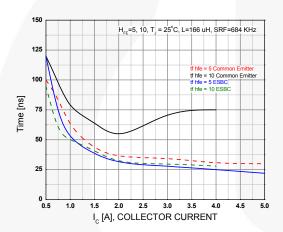
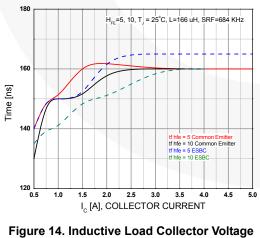
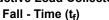


Figure 12. Inductive Load Collector Current Fall - Time (t_r)





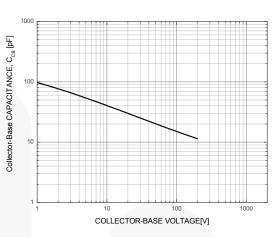


Figure 11. Capacitance

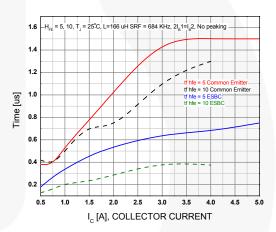
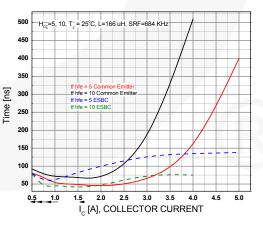
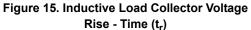
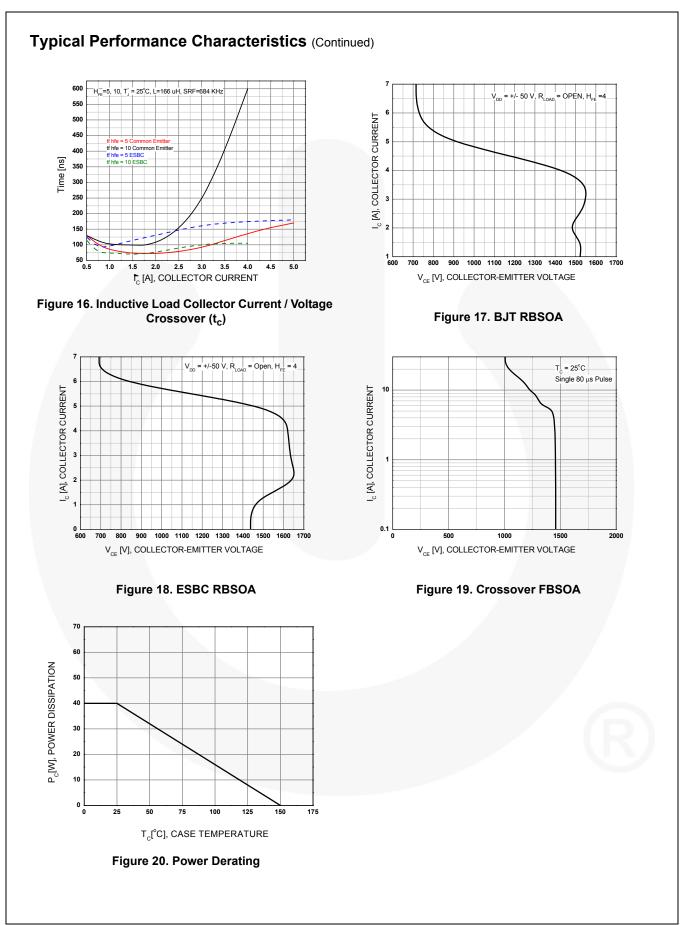


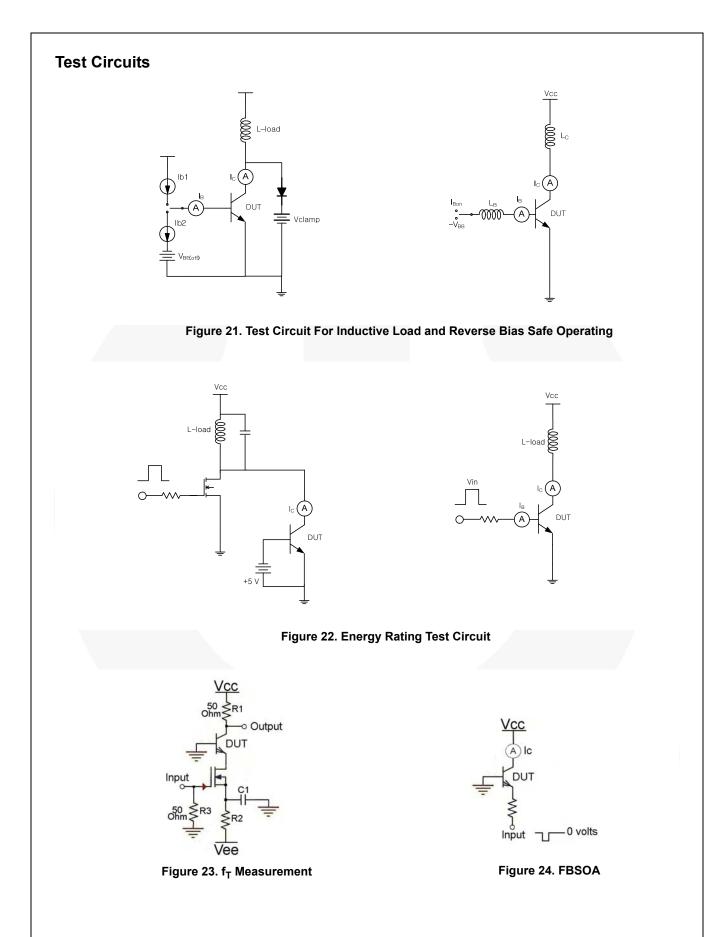
Figure 13. Inductive Load Collector Current Storage - Time (t_{stg})







FJPF2145 — ESBC[™] Rated NPN Power Transistor



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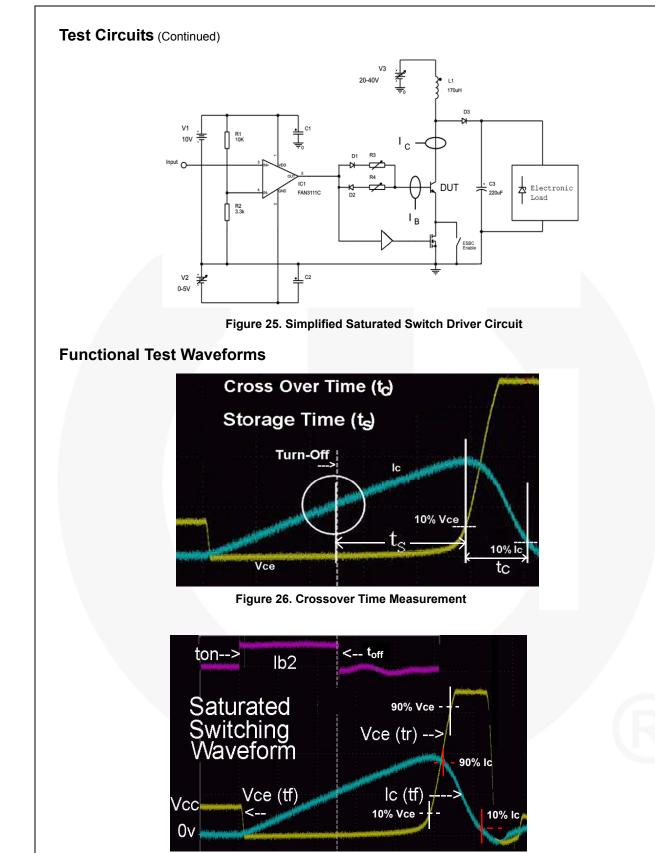
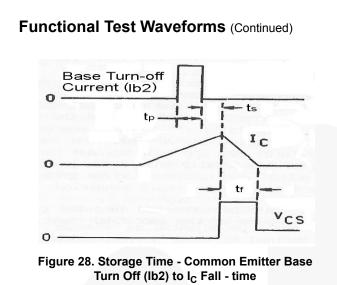


Figure 27. Saturated Switching Waveform



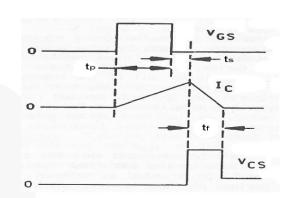
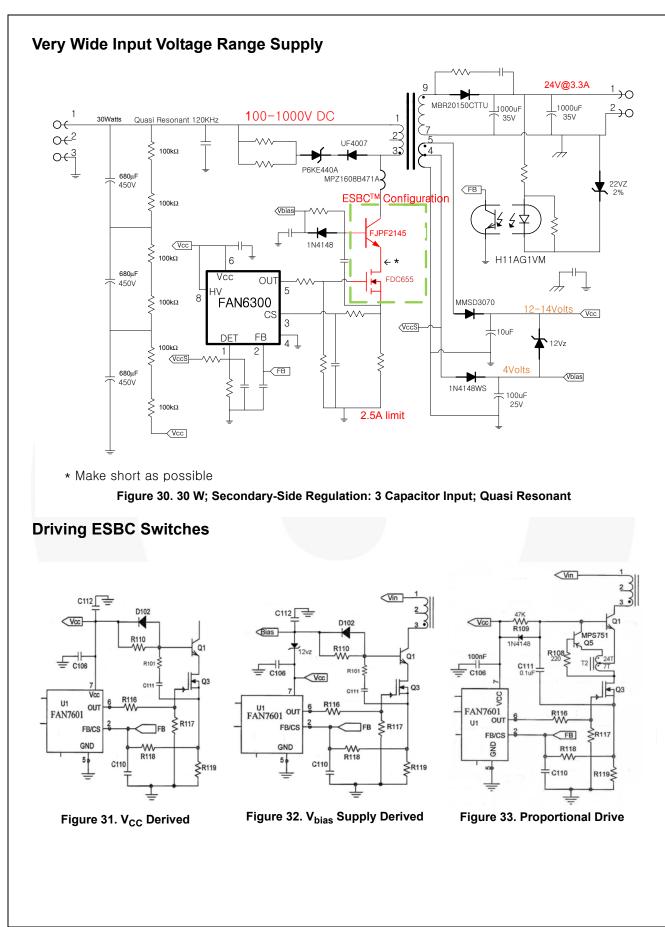
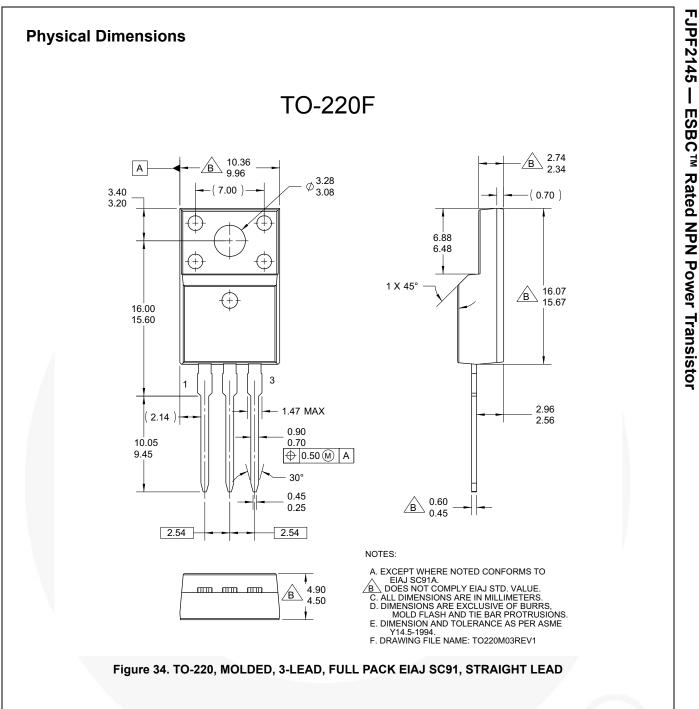


Figure 29. Storage Time - ESBC FET Gate (off) to I_C Fall - time





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