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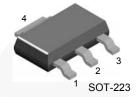


October 2014

# FJT44 NPN Epitaxial Silicon Transistor

#### **Features**

· High-Voltage Transistor



1. Base 2,4. Collector 3. Emitter

## **Ordering Information**

| Part Number | Marking | Package    | Packing Method, Size    |
|-------------|---------|------------|-------------------------|
| FJT44TF     | FJT44   | SOT-223 4L | Tape and Reel, 4000 pcs |
| FJT44KTF    | FJT44   | SOT-223 4L | Tape and Reel, 2500 pcs |

## **Absolute Maximum Ratings**(1),(2)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}\text{C}$  unless otherwise noted.

| Symbol           | Parameter                 | Value       | Unit |
|------------------|---------------------------|-------------|------|
| V <sub>CBO</sub> | Collector-Base Voltage    | 500         | V    |
| V <sub>CEO</sub> | Collector-Emitter Voltage | 400         | V    |
| $V_{EBO}$        | Emitter-Base Voltage      | 6           | >    |
| I <sub>C</sub>   | Collector Current         | 300         | mA   |
| TJ               | Junction Temperature      | 150         | °C   |
| T <sub>STG</sub> | Storage Temperature Range | -55 to +150 | °C   |

#### Notes

- 1. These ratings are based on a maximum junction temperature of 150°C.
- 2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty-cycle operations.

## Thermal Characteristics(3)

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

| Symbol          | Parameter                                | Max. | Unit  |
|-----------------|--|------|-------|
| l Pn            | Power Dissipation, T <sub>C</sub> = 25°C | 2    | W     |
|                 | Derate Above 25°C                        | 16   | mW/°C |
| $R_{\theta JA}$ | Thermal Resistance, Junction-to-Ambient  | 62.5 | °C/W  |

#### Note:

3. Device is mounted on FR-4 PCB 36 mm × 18 mm × 1.5 mm; mounting pad for the collector lead minimum 6 cm<sup>2</sup>.

## **Electrical Characteristics**(4)

Values are at  $T_A = 25$ °C unless otherwise noted.

| Symbol                | Parameter                            | Conditions   | Min. | Тур. | Max. | Unit |
|-----------------------|--------------------------------------|--|------|------|------|------|
| BV <sub>CBO</sub>     | Collector-Base Breakdown Voltage     | $I_C = 100 \mu A, I_E = 0$                                 | 500  |      |      | V    |
| BV <sub>CEO</sub>     | Collector-Emitter Breakdown Voltage  | $I_C = 1 \text{ mA}, I_B = 0$                              | 400  |      |      | V    |
| BV <sub>EBO</sub>     | Emitter-Base Breakdown Voltage       | $I_E = 100 \mu A, I_C = 0$                                 | 6    |      |      | V    |
| I <sub>CBO</sub>      | Collector-Base Cut-Off Current       | V <sub>CB</sub> = 400 V, I <sub>E</sub> = 0                |      |      | 100  | nA   |
| I <sub>CES</sub>      | Collector-Emitter Cut-Off Current    | V <sub>CE</sub> = 400 V, V <sub>BE</sub> = 0               |      |      | 500  | nA   |
| I <sub>EBO</sub>      | Emitter-Base Cut-Off Current         | $V_{EB} = 4 \text{ V}, I_{C} = 0$                          |      |      | 100  | nA   |
|                       | DC Current Gain                      | $V_{CE} = 10 \text{ V, } I_{C} = 1 \text{ mA}$             | 40   |      |      |      |
| h <sub>FE</sub>       |                                      | $V_{CE} = 10 \text{ V, } I_{C} = 10 \text{ mA}$            | 50   |      | 200  |      |
|                       |                                      | $V_{CE} = 10 \text{ V, I}_{C} = 50 \text{ mA}$             | 45   |      |      |      |
|                       |                                      | $V_{CE} = 10 \text{ V, I}_{C} = 100 \text{ mA}$            | 40   |      |      |      |
| V <sub>CE</sub> (sat) |                                      | $I_C = 1 \text{ mA}, I_B = 0.1 \text{ mA}$                 |      |      | 0.40 | V    |
|                       | Collector-Emitter Saturation Voltage | I <sub>C</sub> = 10 mA, I <sub>B</sub> = 1 mA              |      |      | 0.50 |      |
|                       |                                      | I <sub>C</sub> = 50 mA, I <sub>B</sub> = 5 mA              |      |      | 0.75 |      |
| V <sub>BE</sub> (sat) | Base-Emitter Saturation Voltage      | I <sub>C</sub> = 10 mA, I <sub>B</sub> = 1 mA              |      |      | 0.75 | V    |
| C <sub>obo</sub>      | Output Capacitance                   | V <sub>CB</sub> = 20 V, I <sub>E</sub> = 0,<br>f = 1.0 MHz |      |      | 7    | pF   |

#### Note:

4. Pulse test: pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2.0%

## **Typical Performance Characteristics**

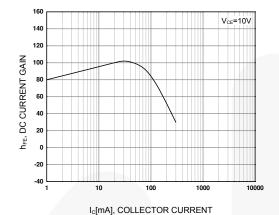


Figure 1. DC Current Gain

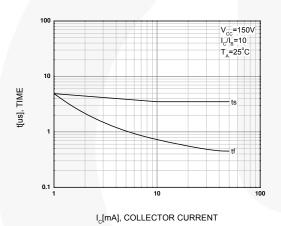


Figure 3. Turn-Off Switching Times

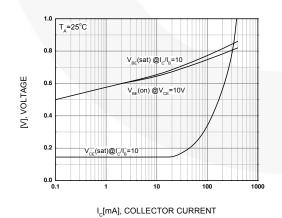


Figure 5. On Voltage

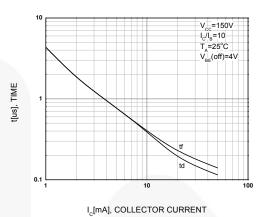


Figure 2. Turn-On Switching Times

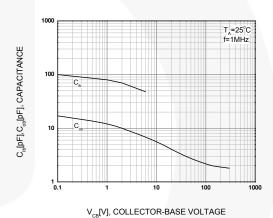
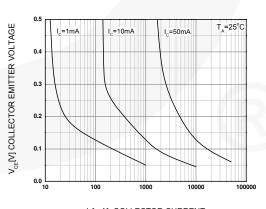


Figure 4. Capacitance



 $I_{\rm c}$ [mA], COLLECTOR CURRENT

Figure 6. Collector Saturation Region

## Typical Performance Characteristics (Continued)

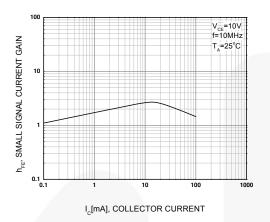
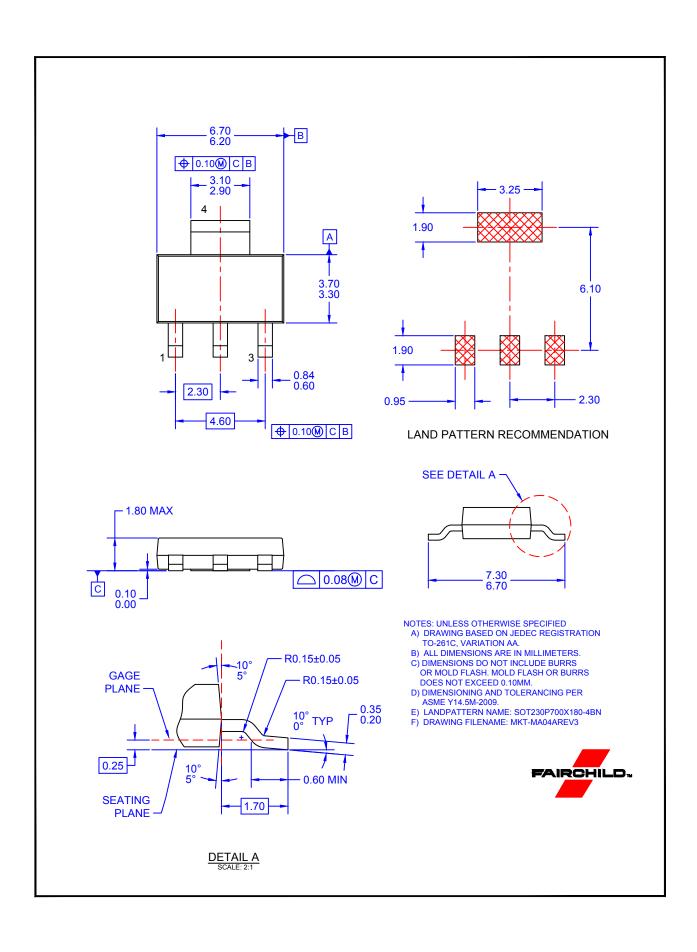


Figure 7. High Frequency Current Gain



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