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November 2013

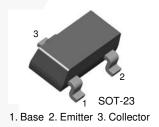
# FJV4104R PNP Epitaxial Silicon Transistor with Bias Resistor

#### **Features**

- · 100 mA Output Current Capability
- Built-in Bias Resistor ( $R_1 = 47 \text{ k}\Omega$ ,  $R_2 = 47 \text{ k}\Omega$ )

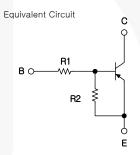
## **Application**

- · Switching, Interface, and Driver Circuits
- Inverters
- · Digital Applications in Industrial Segments



# **Description**

Transistors with built-in resistors can be excellent space- and cost-saving solutions by reducing component count and simplifying circuit design.



# **Ordering Information**

Part Number	Top Mark	Package	Packing Method
FJV4104RMTF	R74	SOT-23 3L	Tape and Reel

## **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}\text{C}$  unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>CBO</sub>	Collector-Base Voltage	-50	V
V <sub>CEO</sub>	Collector-Emitter Voltage	-50	V
V <sub>EBO</sub>	Emitter-Base Voltage	-10	V
I <sub>C</sub>	Collector Current	-100	mA
TJ	Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature	-55 to 150	°C

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## **Thermal Characteristics**

Values are at  $T_A = 25^{\circ}\text{C}$  unless otherwise noted.

Symbol	Parameter	Value	Unit
D_	Power Dissipation	200	mW
P <sub>D</sub>	Derate Above T <sub>A</sub> = 25°C	1.60	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	625	°C/W

#### Note:

1. FR-4 76 x 114 x 0.6T mm<sup>3</sup> (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

## **Electrical Characteristics**

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	$I_C = -10  \mu A, I_E = 0$	-50			V
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	$I_C = -100  \mu A, I_B = 0$	-50			V
I <sub>CBO</sub>	Collector Cut-Off Current	$V_{CB} = -40 \text{ V}, I_{E} = 0$			-0.1	μΑ
h <sub>FE</sub>	DC Current Gain	$V_{CE} = -5 \text{ V}, I_{C} = -5 \text{ mA}$	68			
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	$I_C = -10 \text{ mA}, I_B = -0.5 \text{ mA}$			-0.3	V
f <sub>T</sub>	Current Gain Bandwidth Product	$V_{CE} = -10 \text{ V}, I_{C} = -5 \text{ mA}$	1	200		MHz
C <sub>ob</sub>	Output Capacitance	$V_{CB} = -10 \text{ V}, I_{E} = 0,$ f = 1.0 MHz		5.5		pF
V <sub>I</sub> (off)	Input-Off Voltage	$V_{CE} = -5 \text{ V}, I_{C} = -100 \mu\text{A}$			-0.5	V
V <sub>I</sub> (on)	Input-On Voltage	$V_{CE} = -0.3 \text{ V}, I_{C} = -2 \text{ mA}$	-3			V
R <sub>1</sub>	Input Resistor		32	47	62	kΩ
R <sub>1</sub> /R <sub>2</sub>	Resistor Ratio		0.9	1.0	1.1	

# **Typical Performance Characteristics**

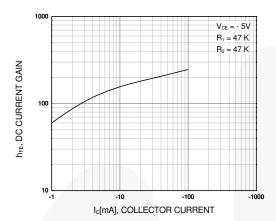


Figure 1. DC Current Gain

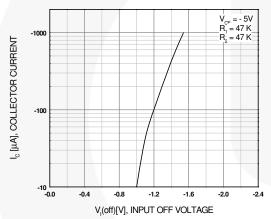


Figure 3. Input-Off Voltage

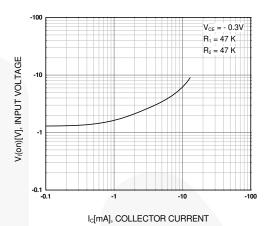


Figure 2. Input-On Voltage

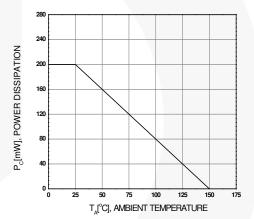


Figure 4. Power Derating

## **Physical Dimensions**

# SOT-23

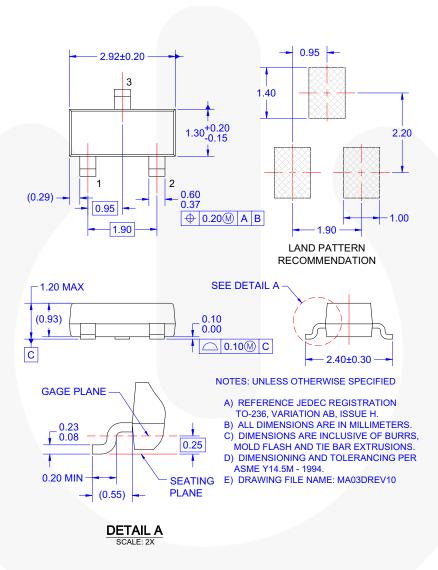


Figure 5. 3-LEAD, SOT-23, JEDEC TO-236, LOW PROFILE (ACTIVE)

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