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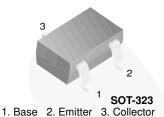
January 2014



FJX992 PNP Audio-Frequency Low-Noise Amplifier

Features

- High Voltage: V_{CEO} = -120 V
- Excellent h_{FE} Linearity
- High h_{FE}: h_{FE} = 200 ~ 700



Ordering Information

Part Number	Marking	Package	Packing Method
FJX992TF	992D	SOT-323 3L (SC70 3L)	Tape and Reel

Absolute Maximum Ratings(1),(2)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Unit
V _{CEO}	Collector-Emitter Voltage	-120	V
V _{CBO}	Collector-Base Voltage	-120	V
V _{EBO}	Emitter-Base Voltage	-5	V
Ι _C	Collector Current -100		mA
T _J , T _{STG}	Junction and Storage Temperature Range -55 to +150		

Notes:

- 1. These ratings are based on a maximum junction temperature of 150°C.
- These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty cycle operations.

Thermal Characteristics⁽³⁾

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Max.	Unit
PD	Total Device Dissipation	235	mW
	Derate Above T _A = 25°C	1.88	mW/°C
R _{θJA}	Thermal Resistance, Junction to Ambient	530	°C/W

Note:

3. PCB size: FR-4 76 x 114 x 1.57 mm3 (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

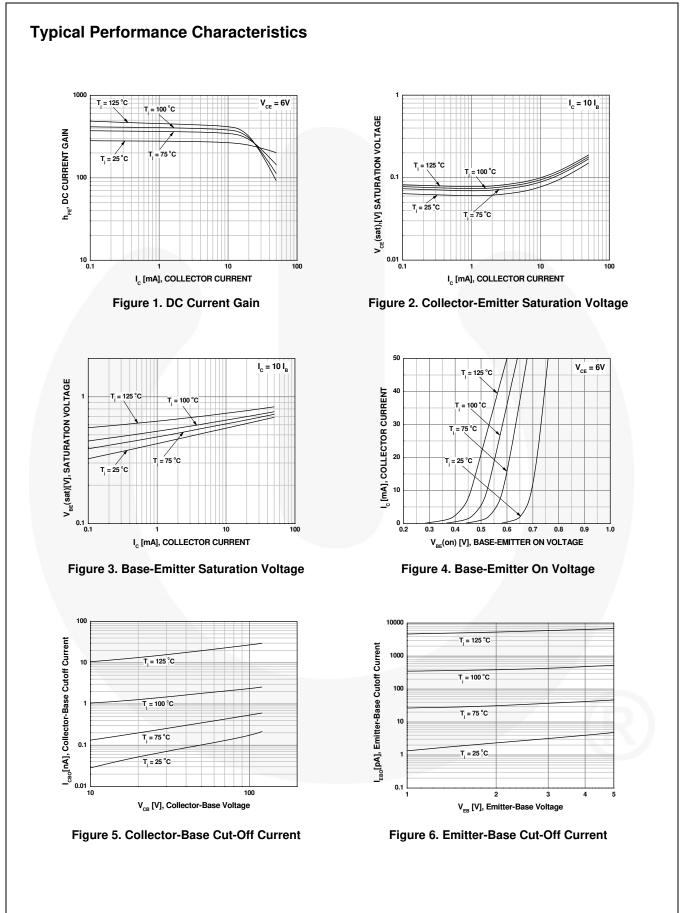
Electrical Characteristics

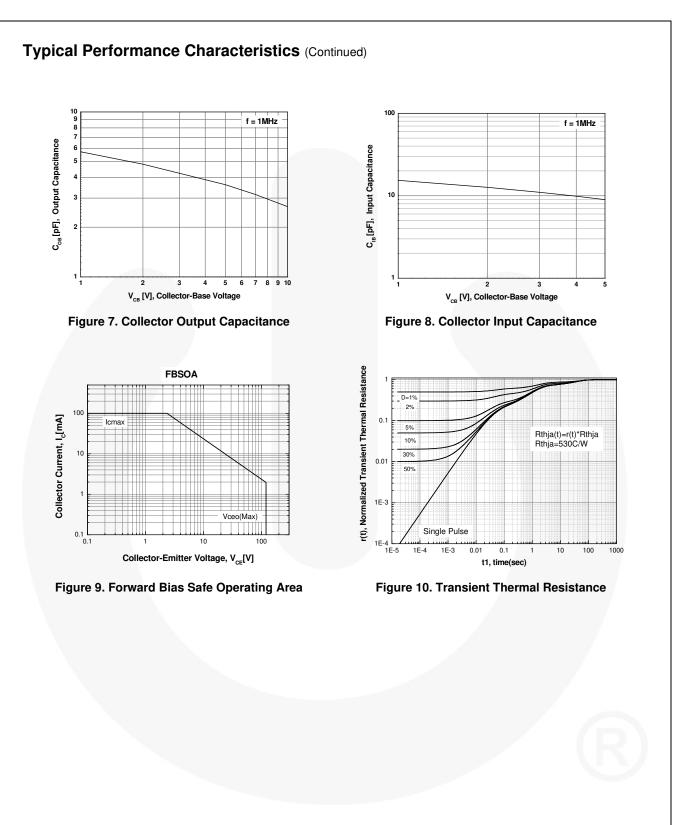
Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

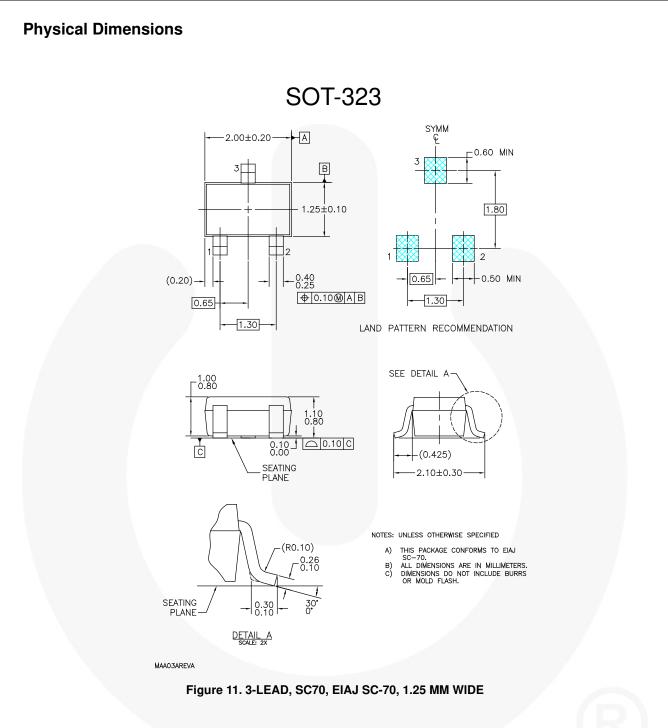
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Off Characte	eristics					
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage ⁽⁴⁾	$I_{\rm C} = -1 {\rm mA}, I_{\rm B} = 0$	-120			V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	$I_{\rm C} = -100 \ \mu {\rm A}, \ I_{\rm E} = 0$	-120			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	$I_{E} = -10 \ \mu A, \ I_{C} = 0$	-5			V
I _{CBO}	Collector-Base Cut-Off Current	V _{CB} = -120 V, I _E = 0			-100	nA
I _{EBO}	Emitter-Base Cut-Off Current	$V_{EB} = -5 V, I_{C} = 0$			-100	nA
On Characte	eristics					
h	DC Current Gain ⁽⁴⁾	$V_{CE} = -6 V, I_{C} = -0.1 mA$	150			
h _{FE}		$V_{CE} = -6 V, I_{C} = -2 mA$	200		700	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = -10 mA, I _B = -1 mA			-0.3	V
V _{BE} (on)	Base-Emitter On Voltage	$V_{CE} = -6 V, I_{C} = -1 mA$			-0.65	V
Small Signa	I Characteristics					
f _T	Current Gain - Bandwidth Product	$V_{CE} = -6 V, I_{C} = -1 mA$		100		MHz
C _{ob}	Output Capacitance	$V_{CB} = -10 \text{ V}, \text{ I}_{E} = 0,$ f = 1 MHz		4		pF

Note:

4. Pulse test: pulse width \leq 300 µs, duty cycle \leq 2%.







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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
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